



# DUAL 100V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS</sub> (ON) Max	I <sub>D</sub> Tc = +25°C
100V	$222m\Omega$ @ V <sub>GS</sub> = $10V$	10.5A
	$270 \text{m}\Omega$ @ V <sub>GS</sub> = 4.5V	9.5A

### **Features and Benefits**

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

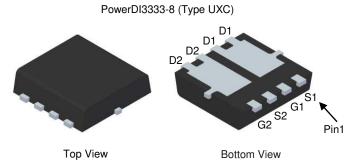
### **Description and Applications**

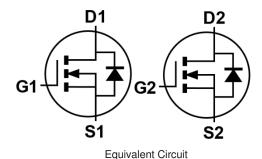
This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Load Switch

### **Mechanical Data**

- Case: PowerDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208<sup>®</sup>
- Weight: 0.072 grams (Approximate)





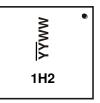
**Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN10H220LDV-7	PowerDI3333-8 (Type UXC)	2000/Tape & Reel
DMN10H220LDV-13	PowerDI3333-8 (Type UXC)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



1H2 = Product Type Marking Code

\overline{\text{YY}WW} = Date Code Marking

\overline{\text{YY}} = Last Two Digits of Year (ex: 20 for 2020)

WW = Week Code (01 to 53)



### **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	100	V	
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current (Note 6) $V_{GS} = 10V$ $T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$		lο	10.5 8	Α
Maximum Body Diode Forward Current (Note 6)	Is	10.5	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	42	Α	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	Ism	42	Α	
Avalanche Current (Note 7) L = 0.1mH		las	4.7	Α
Avalanche Energy (Note 7) L = 0.1mH		Eas	1.1	mJ

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.8	W
Thermal Resistance, Junction to Ambient (Note 5)	•	R <sub>0JA</sub>	70	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	PD	40	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	3.12	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	100	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	•				•		
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	D	_	170	222	mΩ	$V_{GS} = 10V, I_D = 2A$	
Static Drain-Source On-Resistance	RDS(ON)	_	206	270	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1A	
Diode Forward Voltage	V <sub>SD</sub>	-	0.8	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	366	_		V <sub>DS</sub> = 50V, f = 1MHz,	
Output Capacitance	Coss	_	16	_	рF		
Reverse Transfer Capacitance	Crss	_	12	_		VGS = 0V	
Gate Resistance	Rg		2.4		Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg		3.7				
Total Gate Charge (VGS = 10V)	Qg	_	6.7	_	nC	V <sub>DD</sub> = 50V, I <sub>D</sub> = 1.6A	
Gate-Source Charge	Qgs		1.3		IIC		
Gate-Drain Charge	Q <sub>gd</sub>		2.0				
Turn-On Delay Time	td(ON)		6.2				
Turn-On Rise Time	tr		8.7			$V_{DD} = 50V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	tD(OFF)	_	7.4	_	ns	$R_G = 6.8\Omega$ , $I_D = 1.0A$	
Turn-Off Fall Time	t <sub>F</sub>		4.2				
Body Diode Reverse Recovery Time	trr	_	20	_	ns	1 11 11 11 1000 1	
Body Diode Reverse Recovery Charge	Qrr	_	11	_	$\frac{1}{100}$ Is = 1.1A, dI/dt = 100A/µs		

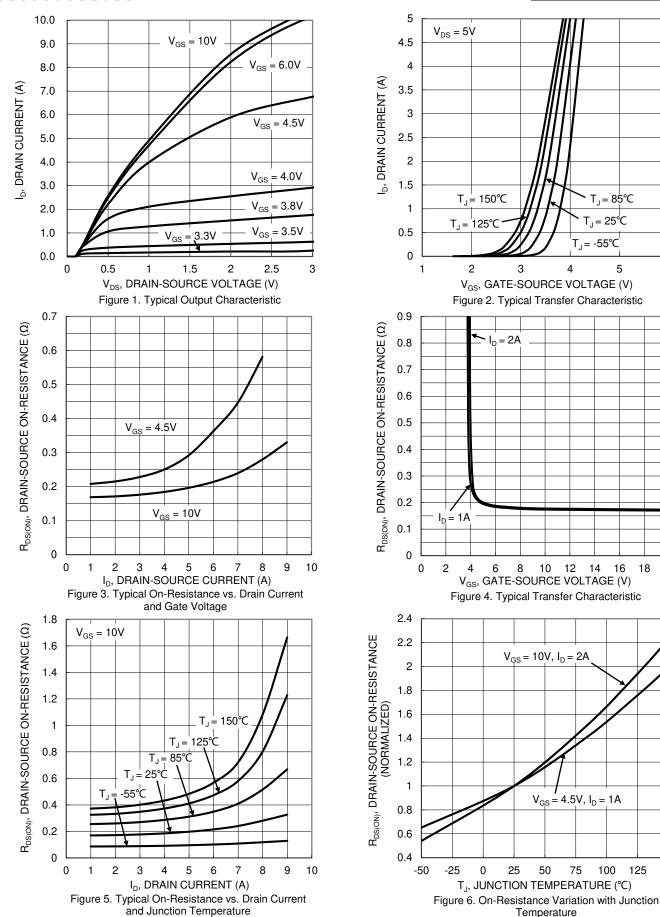
5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
6. Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

- 7.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_{J} = +25^{\circ}C$ .
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.



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125 150





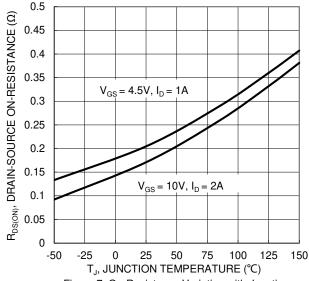


Figure 7. On-Resistance Variation with Junction Temperature

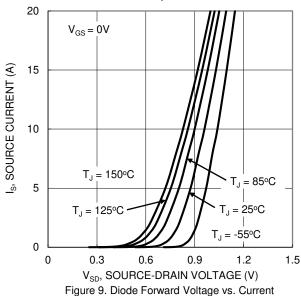


Figure 11. Gate Charge

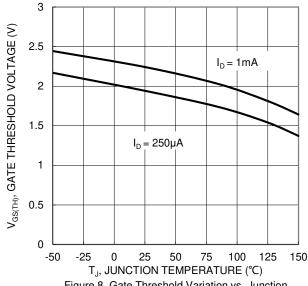
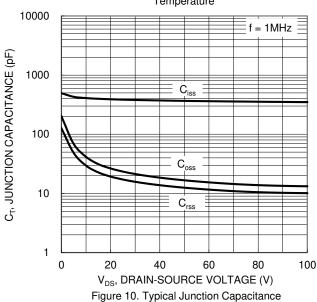


Figure 8. Gate Threshold Variation vs. Junction Temperature



1000  $\begin{array}{c} R_{\text{DS(ON)}} \\ \text{Limited} \end{array}$ 100 ID, DRAIN CURRENT (A) 10 = 100ms I<sub>J(Max)</sub> T<sub>C</sub> = 25°C  $P_W = 1s$ 0.1 Single Pulse DUT on Infinite Heatsink  $V_{GS} = 10V$ 0.01 0.1 10 100 1000 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V)



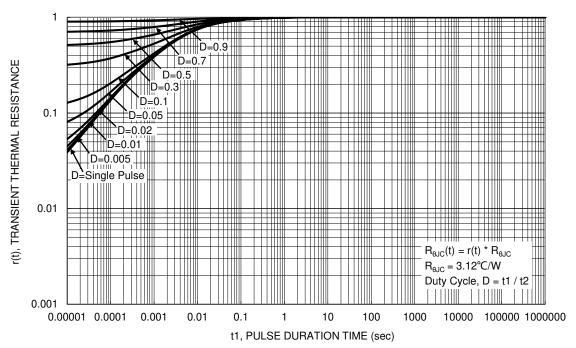


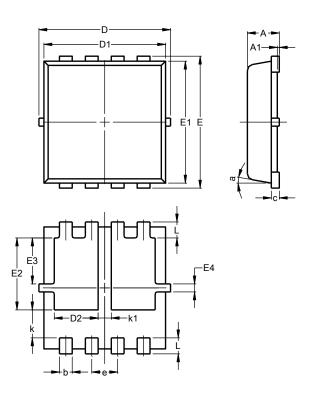
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8 (Type UXC)

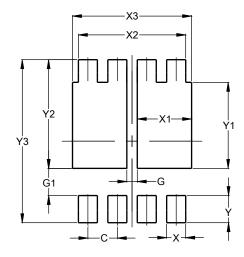


PowerDI3333-8						
(Type UXC)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
<b>A</b> 1	0.00	0.05				
۵	0.25	0.40	0.32			
C	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	0.90	1.30	1.10			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	_	-	0.65			
L	0.30	0.50	0.40			
k	0.50	0.90	0.70			
k1	0.13	0.53	0.33			
а	0°	12°	10°			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8 (Type UXC)



Dimensions	Value (in mm)		
С	0.650		
G	0.230		
G1	0.600		
X	0.420		
X1	1.200		
X2	2.370		
Х3	2.630		
Υ	0.600		
Y1	1.900		
Y2	2.400		
Y3	3.600		



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