

S101D01 Series S201D01 Series

*Zero cross type is also available. (\$101D02 Series/\$201D02 Series)

I_T(rms)≤1.2A, Non-Zero Cross type DIP 16pin Triac output SSR



■ Description

S101D01 Series and S201D01 Series Solid State Relays (SSR) are an integration of an infrared emitting diode (IRED), a Phototriac Detector and a main output Triac. These devices are ideally suited for controlling high voltage AC loads with solid state reliability while providing 4.0kV isolation (V_{iso}(rms)) from input to output.

■ Features

- 1. Output current, I_T(rms)≤1.2A
- 2. Non-zero crossing functionary
- 3. 16 pin DIP package
- 4. High repetitive peak off-state voltage

(V_{DRM}: 600V, **S201D01 Series**) (V_{DRM}: 400V, **S101D01 Series**)

5. Superior noise immunity

 $(dV/dt : MIN. 200V/\mu s, S101D01 Series)$ $(dV/dt : MIN. 100V/\mu s, S201D01 Series)$

- 6. Response time, ton: MAX. 100μs
- 7. Lead-free terminal components are also available (see Model Line-up section in this datasheet)
- 8. High isolation voltage between input and output $(V_{iso}(rms): 4.0kV)$

■ Agency approvals/Compliance

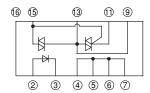
- Recognized by UL508, file No. E94758 (as model No. S101D01/S201D01)
- Approved by CSA 22.2 No.14, file No. LR63705 (as model No. S101D01/S201D01)
- 3. Package resin : UL flammability grade (94V-0)

■ Applications

- 1. Isolated interface between high voltage AC devices and lower voltage DC control circuitry.
- 2. Switching motors, fans, heaters, solenoids, and valves.
- 3. Phase or power control in applications such as lighting and temperature control equipment.



■ Internal Connection Diagram

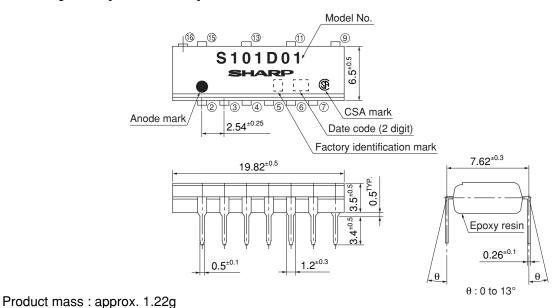


- ② Anode ① Output (T₁) ③ Cathode
 - 9, 13 Output (T₂)
 - (5) Gate ® NC

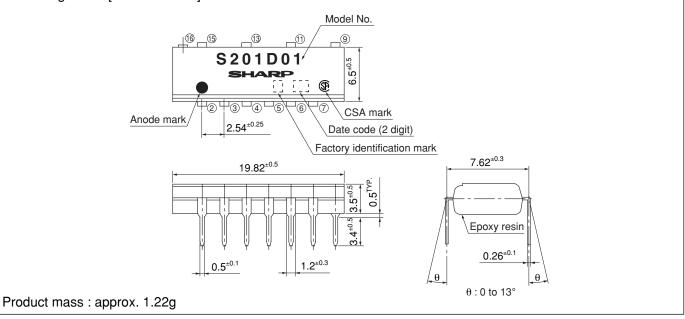
■ Outline Dimensions

(Unit: mm)

1. Through-Hole [ex. S101D01F]



2. Through-Hole [ex. S201D01F]



(Note) To radiate the heat, solder the lead pins 4 to 7,9 on the pattern of the PCB without using a socket such that there is no open pin left.



Date code (2 digit)

1st digit				2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin
no mark	Ionon
	Japan

^{*} This factory marking is for identification purpose only.

Please contact the local SHARP sales representative to see the actural status of the production.

Rank mark

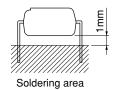
There is no rank mark indicator and currently there are no rank offered for this device.



■ Absolute Maximum Ratings

 $(T_a=25^{\circ}C)$

(18 2							
	Parameter	Symbol	Rating	Unit			
	Forward current	I _F 50 *3		mA			
Input	Reverse voltage	V_R	6	V			
	RMS ON-state current	I _T (rms) 1.2 *3		A			
044	Peak one cycle surge of	I _{surge}	I _{surge} 12 *4				
Output	Repetitive	S101D01		400	V		
	peak OFF-state voltage	S201D01	VDRM	600			
*1 Isolatio	on voltage	V _{iso} (rms)	4.0	kV			
Operati	ing temperature	Topr	-25 to +85	°C			
Storage	temperature	T _{stg}	-40 to +125	°C			
*2Solderi	ng temperature	T_{sol}	260	°C			



■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

Parameter			Symbol	Conditions	MIN.	TYP.	MAX.	Unit
T4	Forward voltage		V_{F}	$I_F=20mA$	-	1.2	1.4	V
Input	Reverse current		I_R	$V_R=3V$	1	1	10	μΑ
	Repetitive peak OFF-state current		I_{DRM}	$V_D = V_{DRM}$	1	_	100	μΑ
Output	ON-state voltage		V_{T}	$I_T=1.2A$	1	_	1.7	V
	Holding current		I_{H}	$V_D=6V$	1	_	25	mA
	Chi 1 4 C COPP 44 14	S101D01	⊣ dV/df	$V_D=1/\sqrt{2} \cdot V_{DRM}$	200	_	_	V/µs
	Critical rate of rise of OFF-state voltage	S201D01			100	_	_	ν/μς
Transfer characteristics	Minimum trigger current		Ift	$V_D=6V$, $R_L=100\Omega$	_	_	10	mA
	Isolation resistance		Riso	DC500V,40 to 60%RH	5×10 ¹⁰	1011	_	Ω
	Turn-on time		ton	$V_D=6V$, $R_L=100\Omega$, $I_F=20mA$	_	_	100	μs

^{*1 40} to 60%RH, AC for 1minute, f=60Hz

^{*2} For 10s

^{*3} Refer to Fig.1, Fig.2 *4 f=50Hz sine wave



■ Model Line-up (1) (Lead-free terminal components)

Lead Form	Through-Hole	***	I _{FT} [mA]		
Chimmin a Doolsooo	Sleeve	V_{DRM}	$(V_D=6V,$		
Shipping Package	25pcs/sleeve	[V]	$R_L=100\Omega$)		
Model No.	S101D01F	400	MAX.10		
	S201D01F	600	MAX.10		

■ Model Line-up (2) (Lead solder plating components)

Lead Form	Through-Hole	***	I _{FT} [mA]		
Chimmin a Da alva aa	Sleeve	V_{DRM}	$(V_D=6V,$		
Shipping Package	25pcs/sleeve	[V]	$R_L=100\Omega$)		
Model No.	S101D01	400	MAX.10		
Model No.	S201D01	600	MAX.10		

Please contact a local SHARP sales representative to see the actual status of the production.



Fig.1 Forward Current vs. Ambient Temperature

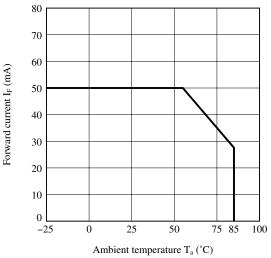


Fig.3 Forward Current vs. Forward Voltage

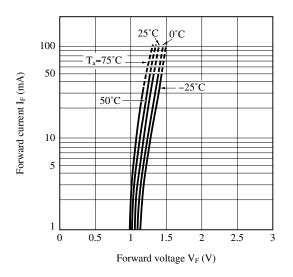


Fig.4-b Minimum Trigger Current vs.

Ambient Temperature (S201D01)

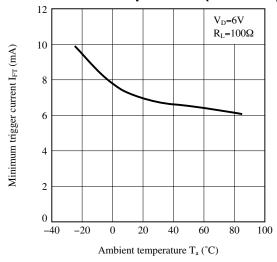


Fig.2 RMS ON-state Current vs. Ambient Temperature

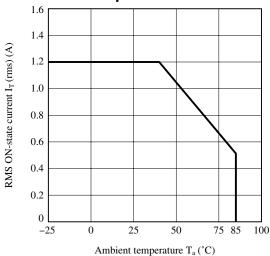


Fig.4-a Minimum Trigger Current vs.
Ambient Temperature (S101D01)

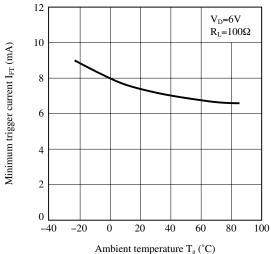


Fig.5-a ON-state Voltage vs.
Ambient Temperature (S101D01)

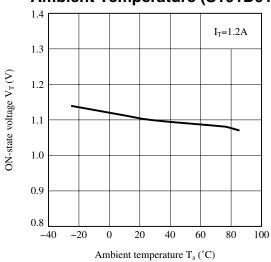




Fig.5-b ON-state Voltage vs.
Ambient Temperature (S201D01)

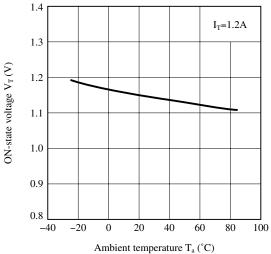


Fig.6-b Relative Holding Current vs.
Ambient Temperature (S201D01)

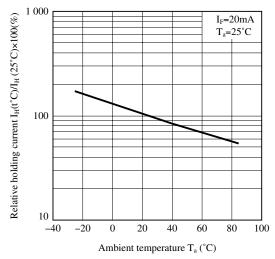


Fig.7-b ON-state Current vs.
ON-state Voltage (S201D01)

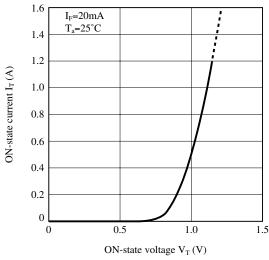


Fig.6-a Relative Holding Current vs.
Ambient Temperature (S101D01)

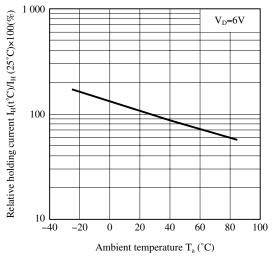


Fig.7-a ON-state Current vs.
ON-state Voltage (S101D01)

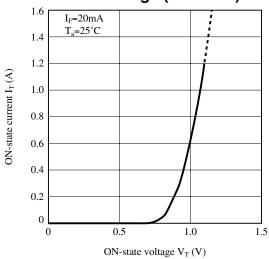


Fig.8-a Turn-on Time vs. Forward Current (S101D01)

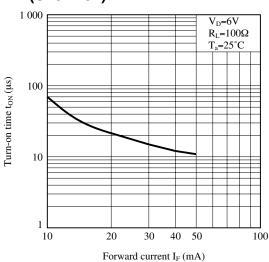
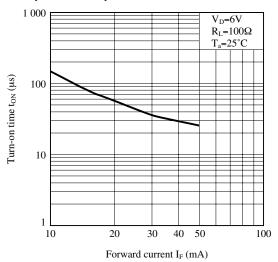




Fig.8-b Turn-on Time vs. Forward Current (S201D01)



Remarks: Please be aware that all data in the graph are just for reference.



■ Design Considerations

Recommended Operating Conditions

Parameter		Symbol	Conditions		MAX.	Unit	
Input signal current at ON state		ON state	$I_F(ON)$)N) –		25	mA
Input	Input signal current at OFF state		I _F (OFF)	-	0	0.1	mA
Output	Load supply voltage	S101D01	V _{OUT} (rms)	_	1	120	V
		S201D01				240	
	Load supply current		I _{OUT} (rms)	Locate snubber circuit between output terminals (Cs=0.022 μ F, Rs=47 Ω)	-	I _T (rms)×80%(*)	mA
	Frequency		f	-	50	60	Hz
Operating temperature		T_{opr}	-	-20	80	°C	

^(*) See Fig.2 about derating curve (I_T(rms) vs. ambient temperature).

Design guide

In order for the SSR to turn off, the triggering current (I_F) must be 0.1mA or less.

In phase control applications or where the SSR is being by a pulse signal, please ensure that the pulse width is a minimum of 1ms.

When the input current (I_F) is below 0.1mA, the output Triac will be in the open circuit mode. However, if the voltage across the Triac, V_D , increases faster than rated dV/dt, the Triac may turn on. To avoid this situation, please incorporate a snubber circuit. Due to the many different types of load that can be driven, we can merely recommend some circuit values to start with : $Cs=0.022\mu F$ and $Rs=47\Omega$. The operation of the SSR and snubber circuit should be tested and if unintentional switching occurs, please adjust the snubber circuit component values accordingly.

When making the transition from On to Off state, a snubber circuit should be used ensure that sudden drops in current are not accompanied by large instantaneous changes in voltage across the Triac.

This fast change in voltage is brought about by the phase difference between current and voltage.

Primarily, this is experienced in driving loads which are inductive such as motors and solenods.

Following the procedure outlined above should provide sufficient results.

Any snubber or Varistor used for the above mentioned scenarios should be located as close to the main output triac as possible.

All pins shall be used by soldering on the board. (Socket and others shall not be used.)

Degradation

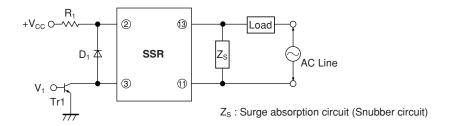
In general, the emission of the IRED used in SSR will degrade over time.

In the case where long term operation and / or constant extreme temperature fluctuations will be applied to the devices, please allow for a worst case scenario of 50% degradation over 5years.

Therefore in order to maintain proper operation, a design implementing these SSRs should provide at least twice the minimum required triggering current from initial operation.



Standard Circuit



☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

Soldering Method

Flow Soldering:

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please solder within one time.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please solder within one time.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below. Immersion time should be 3minutes or less.

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.



■ Package specification

Sleeve package Through-Hole

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

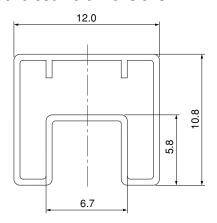
MAX. 25pcs of products shall be packaged in a sleeve.

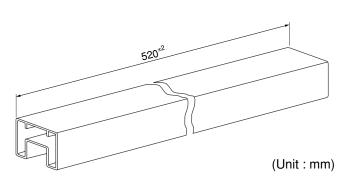
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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