# **DA121TT1G**

# **Silicon Switching Diode**

#### **Features**

 These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Max	Unit
Continuous Reverse Voltage	V <sub>R</sub>	80	V
Recurrent Peak Forward Current	ΙF	200	mA
Peak Forward Surge Current Pulse Width = 10 μs	I <sub>FM(surge)</sub>	500	mA

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	225	mW
Derated above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	555	°C/W
Total Device Dissipation, FR-4 Board (Note 2) T <sub>A</sub> = 25°C	P <sub>D</sub>	360	mW
Derated above 25°C		2.9	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	345	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 x 1.0 Inch Pad



## ON Semiconductor®

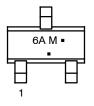
http://onsemi.com





SOT-416 / SC-75 CASE 463 STYLE 2

### **MARKING DIAGRAM**



6A = Specific Device Code

M = Date Code\*

■ = Pb-Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
DA121TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>Date Code orientation and/or orientation may vary depending upon manufacturing location.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Forward Voltage -	V <sub>F</sub>			mV
$(I_F = 1.0 \text{ mA})$		-	715	
$(I_F = 10 \text{ mA})$		-	866	
$(I_F = 50 \text{ mA})$		-	1000	
(I <sub>F</sub> = 150 mA)		-	1250	
Reverse Current -	I <sub>R</sub>			μΑ
$(V_R = 75 \text{ V})$		-	1.0	
(V <sub>R</sub> = 75 V, T <sub>J</sub> = 150°C)		-	50	
$(V_R = 25 \text{ V}, T_J = 150^{\circ}\text{C})$		-	30	
Capacitance - (V <sub>R</sub> = 0, f = 1.0 MHz)	C <sub>D</sub>	-	2.0	pF
Reverse Recovery Time - ( $I_F = I_R = 10 \text{ mA}$ , $R_L = 50 \Omega$ ) (Figure 1)	t <sub>rr</sub>	-	6.0	ns
Stored Charge - (I <sub>F</sub> = 10 mA to V <sub>R</sub> = 6.0 V, R <sub>L</sub> = 500 $\Omega$ ) (Figure 2)	QS	-	45	PC
Forward Recovery Voltage - (I <sub>F</sub> = 10 mA, t <sub>r</sub> = 20 ns) (Figure 3)	V <sub>FR</sub>	-	1.75	V

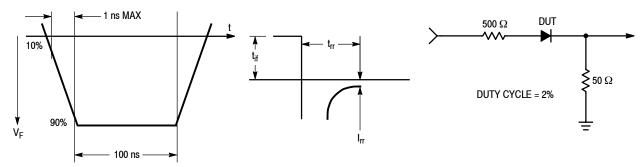


Figure 1. Reverse Recovery Time Equivalent Test Circuit

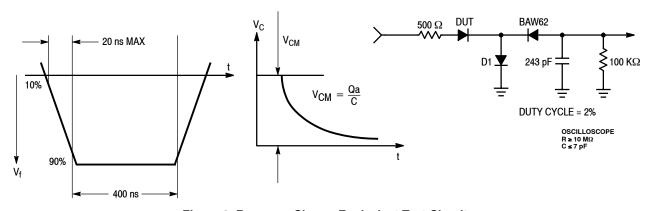


Figure 2. Recovery Charge Equivalent Test Circuit

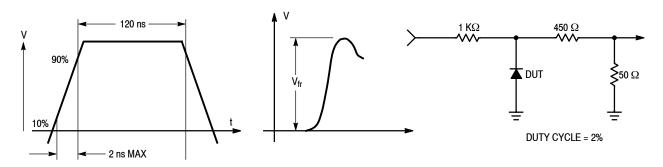
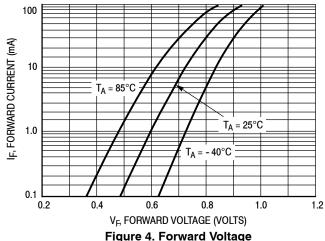


Figure 3. Forward Recovery Voltage Equivalent Test Circuit

### **DA121TT1G**



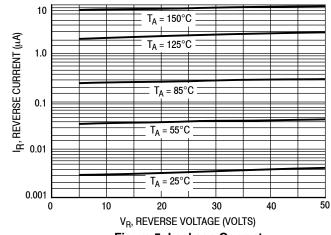


Figure 4. Forward Voltage

Figure 5. Leakage Current

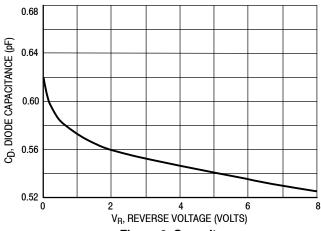


Figure 6. Capacitance

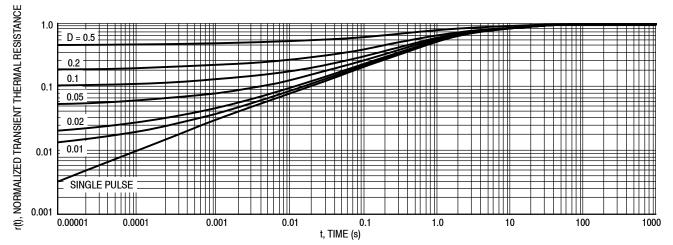


Figure 7. Normalized Thermal Response

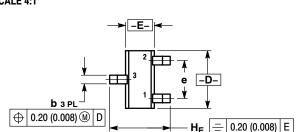
# **MECHANICAL CASE OUTLINE**

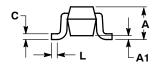




SC-75/SOT-416 CASE 463-01 ISSUE G

**DATE 07 AUG 2015** 





STYLE 1: PIN 1. BASE 2. EMITTER

3. COLLECTOR

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE

STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.061	0.063	0.065
Е	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC			.04 BS0		
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.060	0.063	0.067

### **GENERIC MARKING DIAGRAM\***



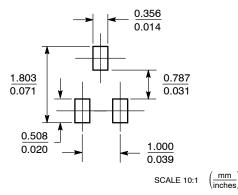
XX= Specific Device Code

Μ = Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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