

NOT RECOMMENDED FOR NEW DESIGN - NO ALTERNATE PART



DMS3016SFG

N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE PowerDI3333-8

Product Summary

BV _{DSS}	Rds(on) max	I _{D MAX} T _A = +25°C
30V	13mΩ @ V _{GS} = 10V	10.2A
300	16mΩ @ V _{GS} = 4.5V	9.3A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

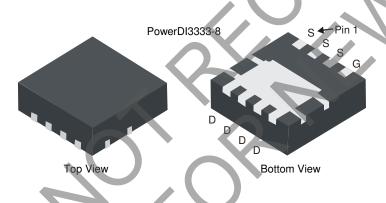
- DC-DC Converters
- Power Management Functions
- Analog Switch

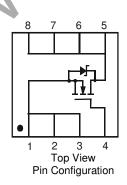
Features and Benefits

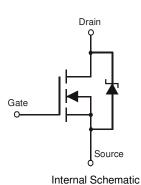
- DIOFET Utilizes a Unique Patented Process to Monolithically Integrate a MOSFET and a Schottky in a Single Die to Deliver:
 - Low R_{DS(ON)} Minimize Conduction Losses
 - Low V_{SD} Reducing the Losses due to Body Diode Conduction
 - ullet Low $Q_{rr}-$ Lower Q_{rr} of the Integrated Schottky Reduces Body Diode Switching Losses
 - Low Gate Capacitance (Q_g/Q_{gs}) Ratio Reduces Risk of Shoot-Through or Cross Conduction Currents at High Frequencies
 - Avalanche Rugged IAR and EAR Rated
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Weight: 0.072 grams (Approximate)







Ordering Information (Note 4)

Part Number	Case	Packaging
DMS3016SFG-7	PowerDI3333-8	2,000/Tape & Reel
DMS3016SFG-13	PowerDI3333-8	3,000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



S30 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 = 2018) WW = Week Code (01 to 53)

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

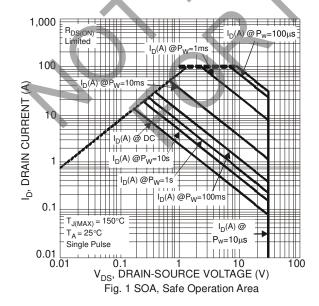
Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±12	V		
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	7.0 5.5	А
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lp	6.4 5.1	A
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l _D	10.2 8.1	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lo	9.3 7.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle=1%)			I _{DM}	80	Α
Avalanche Current (Note 7)			I _{AR}	13	Α
Repetitive Avalanche Energy (Note 7) L = 0.3mH			Ear	24	mJ

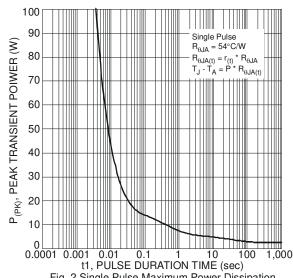
Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation	(Note 5) (Note 6)	P _D	0.98 2.08	W
Thermal Resistance, Junction to Ambient	(Note 5) (Note 6)	$R_{ hetaJA}$	127 60	°C/W
Thermal Resistance, Junction to Case	(Note 6)	$R_{ heta JC}$	3.42	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes:

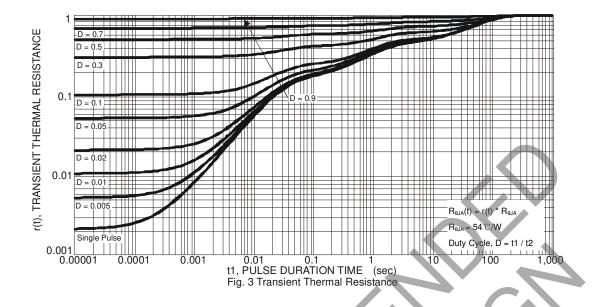
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
- 7 I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep $T_{J} = +25$ °C.







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Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	30			V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	100	μА	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		4	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(TH)}$	1.0		2.2	V	$V_{DS} = V_{GS},\ I_D = 250 \mu A$	
Static Drain-Source On-Resistance		_	10	13	mΩ	$V_{GS} = 10V, I_D = 11.2A$	
Static Drain-Source On-nesistance	R _{DS(ON)}		12	16	1117.5	$V_{GS} = 4.5V, I_D = 10.A$	
Forward Transfer Admittance	Y _{fs}		25	_	S	V _{DS} = 5V, I _D = 11.2A	
Diode Forward Voltage	V_{SD}		0.37	0.6	V	$V_{GS} = 0V, I_S = 1A$	
Maximum Body-Diode + Schottky Continuous Current	Is		_	5	Α		
DYNAMIC CHARACTERISTICS (Note 9)		2					
Input Capacitance	C _{iss}	(1886	_		V _{DS} = 15V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	372	_	pF		
Reverse Transfer Capacitance	Crss		128	_			
Gate Resistance	R_g		2.0	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg		19.5	_		$V_{DS} = 15V, V_{GS} = 10V$ $I_{D} = 11.2A$	
Total Gate Charge (V _{GS} = 10V)	Q_g		44.6	_	nC		
Gate-Source Charge	Qgs	_	4.8	_	IIC		
Gate-Drain Charge	Q _{gd}	_	4.6	_			
Turn-On Delay Time	t _{D(on)}	_	5.8	_			
Turn-On Rise Time	t _r	_	23.7	_	no	$V_{GS} = 10V, V_{DD} = 15V, R_g = 3\Omega,$	
Turn-Off Delay Time	t _{D(off)}		35.4	_	ns	$R_L = 1.2\Omega$	
Turn-Off Fall Time	t _f	_	7.7	_			

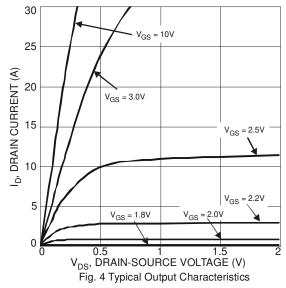
Notes:

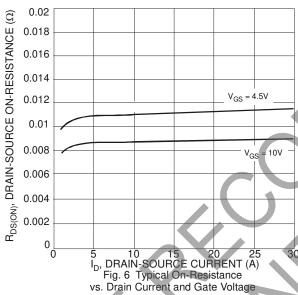
- 8 .Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.

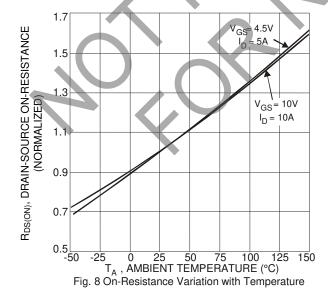


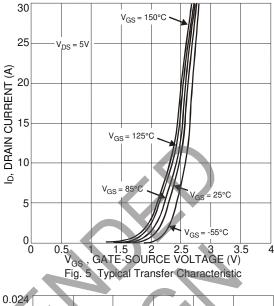
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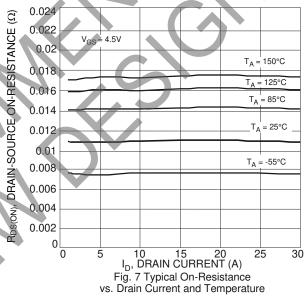
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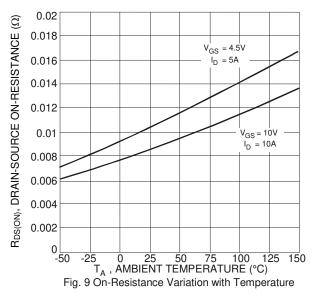








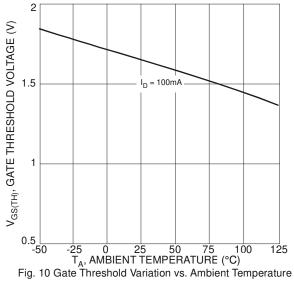


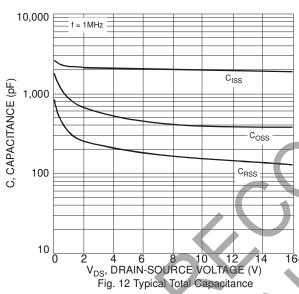


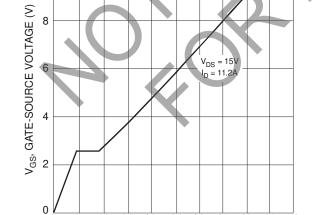


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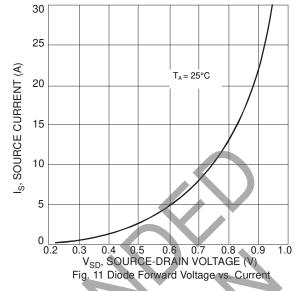
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10 15 20 25 30 35 40 Q_g, TOTAL GATE CHARGE (nC) Fig. 14 Gate-Charge Characteristics



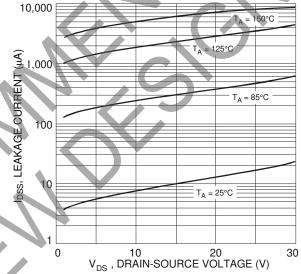


Fig. 13 Typical Drain-Source Leakage Current vs. Voltage

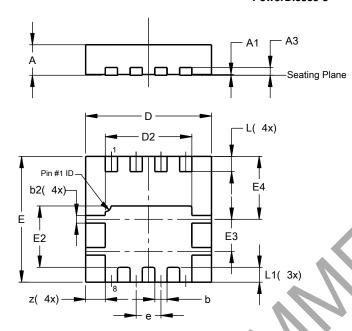
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3		7	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
٥	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
E	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
e		P	0.65		
L	0.35	0.45	0.40		
Ľ		_	0.39		
Z		_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

Dimensions	Value (in mm)		
С	0.650		
X	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Υ	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



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