

#### ● Absolute Maximum Ratings (T<sub>J</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V <sub>KA</sub>	Cathode Voltage	42	V
I <sub>K</sub>	Cathode Current Range (Continuous)	-100 ~ 150	mA
I <sub>REF</sub>	Reference Input Current Range	-0.05 ~ +10	mA
P <sub>D</sub>	Power Dissipation at 25°C: SOT-23 Package TO-92 Package	0.2 0.6	w
TJ	Junction Temperature Range	0 ~ 150	°C
T <sub>OPER</sub>	Operating Temperature Range	-60 ~ +125	°C
T <sub>STG</sub>	Storage Temperature Range	-65 ~ +150	°C

#### Recommended Operating Conditions

Symbol	Parameter	Min.	Тур.	Max.	Unit
V <sub>KA</sub>	Cathode Voltage	V <sub>REF</sub>	-	40	V
I <sub>K</sub>	Cathode Current	0.5		100	mA



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
$V_{REF}$	Reference Input Voltage	Vκa = Vref, Ικ = 10mA	2.483	2.495	2.507	V
$V_{REF(dev)}$	Deviation of Reference Input Voltage Over Full Temperature Range	$Tmin \leq Ta \leq Tmax$	-	3	17	mV
VREF Vка	Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	∆Vка=10V-Vref ∆Vка = 40V - 10V	-	-1.4 -1.0	-2.7 -2.0	mV/V
I <sub>REF</sub>	Reference Input Current	R1 = 10KΩ, R2 = ∞	-	1.8	4	uA
I <sub>REF(dev)</sub>	Deviation of Reference Input Current Over Full Temperature Range	R1 = 10KΩ, R2 = ∞	-	0.4	1.2	uA
I <sub>K(min)</sub>	Minimum Cathode Current for Regulation		-	-	0.5	mA
I <sub>K(off)</sub>	Off-State Cathode Current	Vka = 40V, Iref = 0	-	0.17	0.9	uA
Z <sub>KA</sub>	Dynamic Impedance	Iκ = 1mA to 100 mA , f≦1.0KHz	-	0.27	0.5	Ω

#### **Electrical Characteristics** ( $T_a = 25^{\circ}C$ , VKA = VREF, IK = 10mA unless otherwise specified)

#### Test Circuits

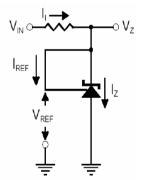


Fig1. Test Circuit for Vz=VREF

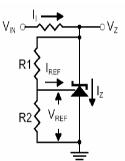


Fig2. Test Circuit for  $V_Z > V_{REF}$ Note:  $V_Z = V_{REF}(1 + R1/R2) + I_{REF}xR1$ 

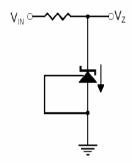
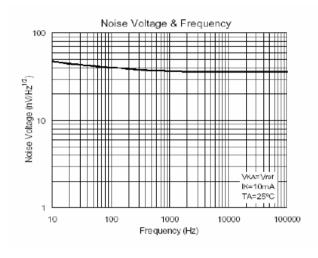


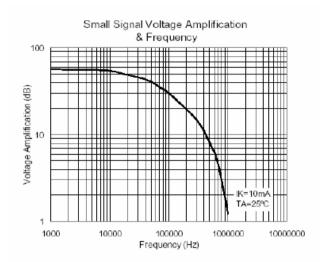
Fig3. Test Circuit for Off-State Current

#### Electrical characteristic curves Change in Reference Input Voltage & Cathode Voltage Cathode Current & Cathode Voltage 150 D 125 -5 Change in Reference Input Voltage 100 -10 Cathode Current (mA) 75 -15 50 25 20 -20 D -25 -25 -30 -50 |к=10m VKA=Vref -35 -75 TA=25°C TA=25°C -100 -40 0 1 Cathode Voltage (V) -3 -2 2 з 4 -1 Ū 50 10 20 30 40 Cathode Voltage (V) Dynamic Impedance & Frequence Cathode Current & Cathode Voltage 1000 100 800 Cathode Current (uA) 0 000 0 000 0 000 600 10 Impedance (ohm) 1 0 VKA=Vref TA=25°C -200 K=10mA to 100mA TA=25°C шШ 0.1 -400 -2 1 10 100 Frequency (Hz) 1000 10000 -1 0 1 Cathode Voltage (V) 2 З

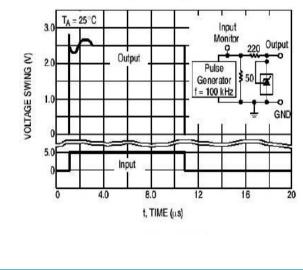


### Electrical characteristic curves

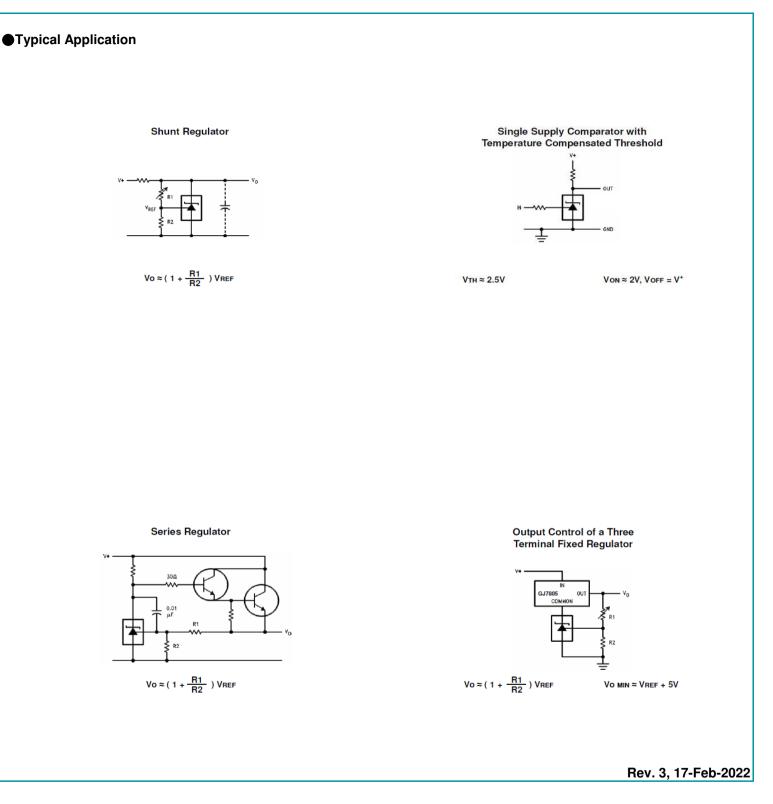




#### Pulse Response

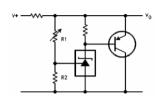


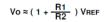




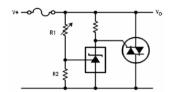
#### Typical Application

Higher Current Shunt Regulator



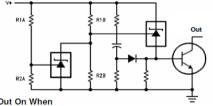






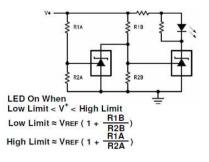
VLimit  $\approx$  (1 +  $\frac{R1}{R2}$ ) VREF

Over Voltage/under Voltage Protection Circuit



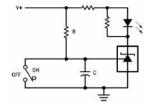
Out On When Low Limit < V<sup>+</sup> < High Limit Low Limit  $\approx$  VREF (1 +  $\frac{R1B}{R2B}$ ) + VBE High Limit  $\approx$  VREF (1 +  $\frac{R1A}{R2A}$ )

Voltage Monitor



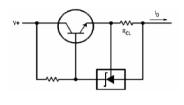
### Typical Application

Delay Timer



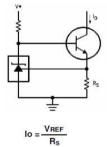
 $Delay = \mathbf{R} \cdot \mathbf{C} \cdot \boldsymbol{\ell} \mathbf{n} \frac{\mathbf{V}^*}{(\mathbf{V}^*) \cdot \mathbf{V}_{\mathsf{REF}}}$ 

Current Limiter or Current Source



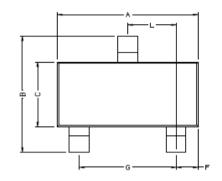
 $IO = \frac{VREF}{R_{CL}}$ 

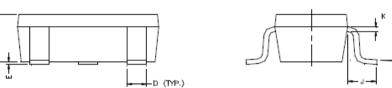
Constant Current Sink



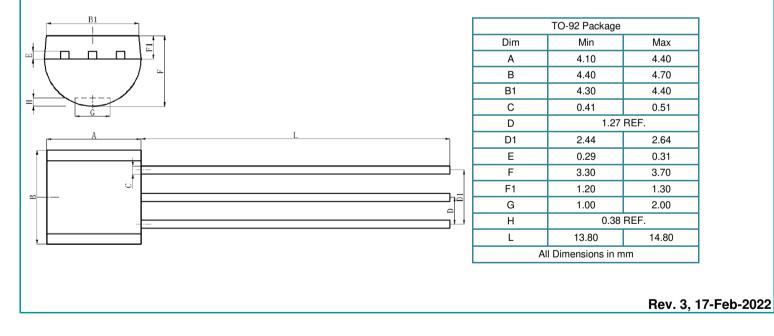


#### Package Dimensions





SOT-23 Package			
Dim	Min	Max	
А	2.70	3.10	
В	2.40	2.80	
С	1.40	1.60	
D	0.35	0.50	
E	0.00	0.10	
F	0.45	0.55	
G	1.90 REF.		
Н	1.00	1.30	
K	0.10	0.20	
J	0.40	-	
L	0.85	1.15	
М	0 °	10 °	
All Dimensions in mm			



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