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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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EOL announced Product

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2SK1153, 2SK1154

Silicon N Channel MOS FET

REJ03G0908-0200
(Previous: ADE-208-1246)
Rev.2.00
Sep 07, 2005

Application

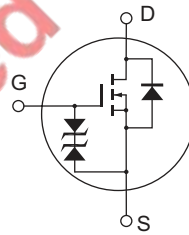
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline

RENESAS Package code: PRSS0004AC-A
(Package name: TO-220AB)



1. Gate
2. Drain
(Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	2SK1153	450	V
	2SK1154	500	
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	3	A
Drain peak current	I _{D(pulse)} * ¹	12	A
Body to drain diode reverse drain current	I _{DR}	3	A
Channel dissipation	P _{ch} * ²	30	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 ∞s, duty cycle ≤ 1%

2. Value at T_C = 25°C

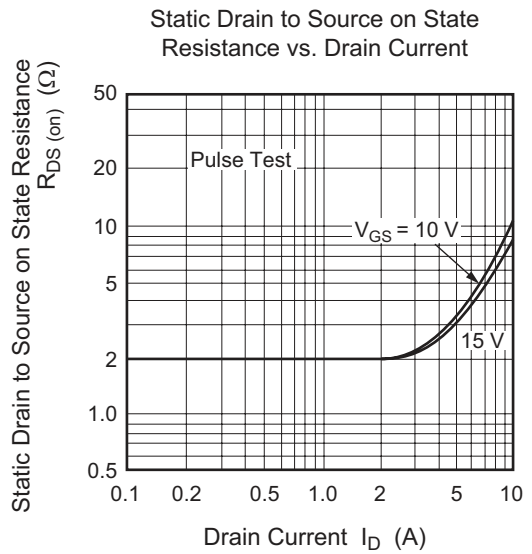
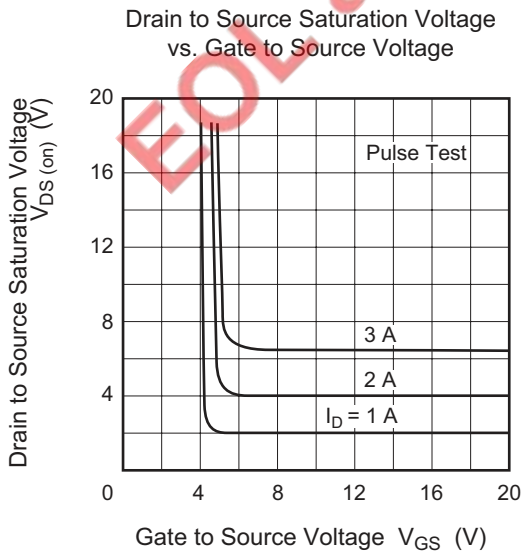
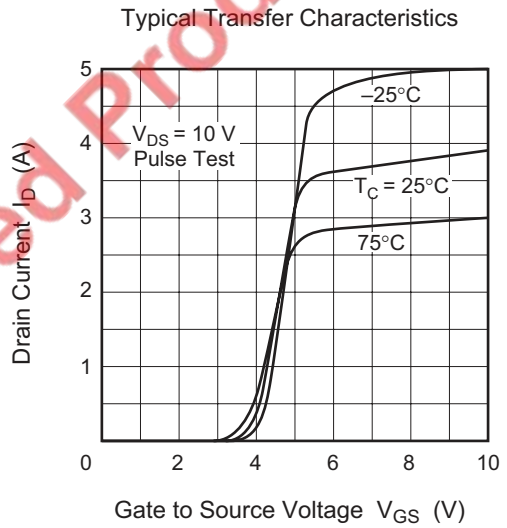
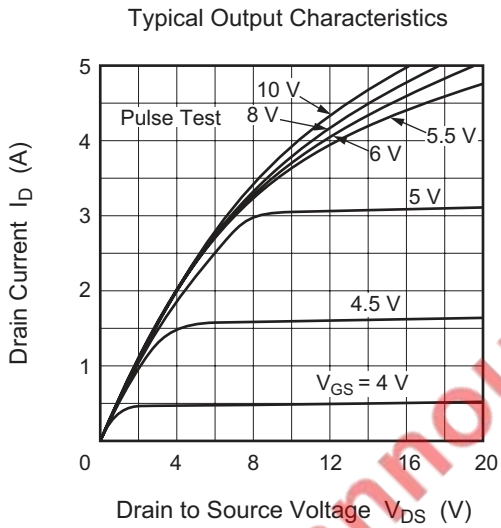
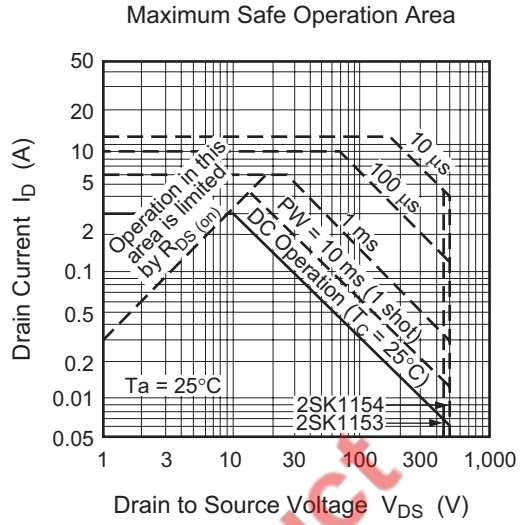
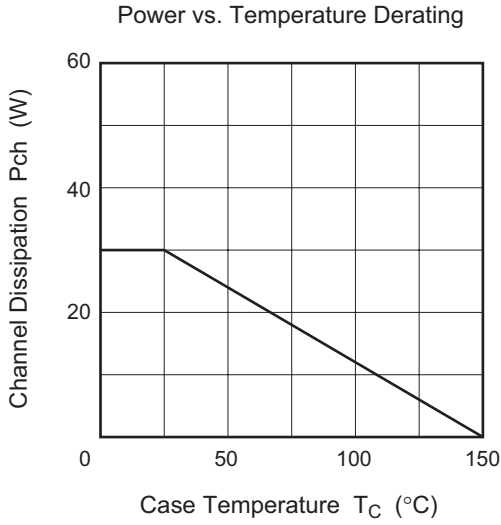
Electrical Characteristics

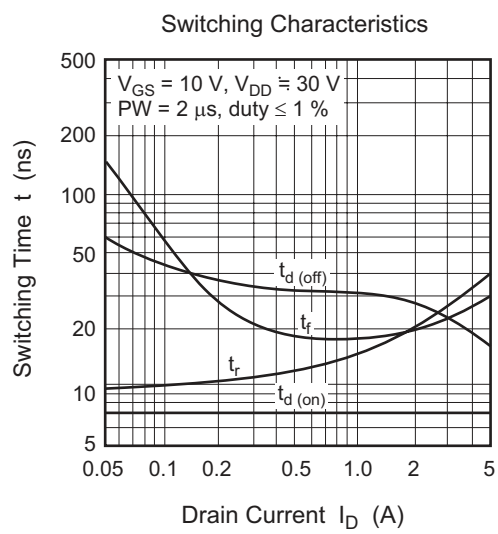
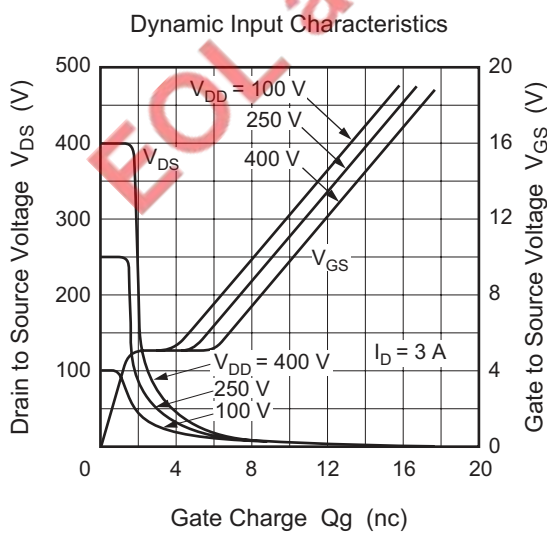
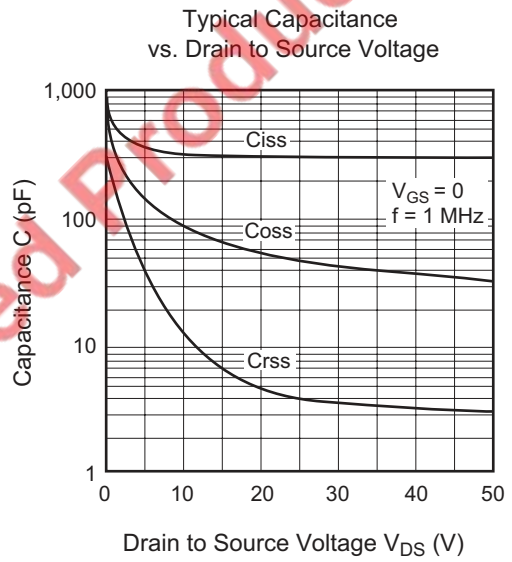
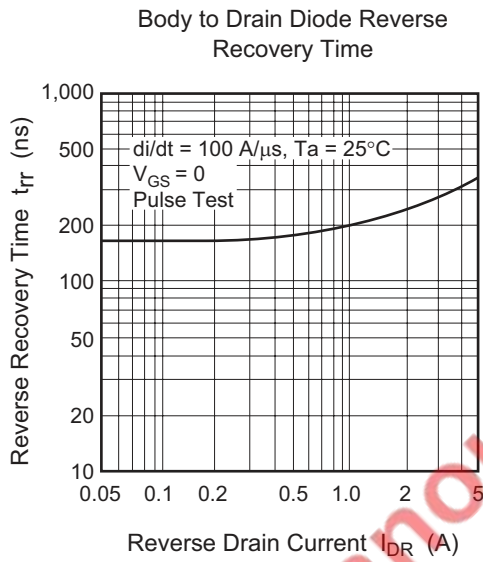
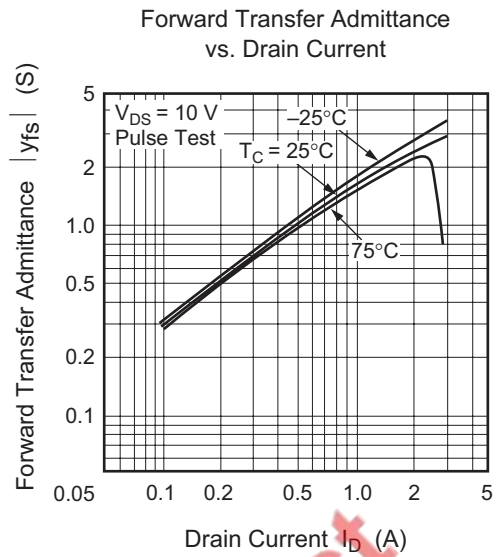
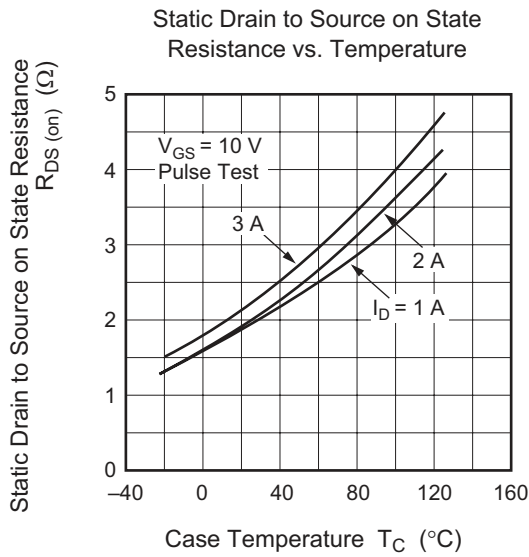
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK1153	450	—	—	V	I _D = 10 mA, V _{GS} = 0
	2SK1154	500	—	—	—	
Gate to source breakdown voltage	V _{(BR)GSS}	±30	—	—	V	I _G = ±100 ∞A, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	∞A	V _{GS} = ±25 V, V _{DS} = 0
Zero gate voltage drain current	2SK1153	—	—	250	∞A	V _{DS} = 360 V, V _{GS} = 0
	2SK1154	—	—	—	—	V _{DS} = 400 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	—	3.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	2SK1153	—	2.0	2.8	Ω	I _D = 2 A, V _{GS} = 10 V * ³
	2SK1154	—	2.2	3.0	—	
Forward transfer admittance	y _{fs}	1.5	2.5	—	S	I _D = 2 A, V _{DS} = 10 V * ³
Input capacitance	C _{iss}	—	330	—	pF	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	90	—	pF	
Reverse transfer capacitance	C _{rss}	—	15	—	pF	
Turn-on delay time	t _{d(on)}	—	7	—	ns	
Rise time	t _r	—	20	—	ns	I _D = 2 A, V _{GS} = 10 V, R _L = 15 Ω
Turn-off delay time	t _{d(off)}	—	30	—	ns	
Fall time	t _f	—	20	—	ns	
Body to drain diode forward voltage	V _{DF}	—	0.9	—	V	I _F = 3 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	300	—	ns	I _F = 3 A, V _{GS} = 0, di _F /dt = 100 A/∞s

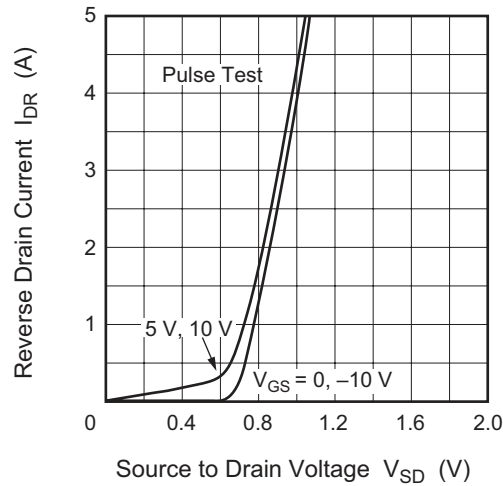
Note: 3. Pulse test

Main Characteristics



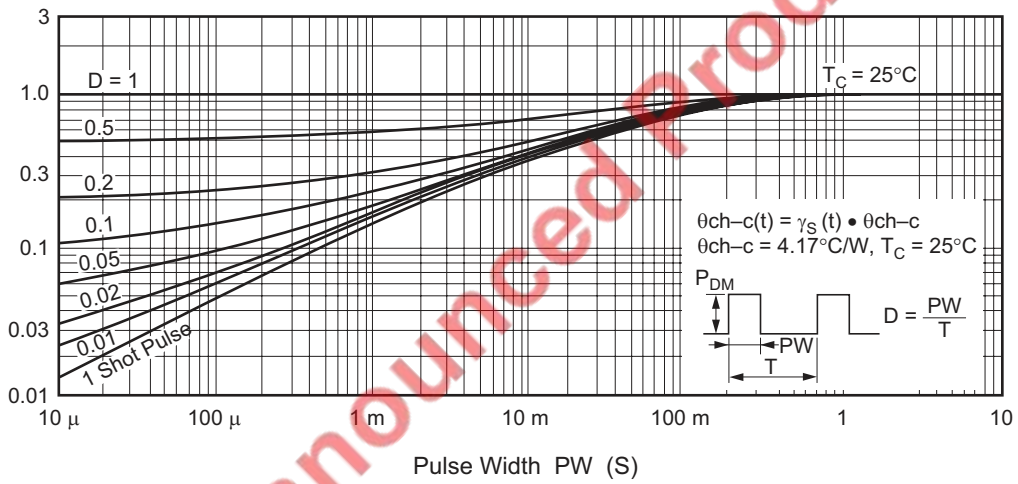


Reverse Drain Current vs. Source to Drain Voltage

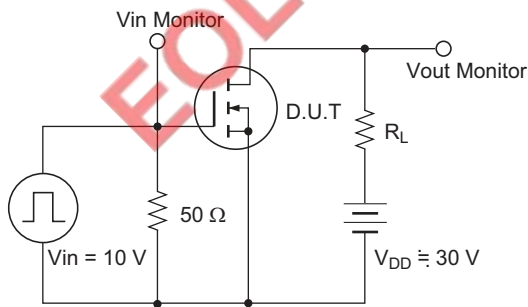


Normalized Transient Thermal Impedance $\gamma_S(t)$

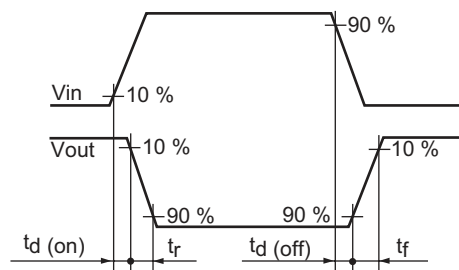
Normalized Transient Thermal Impedance vs. Pulse Width



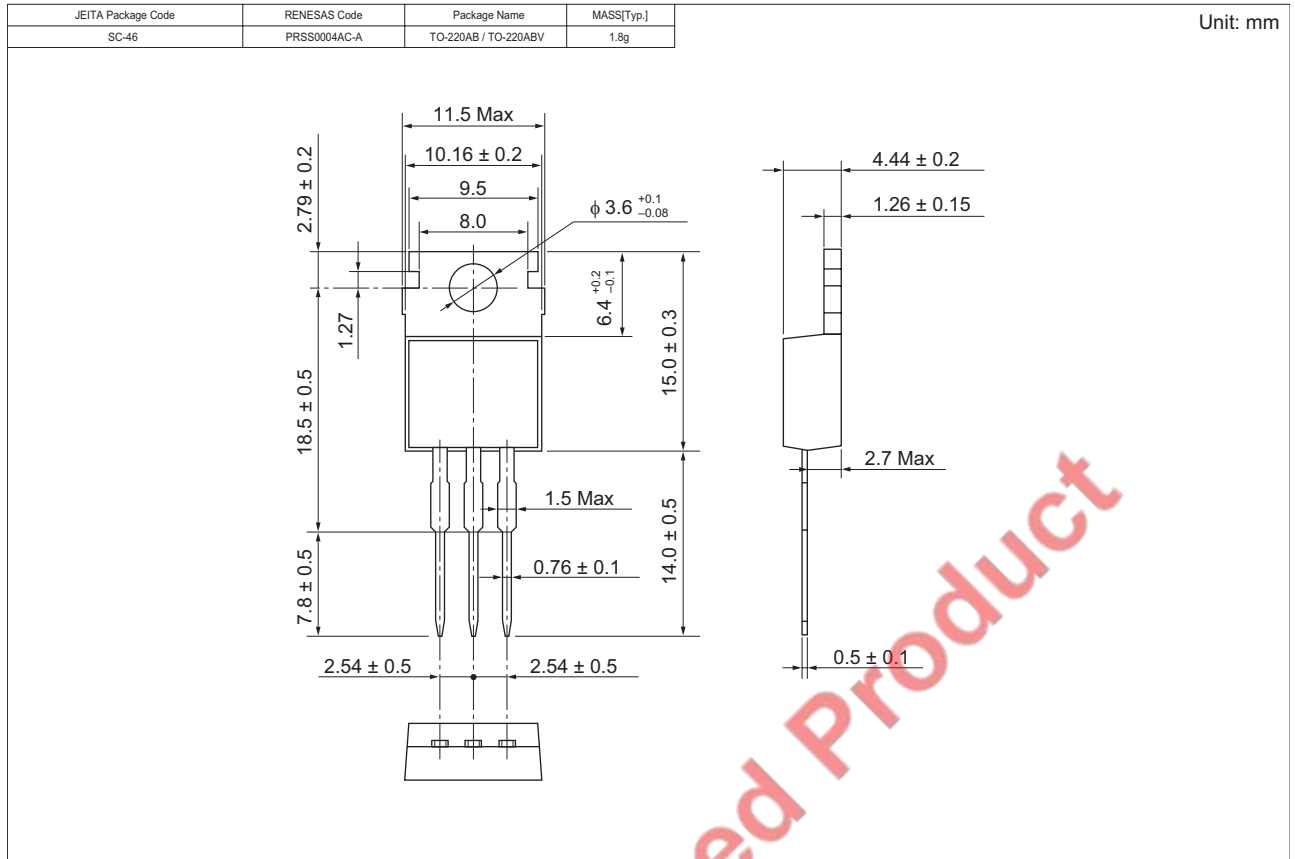
Switching Time Test Circuit



Waveforms



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK1153-E	500 pcs	Box (Sack)
2SK1154-E	500 pcs	Box (Sack)

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