

Nch 650V 7A Power MOSFET

V _{DSS}	650V
R _{DS(on)} (Max.)	0.665Ω
I _D	±7A
P_{D}	78W

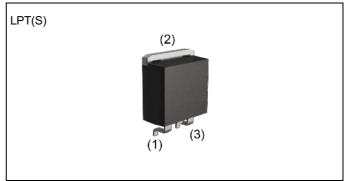
Features

- 1) Low on-resistance
- 2) Ultra fast switching speed
- 3) Parallel use is easy
- 4) Pb-free plating; RoHS compliant

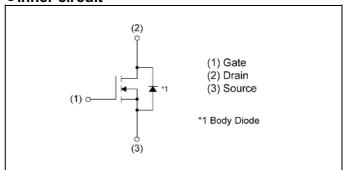
Application

Switching

Outline



•Inner circuit



Packaging specifications

Packing	Embossed Tape
Packing code	TL
Marking	R6507KNJ
Basic ordering unit (pcs)	1000

ullet Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	650	V
Continuous drain current (T _c = 25°C)		I _D *1	±7	Α
Pulsed drain current	I _{DP} *2	±21	Α	
Coto Course valters	static		±20	V
Gate - Source voltage	AC(f>1Hz)	V_{GSS}	±30	V
Avalanche current, single pulse		I _{AS}	1.3	Α
Avalanche energy, single pulse		E _{AS} *3	136	mJ
Power dissipation (T _c = 25°C)	P _D	78	W	
Junction temperature	T _j	150	°C	
Operating junction and storage tempera	ature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Downwortow	Cymah al	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *4	-	-	1.6	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

●Electrical characteristics (T_a = 25°C)

Davameter	Cymah al	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V(DD)DCC		650	-	1	V	
		V _{DS} = 650V, V _{GS} = 0V					
Zero gate voltage drain current	I _{DSS}	$T_j = 25^{\circ}C$	-	-	100	μΑ	
		$T_j = 125^{\circ}C$	ı	-	1000		
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	1	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 200 \mu A$	3	-	5	V	
		$V_{GS} = 10V, I_D = 2.4A$					
Static drain - source on - state resistance	R _{DS(on)} *6	$T_j = 25^{\circ}C$	-	0.605	0.665	Ω	
		$T_j = 125^{\circ}C$	-	-	-		
Gate resistance	R_{G}	f = 1MHz, open drain	-	3.2	-	Ω	

● Electrical characteristics (T_a = 25°C)

Davamatar	Cymah al	Conditions		Unit			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	470	-		
Output capacitance	Output capacitance C _{oss}		-	470	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	20	-		
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300V$, $V_{GS} = 10V$	-	20	-		
Rise time	t _r *6	I _D = 3.5A	-	20	-		
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 86.6\Omega$	-	35	-	ns	
Fall time	t _f *6	$R_G = 10\Omega$	-	25	-		

● Gate charge characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			l linit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*6}	V _{DD} ≈ 300V	-	14.5	-	
Gate - Source charge	Q _{gs} *6	I _D = 7A	-	4.2	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	5.8	-	
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 300V, I _D = 7A	-	6.9	-	V

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Mounted on an epoxy PCB FR4 (25mm x 27mm x 0.8mm)

^{*6} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Cymbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Source current	I _S *1			-	7	Α	
Pulsed source current	l _{SP} *2	T _C = 25°C	-	-	21	Α	
Source-Drain voltage	V _{SD} *6	$V_{GS} = 0V, I_{S} = 7A$	-	-	1.5	٧	
Reverse recovery time	t _{rr} *6		-	320	-	ns	
Reverse recovery charge	Q _{rr} *6	Q_{rr}^{*6} $I_S = 7A$ $di/dt = 100A/\mu s$	-	2.7	-	μC	
Peak reverse recovery current	_{rr} *6		-	17	-	Α	

Fig.1 Power Dissipation Derating Curve

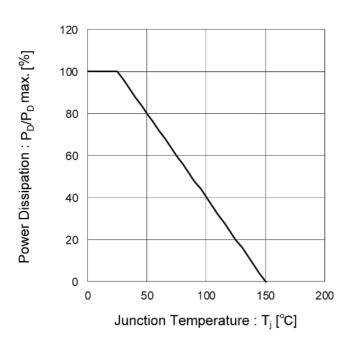


Fig.2 Drain Current Derating Curve

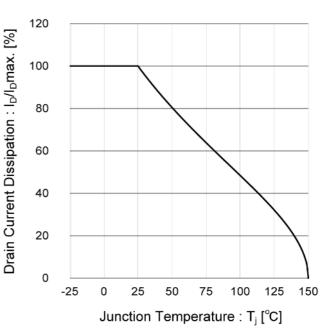


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

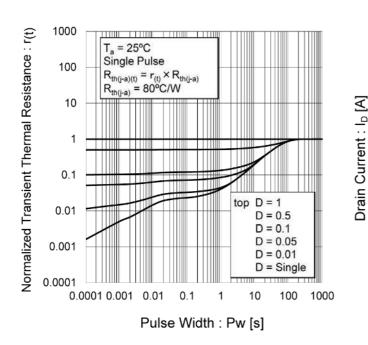


Fig.4 Maximum Safe Operating Area

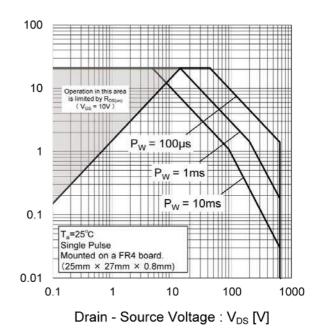


Fig.5 Avalanche Energy Derating Curve

120 Avalanche Energy: EAS / EAS max [%] 100 80 60 40 20 0 0 25 50 75 100 125 150 175 Junction Temperature : T_j [°C]

Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

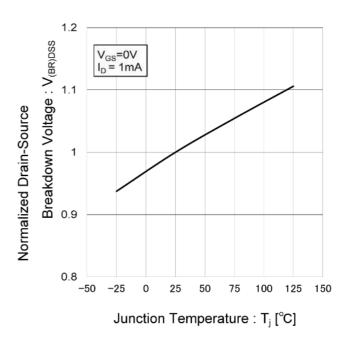


Fig.7 Typical Output Characteristics(I)

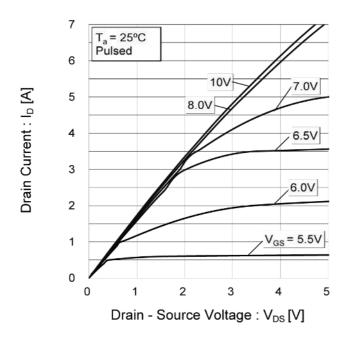
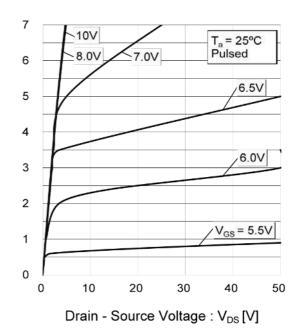


Fig.8 Typical Output Characteristics(II)



Drain Current: Ip [A]

Fig.9 Typical Transfer Characteristics

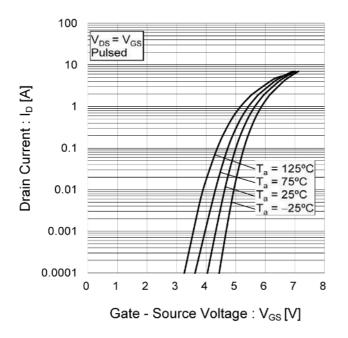


Fig.10 Normalized Gate Threshold

Voltage vs. Junction Temperature

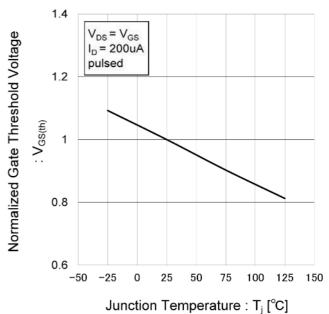


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

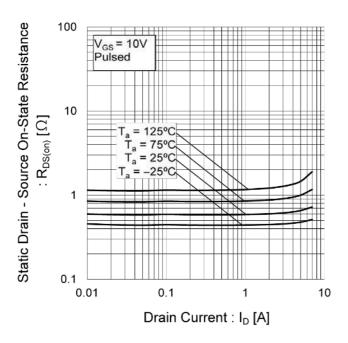
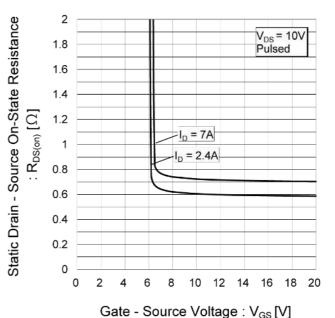


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



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Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

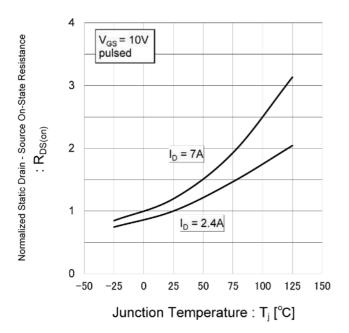


Fig.14 Typical Capacitance vs.

Drain - Source Voltage

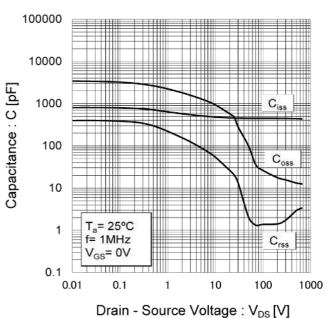


Fig.15 Switching Characteristics

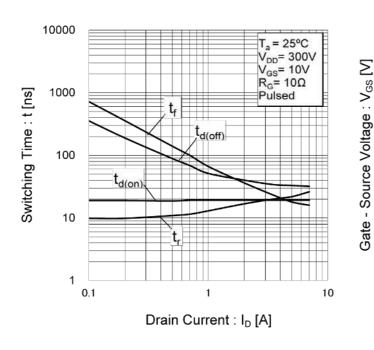


Fig.16 Typical Gate Charge

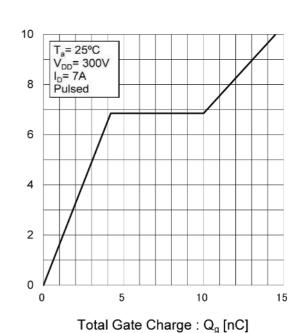
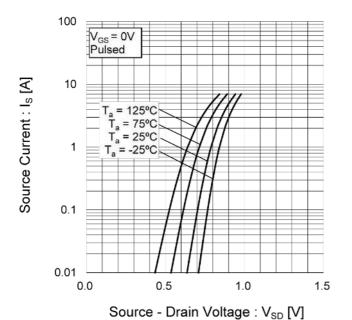


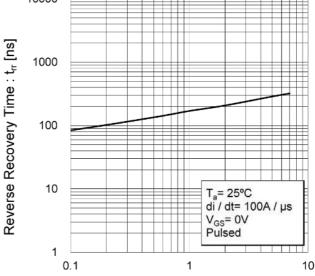
Fig.17 Source Current vs. Source - Drain Voltage



10000

Fig.18 Reverse Recovery Time vs.

Inverse Diode Forward Current



Inverse Diode Forward Current: I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

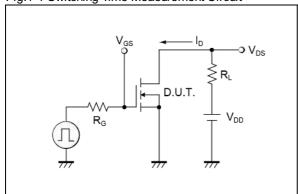


Fig.2-1 Gate Charge Measurement Circuit

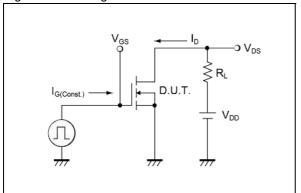


Fig.3-1 Avalanche Measurement Circuit

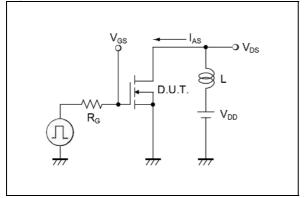


Fig.4-1 trr Measurement Circuit

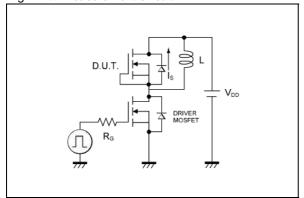


Fig.1-2 Switching Waveforms

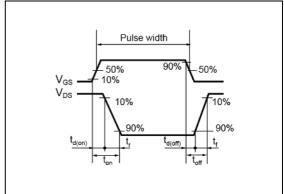


Fig.2-2 Gate Charge Waveform

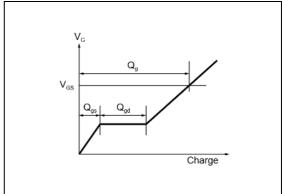


Fig.3-2 Avalanche Waveform

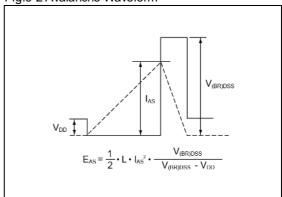
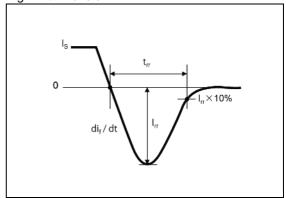
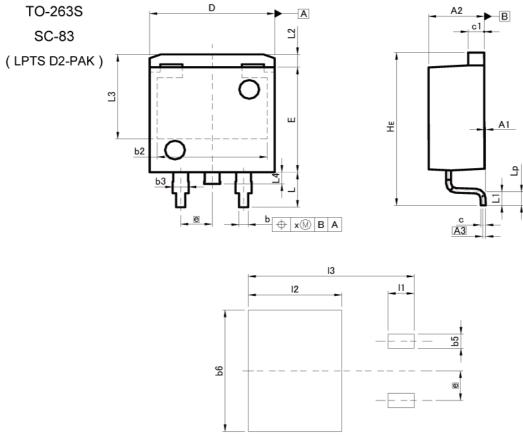


Fig.4-2 trr Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.:	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.8	90	0.3	50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.	20	0.0	47
L2	1.	10	0.0	43
L3	7.:	25	0.2	85
L4	1.0	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х		0.25	47	0.010
DIM L	MILIM	TERS	INC	HES

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
bb	=	1.23	-	0.049
b6	-0	10.40	; =	0.409
	<u>22</u> 6	2.10) 19 <u>2</u> 4	0.083
12	##X	7.55	100	0.297
13	- 33	13.40	9 =)	0.528

Dimension in mm/inches



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CLASSIV	CLASSⅢ	CLASSⅢ	CLASSIII

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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