

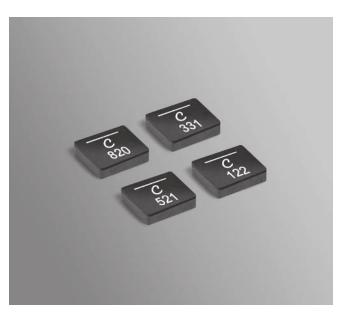






Shielded Power Inductors – XGL3512





- · Industry's lowest DCR and ultra low AC losses
- AEC-Q200 Grade 1 (-40°C to +125°C)
- Superior current handling with soft saturation characteristics
- Wide inductance range from 0.082 to 1.2 µH

Designer's Kit C502 contains 3 of each value.

Core material Composite

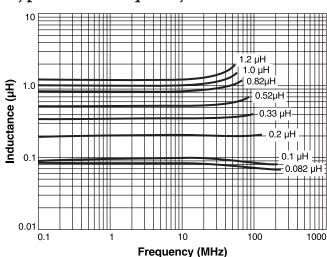
Core and winding loss See www.coilcraft.com/coreloss **Environmental** RoHS compliant, halogen free **Terminations** RoHS compliant tin-silver (96.5/3.5) over copper. Other terminations available at additional cost. **Weight:** 0.066 – 0.070 g

Operating voltage: 0 – 80 V

Ambient temperature -40°C to +125°C with (40°C rise) Irms current. Maximum part temperature +165°C (ambient + temp rise). Derating. Storage temperature Component: -55°C to +165°C. Tape and reel packaging: -55°C to +80°C

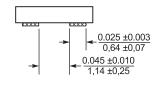
Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)

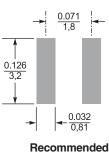
Packaging 1500/7" reel; 5000/13" reel Plastic tape: 12 mm wide, 0.23 mm thick, 8 mm pocket spacing, 1.4 mm pocket depth PCB washing Tested to MIL-STD-202 Method 215 plus an additional aqueous wash. See Doc787_PCB_Washing.pdf.



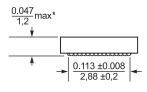
 $\begin{array}{c|c} \bullet 0.126 \pm 0.006 \\\hline 3.2 \pm 0.15 \end{array}$

Dash number Indicates direction of terminals and start (short) lead. Connect high dv/dt here for lowest EMI.





Land Pattern



*For optional tin-lead and tin-silver-copper terminations, dimensions are for the mounted part. Dimensions before mounting can be an additional 0.005 inch / 0.13 mm.

Dimensions are in $\frac{\text{inches}}{\text{mm}}$



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Typical L vs Frequency



Halogo Free AEC

Shielded Power Inductors-XGL3512

H		Inductance ² ±20% (μH)	DCR (mOhms) ³		SRF typ ⁴	Isat (A)⁵			Irms (A) ⁶	
NI	Part number ¹		typ	max	(MHz)	10% drop	20% drop	30% drop	20°C rise	40°C rise
	XGL3512-820ME_	0.082	2.8	3.3	340	7.0	11.8	17.3	15.7	21.2
	XGL3512-900ME_	0.090	3.3	3.8	330	6.8	11.3	16.4	14.4	19.4
	XGL3512-101ME_	0.10	3.5	4.0	305	6.5	10.7	15.2	13.5	18.0
n	XGL3512-201ME_	0.20	6.3	7.3	175	4.3	7.1	10.0	11.3	15.2
	XGL3512-331ME_	0.33	10.8	12.5	135	3.4	5.5	7.8	8.6	11.5
	XGL3512-401ME_	0.40	12.3	14.2	125	3.2	5.1	7.1	7.7	11.2
	XGL3512-521ME_	0.52	15.5	17.9	105	2.8	4.5	6.4	7.0	9.4
	XGL3512-621ME_	0.62	20.7	23.8	90	2.6	4.1	5.7	6.0	8.1
	XGL3512-821ME_	0.82	24.5	28.2	80	2.2	3.6	5.1	5.6	7.4
С	XGL3512-102ME_	1.0	28.0	32.2	70	1.9	3.2	4.5	4.9	6.6
	XGL3512-122ME_	1.2	32.5	37.4	60	1.7	2.8	4.1	4.4	5.9

1. When ordering, please specify termination and packaging codes:

XGL3512-122MEC

- Termination: E = RoHS compliant tin-silver over copper. Special order: T = RoHS tin-silver-copper (95.5/4/0.5) or S = non-RoHS tin-lead (63/37).
- Packaging: C = 7" machine-ready reel. EIA-481 embossed plastic tape (1500 per full reel). Quantities less than full reel available: in tape (not machine ready) or with leader and trailer (\$25 charge).
 - **D** = 13" machine-ready reel. EIA-481 embossed plastic tape (5000 per full reel).
 - Factory order only, not stocked.
- Inductance tested at 1 MHz, 0.1 Vrms, 0 Adc.
 DCR measured on a micro-ohmmeter.
- 3. DCR measured on a micro-onmmeter.
- 4. SRF measured using Agilent/HP 4395A or equivalent.
- DC current at 25°C that causes the specified inductance drop from its value without current. Click for temperature derating information.
- Current that causes the specified temperature rise from 25°C ambient. This information is for reference only and does not represent absolute maximum ratings.

Click for temperature derating information.

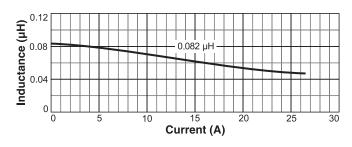
- 7. Electrical specifications at 25°C.
- Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

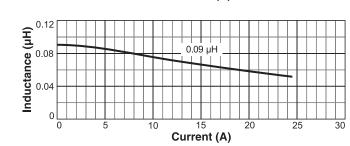
Irms Testing

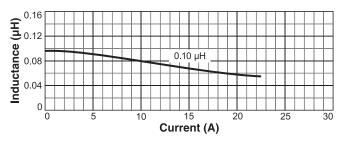
Irms testing was performed on 0.75 inch wide $\times 0.25$ inch thick copper traces in still air.

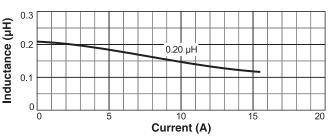
Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.

L vs Current











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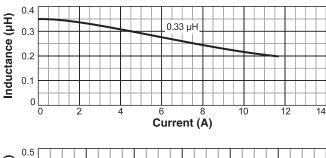
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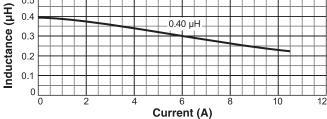
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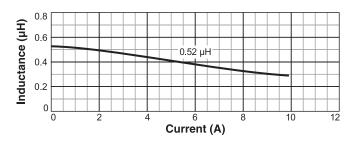


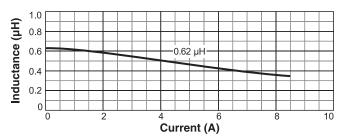
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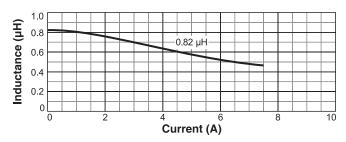
L vs Current

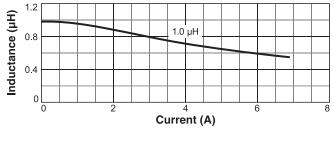


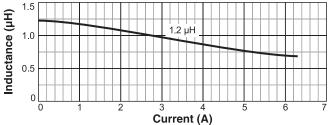














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