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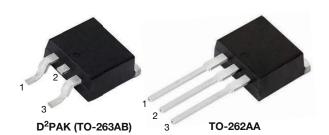
Vishay Semiconductors

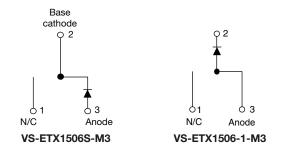
COMPLIANT

HALOGEN

FREE

# Hyperfast Rectifier, 15 A FRED Pt®





PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub> 15 A							
$V_{R}$	600 V						
V <sub>F</sub> at I <sub>F</sub>	1.55 V						
t <sub>rr</sub> (typ.)	18 ns						
T <sub>J</sub> max.	175 °C						
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA						
Circuit configuration	Single						

#### **FEATURES**

- Hyperfast recovery time, extremely low Q<sub>rr</sub>
- · Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC®-JESD 47
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recover time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 127 °C	15	Λ					
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	120	A					
Operating junction and storage temperatures	$T_J$ , $T_{Stg}$		-65 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL TEST CONDITIONS				MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-	.,		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	2.5	3.4	V		
Forward voltage		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.55	2			
Doverno logicogo current	I <sub>R</sub>	$V_R = V_R$ rated	-	0.02	36			
Reverse leakage current		$T_J = 150$ °C, $V_R = V_R$ rated	-	40	250	μA		
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	12	-	pF		
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ		



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST (	CONDITIONS	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, dI_F/dt = 1$	00 A/μs, V <sub>R</sub> = 30 V	-	17	23			
Poveree recovery time		$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A}$	100 A/μs, V <sub>R</sub> = 30 V	-	18	30	no		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-	ns		
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 390 V	-	45	-			
5 .		T <sub>J</sub> = 25 °C		-	2.7	-	A		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	5.5	-			
Doverse receivent charge	0	T <sub>J</sub> = 25 °C		-	26	-	nC		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	130	-	110		
Reverse recovery time	t <sub>rr</sub>		I <sub>E</sub> = 15 A	-	32	-	ns		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = 800 A/\mu s$	-	17	-	Α		
Reverse recovery charge	Q <sub>rr</sub>	1	$V_{R} = 390 \text{ V}$	-	290	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C			
Thermal resistance, junction to case	$R_{thJC}$		-	1.3	1.51	°C/W			
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70				
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-				
Woight			-	2.0	-	g			
Weight			-	0.07	-	OZ.			
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)			
Maulian desire		Case style D <sup>2</sup> PAK (TO-263AB)		ETX1	TX1506S				
Marking device		Case style TO-262	ETX1506-1						

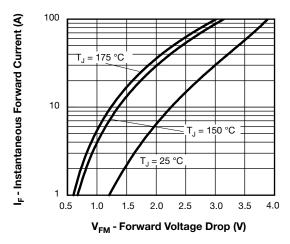


Fig. 1 - Typical Forward Voltage Drop Characteristics

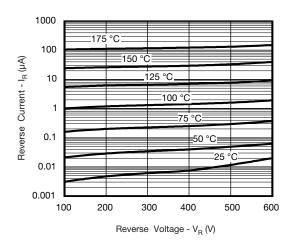


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

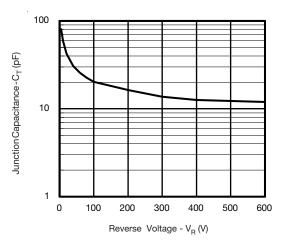


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

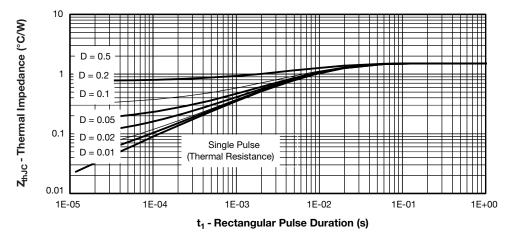


Fig. 4 - Max. Thermal Impedance Z<sub>thJC</sub> Characteristics

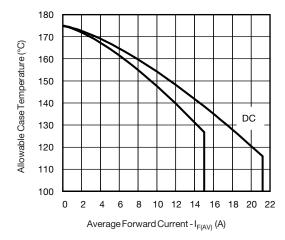


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

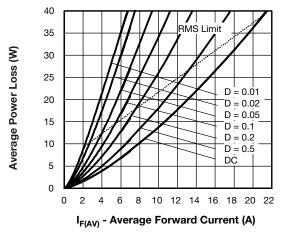


Fig. 6 - Forward Power Loss Characteristics

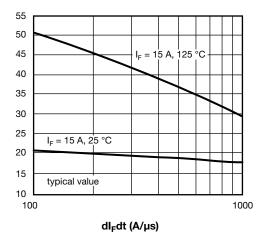


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

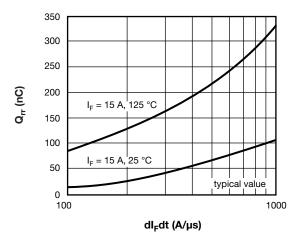
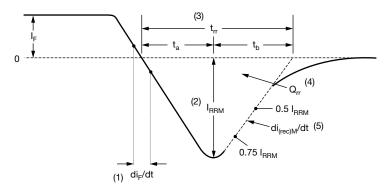


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $t_{\rm rr}$  reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75  $I_{RRM}$  and 0.50  $I_{RRM}$ extrapolated to zero current.
- (4) Q<sub>rr</sub> area under curve defined by t<sub>rr</sub> and I<sub>RRM</sub>

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

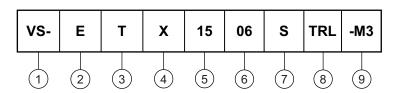
Fig. 9 - Reverse Recovery Waveform and Definitions

# VS-ETX1506S-M3, VS-ETX1506-1-M3

### Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

### **Device code**



Vishay Semiconductors product

Circuit configuration E = single diode

T = TO-220

3 4 5 X = hyperfast recovery time

Current code (15 = 15 A)

Voltage code (06 = 600 V)

•  $S = D^2PAK (TO-263AB)$ 

• -1 = TO-262AA

8 • None = tube (50 pieces)

• TRL = tape and reel (left oriented, for D<sup>2</sup>PAK (TO-263AB) package)

• TRR = tape and reel (right oriented, for D<sup>2</sup>PAK (TO-263AB) package)

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free 9

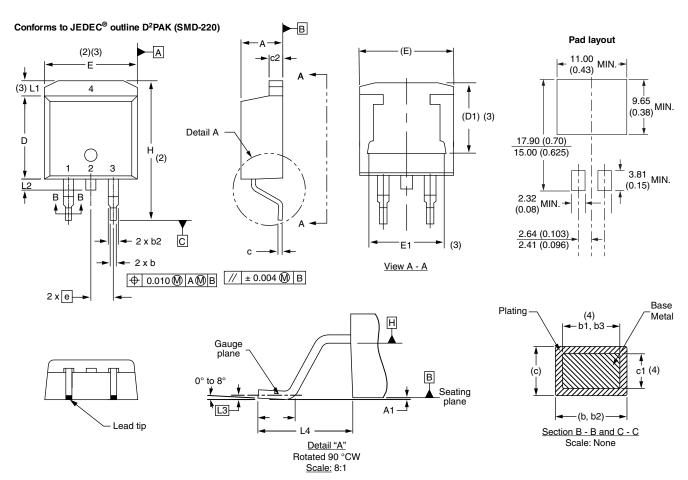
ORDERING INFORMATION (Example)								
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION								
VS-ETX1506S-M3	50	Antistatic plastic tubes						
VS-ETX1506STRR-M3	800	13" diameter plastic tape and reel						
VS-ETX1506STRL-M3	800	13" diameter plastic tape and reel						
VS-ETX1506-1-M3	50	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS							
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164					
Differisions	TO-262AA	www.vishay.com/doc?96165					
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96424					



# D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	NOTES	SYMBOL		MILLIM	ETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

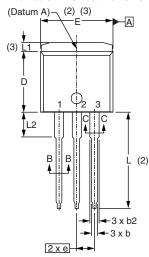
Revision: 13-Jul-17 Document Number: 96164

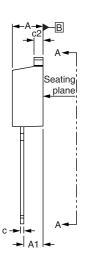


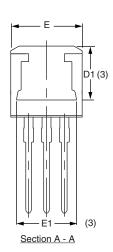
### **TO-262AA**

### **DIMENSIONS** in millimeters and inches

#### Modified JEDEC® outline TO-262







**⊕** 0.010 **M** A**M** B

#### Lead assignments

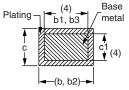


**Diodes** 

1. - Anode (two die)/open (one die)

2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIN	IETERS	INC	INCHES			
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54 BSC		0.100	BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.56	3.71	0.140	0.146			

#### **Notes**

(4) Dimension b1 and c1 apply to base metal only

Controlling dimension: inches

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)



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