

ASEK-1363-40-41-T-KIT Quick Guide

The ASEK-1363-40-41-T-KIT as described below is for the purpose of evaluating certain Allegro devices in the KT package with an ASEK20.

At the time of release, this board supports A1340, A1341 and A1363 KT devices.
 Note: This board does not support A1367.

ASEK-1363-40-41-T-KIT Bill of Materials

- ASEK-20 Kit (Part # 85-0540-600)
 - ASEK-20 Chassis with main Motherboard inside (85-0540-004)
 - USB Communications Cable
 - DC Power Supply/Cable with AC Outlet Adapters
 - Proto Board (Part # 85-0540-103)
 - Ribbon Cable (Part # 85-0540-300)
- ASEK-1363-40-41-T:
 - ASEK-20 4 Lead KT Daughterboard (Part #: 85-0614-003 Rev2)

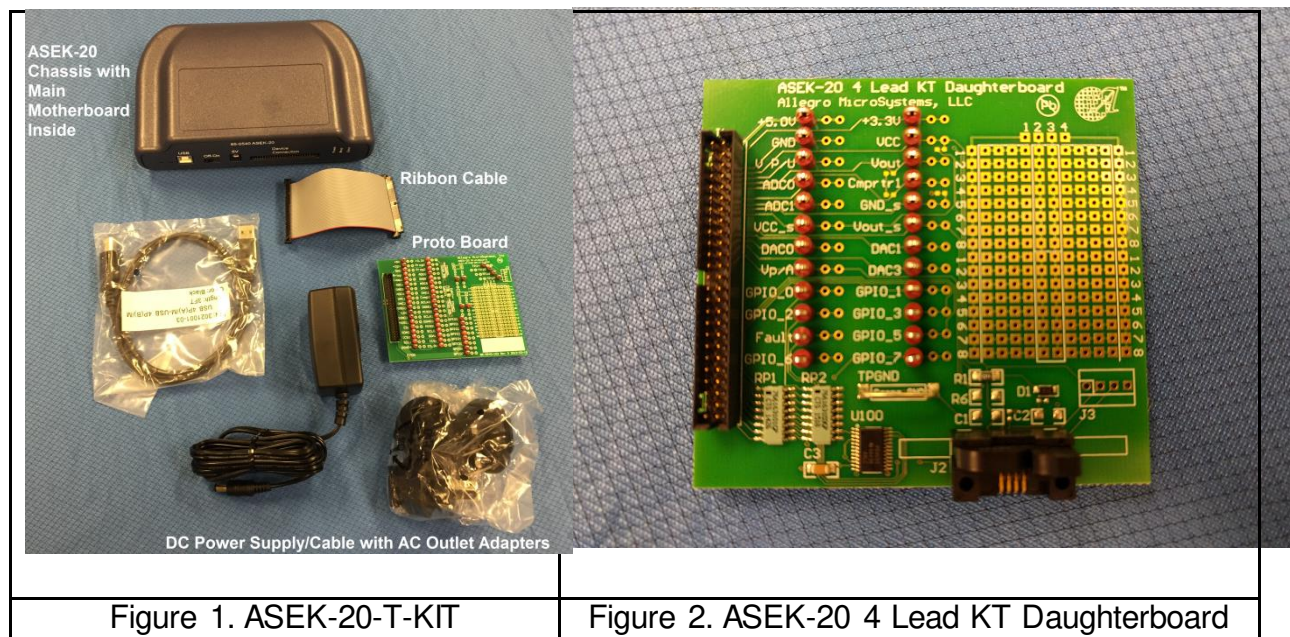


Figure 1. ASEK-20-T-KIT

Figure 2. ASEK-20 4 Lead KT Daughterboard

Instructions for Configuring ASEK-1363-40-41-T-KIT

1. Connect one end of the USB communications cable to a personal computer
2. Connect the other end of the USB communications cable to the “USB” port on the ASEK-20 chassis.
3. Connect the ribbon cable to the J2 connector on the daughterboard (85-0614-003)
4. Connect the other end of the ribbon cable to the “Device Connection” port on the ASEK-20 chassis
5. Connect the DC Power Supply/Cable to the 5V port on the ASEK-20 chassis
6. Plug in the DC Power Supply to a 110V/220AC 60/50Hz outlet with the proper adapter
7. Place your KT device in daughter board socket.
8. If device cannot be evaluated directly in the daughterboard socket, customer can construct a wiring harness to interface the daughterboard. Necessary test points are indicated below.

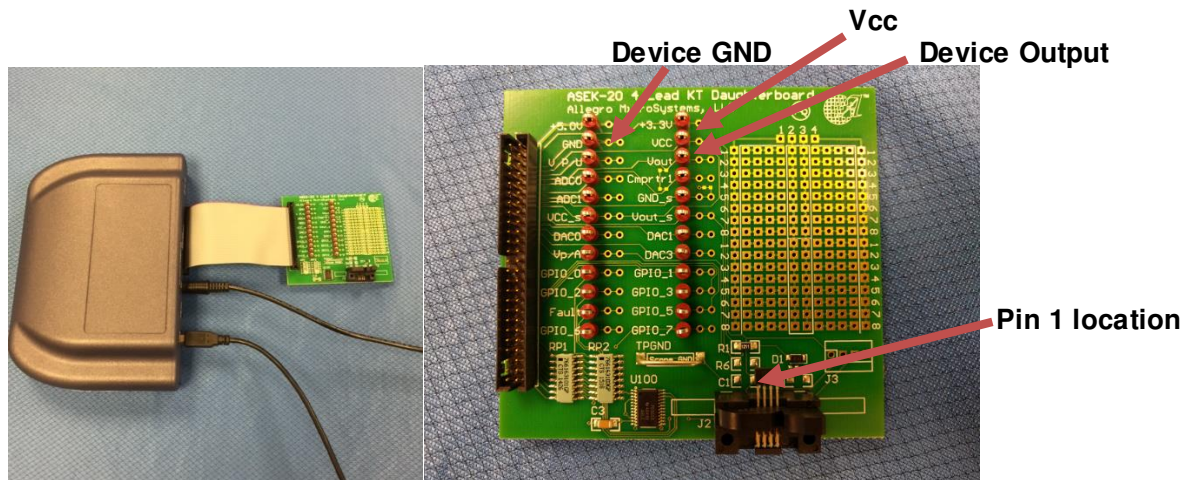


Figure 3. Setup of ASEK-1363-40-41-T-KIT

Software for ASEK-1363-40-41-T-KIT

In order to download the software for your specific Allegro KT Package device, register at the software portal below:

<https://registration.allegromicro.com/login>

Allegro Sample Devices

This kit does not include sample devices.

Revision History Table

Revision	Change Description	Res.	Page(s)	Date
-	Original release	WB	All	1/20/2016
1.0	Added ASEK20 documentation	WB	All	3/8/2016