

#### **NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART**



**DMJ70H900HJ3** 

#### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS</sub> (ON) Max	I <sub>D Max</sub> T <sub>C</sub> = +25°C	
700V	$0.9\Omega$ @ $V_{GS} = 10V$	7A	

### **Features and Benefits**

- Low On-Resistance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

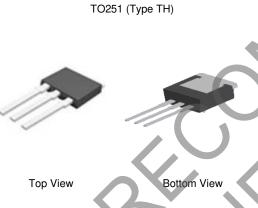
## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

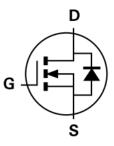
- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

### **Mechanical Data**

- Case: TO251
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (Approximate)







Top View Pin Configuration

Internal Schematic

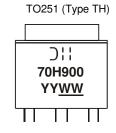
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H900HJ3	TO251 (Type TH)	75 Pieces / Tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



 $\bigcup \prod = Manufacturer's Marking$ 70H900 = Product Type Marking Code YYWW = Date Code Marking YY or YY = Last Two Digits of Year (ex: 19 = 2019) WW or WW = Week Code (01 to 53)



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# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	700	V	
Gate-Source Voltage	V <sub>GSS</sub>	±30	V	
Ocation of Davis Oceans (Note 5) V	T <sub>C</sub> = +25°C		7	Α
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	T <sub>C</sub> = +100°C	ID	4	
Maximum Body Diode Forward Current (Note 6)	•	Is	1.6	Α
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	10	Α
Avalanche Current (Note 7)	L = 60mH	I <sub>AS</sub>	1.3	А
Avalanche Energy (Note 7)	L = 60mH	E <sub>AS</sub>	50	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	8	V/ns

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

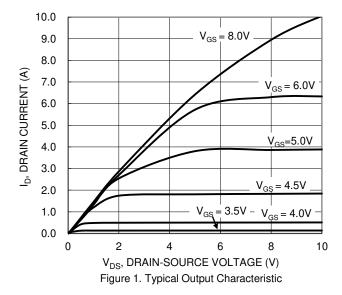
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	PD	68	w
Thermal Resistance, Junction to Ambient (Note 6)	</td <td>R<sub>θJA</sub></td> <td>79</td> <td>°C/W</td>	R <sub>θJA</sub>	79	°C/W
Thermal Resistance, Junction to Case (Note 5)		R <sub>0JC</sub>	1.8	C/ VV
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	700		<b>/</b> -	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	1	_	1	μΑ	$V_{DS} = 700V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		1	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	3.4	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		0.7	0.9	Ω	$V_{GS} = 10V, I_D = 1.5A$	
Diode Forward Voltage	V <sub>SD</sub>		0.85	1.3	V	$V_{GS} = 0V$ , $I_S = 5A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>	_	603	_		V 50V f 1MIL	
Output Capacitance	Coss	_	230	_	pF	$V_{DS} = 50V$ , $f = 1MHz$ , $V_{GS} = 0V$	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	16	_			
Gate Resistance	$R_g$	_	4	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	18.4	_		$V_{DD} = 560V, I_D = 5A,$	
Gate-Source Charge	Q <sub>gs</sub>	_	2.1	_	nC		
Gate-Drain Charge	Q <sub>gd</sub>	_	9.8	_		$V_{GS} = 10V$	
Turn-On Delay Time	t <sub>D(ON)</sub>		8.7	_		$V_{DD} = 350V, V_{GS} = 10V,$ $R_g = 4.7\Omega, I_D = 5A$	
Turn-On Rise Time	t <sub>R</sub>	_	18.7	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	28.5	_			
Turn-Off Fall Time	t <sub>F</sub>	_	10.7	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>		239	_	ns	I- 5A di/dt 100A/vo	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>		2.2		μC	I <sub>S</sub> = 5A, di/dt = 100A/μs	

Notes:

- 5. Device mounted on infinite heatsink.
- 6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- Guaranteed by design. Not subject to production testing.
   Short duration pulse test used to minimize self-heating effect.



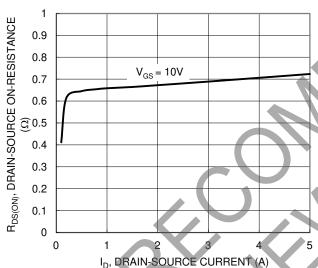


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

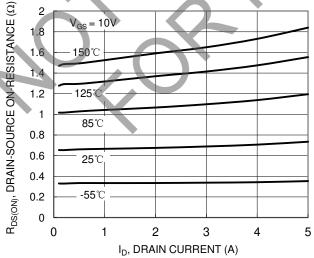


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

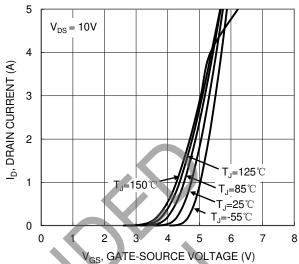


Figure 2. Typical Transfer Characteristic

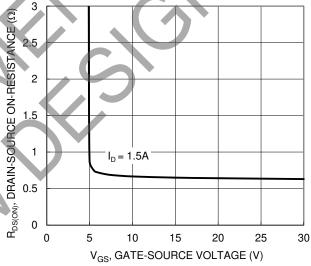


Figure 4. Typical Transfer Characteristic

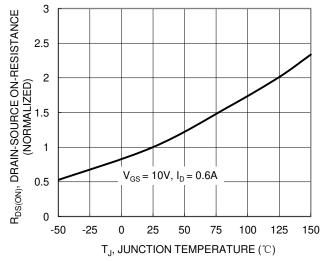


Figure 6. On-Resistance Variation with Temperature



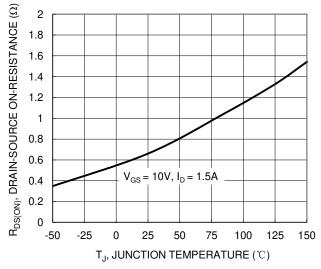
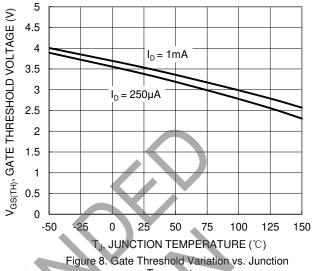
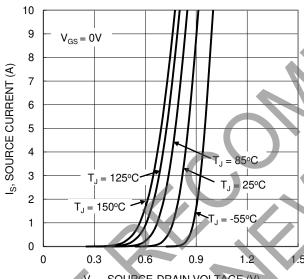


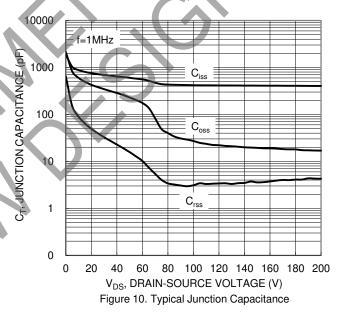
Figure 7. On-Resistance Variation with Temperature



Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



100  $R_{\text{DS}(\underline{O}N)}$  Limited  $P_W = 1 \mu s$ ID, DRAIN CURRENT (A) 10  $P_W = 10ms$ =100ms  $T_{J(Max)} = 150^{\circ}C$   $T_C = 25^{\circ}C$ Single Pulse DUT on Infinite Heatsink V<sub>GS</sub>= 10V 0.01 100 1000 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area

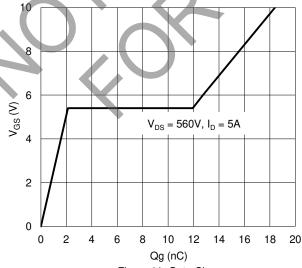


Figure 11. Gate Charge



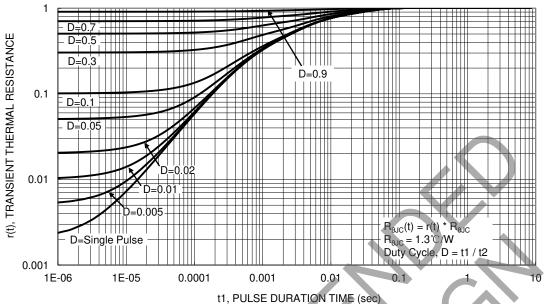


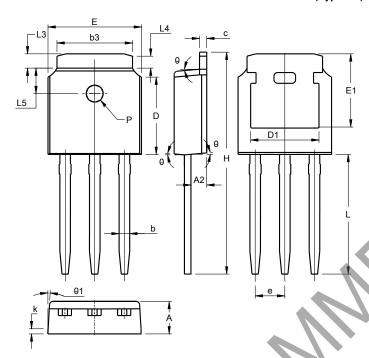
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### TO251 (Type TH)



TO251 (Type TH)						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b3	5.20	5.50	5.33			
С	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	¥,	5.30 REF	=			
е	2	.286 BS	C			
E	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
H.	16.22	16.82	16.52			
k		0.40REF				
	9.15	9.65	9.40			
L3	0.88	1.28	1.02			
L4	0.75 REF					
L5	1.65	1.95	1.80			
PØ	1.20					
θ	5°	9°	7°			
θ1	5°	9°	7°			
All Dimensions in mm						



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