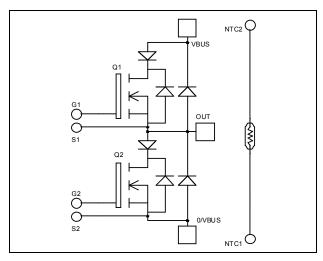
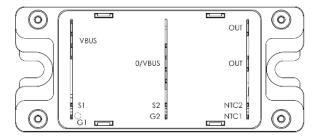


Phase leg Series & SiC parallel diodes Super Junction MOSFET Power Module





## APTC60AM35SCTG

## $V_{DSS} = 600V$

 $R_{DSon} = 35m\Omega \max @ Tj = 25^{\circ}C$ 

 $I_D = 72A$  @ Tc = 25°C

#### Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### Features

### *CoolMOS*<sup>тм</sup>

- Ultra low R<sub>DSon</sub>
- Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated

### Parallel SiC Schottky Diode

- Zero reverse recovery
- \_ Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
- Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

### **Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- **RoHS** Compliant

Symbol	Parameter		Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage		600	V
т		$T_c = 25^{\circ}C$	72	
I <sub>D</sub>	Continuous Drain Current	$T_c = 80^{\circ}C$	54	А
I <sub>DM</sub>	Pulsed Drain current	288		
V <sub>GS</sub>	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		35	mΩ
P <sub>D</sub>	Maximum Power Dissipation	$T_c = 25^{\circ}C$	416	W
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)		20	А
E <sub>AR</sub>	Repetitive Avalanche Energy		1	mI
E <sub>AS</sub>	Single Pulse Avalanche Energy		1800	mJ

All ratings (a)  $T_i = 25^{\circ}C$  unless otherwise specified

# Absolute maximum ratings

👀 🚓 CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}C$			50	
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125^{\circ}C$			500	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 36A$			35	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2mA$	2.1	3	3.9	V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = \pm 20 V, V_{DS} = 0V$			±150	nA

## **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$		14		
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 25 V$		5.13		nF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		0.42		
Qg	Total gate Charge	$V_{GS} = 10V$		518		
Q <sub>gs</sub>	Gate – Source Charge	$V_{Bus} = 300 V$		58		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 72A$		222		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive switching @ 125°C		21		
Tr	Rise Time	$V_{GS} = 15V$		30		ns
T <sub>d(off)</sub>	Turn-off Delay Time	$V_{Bus} = 400V$ $I_D = 72A$		283		
$T_{\rm f}$	Fall Time	$R_G = 2.5\Omega$		84		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		804		т
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 400V$ $I_D = 72A, R_G = 2.5\Omega$		1960		μJ
Eon	Turn-on Switching Energy	Inductive switching (a) 125°C $V_{GS} = 15V$ , $V_{Bus} = 400V$ $I_D = 72A$ , $R_G = 2.5\Omega$		1315		T
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy			2412		μJ
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.3	°C/W

### Series diode ratings and characteristics

Symbol	Characteristic Test Conditions			Min	Тур	Max	Unit	
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage					600	V	
I <sub>RM</sub>	Reverse Leakage Current	$V_{R} = 600 V$				150	μA	
$I_{\rm F}$	DC Forward current		$Tc = 80^{\circ}C$		100		Α	
$V_{\rm F}$	Diode Forward Voltage	$I_{\rm F} = 100 {\rm A}$	$T_i = 25^{\circ}C$		1.6	2	V	
v <sub>F</sub>	Diode Forward Voltage	$V_{GE} = 0V$	$T_i = 150^{\circ}C$		1.5		v	
+	Reverse Recovery Time	$I_{F} = 100A$ $V_{R} = 300V$ $di/dt = 2500A/\mu s$	$T_j = 25^{\circ}C$		100		ns	
t <sub>rr</sub>	Reverse Recovery Time		$T_{j} = 150^{\circ}C$		150		115	
0	Pavara Paaavary Charga		$V_{\rm R} = 300 V$ $I_{\rm j} = 25^{\circ} C$	$T_j = 25^{\circ}C$		5.1		
Q <sub>rr</sub>	Reverse Recovery Charge				10.7		μC	
Б	Powerse Poeevery Energy	Prese Recovery Energy	$T_i = 25^{\circ}C$		1.2		mJ	
Err	Reverse Recovery Ellergy		$T_{j} = 150^{\circ}C$		2.4		IIIJ	
R <sub>thJC</sub>	Junction to Case Thermal Resistance					0.71	°C/W	



### Parallel SiC diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage					600	V
I <sub>RM</sub>	Reverse Leakage Current	V <sub>R</sub> =600V	$T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$		200 400	800 4000	μΑ
I <sub>F</sub>	DC Forward Current	$Tc = 125^{\circ}C$			40		Α
$\mathbf{V}_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 40A \qquad \qquad \frac{T_i = 25^{\circ}C}{T_i = 175^{\circ}C}$			1.6 2.0	1.8 2.4	V
Qc	Total Capacitive Charge	$I_F = 40A, V_R = 600V$ di/dt =1200A/µs			112		nC
С	Total Capacitance	$f = 1MHz, V_R = 200V$			260		pF
R <sub>thJC</sub>	Junction to Case Thermal Resistance	$f = 1 MHz, V_R = 400V$			200	0.8	°C/W

## Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
T <sub>J</sub>	Operating junction temperature range			-40	150	
T <sub>JOP</sub>	Recommended junction temperature under switching conditions			-40	T <sub>J</sub> max -25	°C
T <sub>STG</sub>	Storage Temperature Range			-40	125	C
T <sub>C</sub>	Operating Case Temperature			-40	100	
Torque	Mounting torque	To Heatsink	M5	2.5	4.7	N.m
Wt	Package Weight				160	g

### Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	Min	Тур	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
$\Delta R_{25}/R_{25}$			5		%
B <sub>25/85</sub>	$T_{25} = 298.15 \text{ K}$		3952		K
$\Delta B/B$	T <sub>C</sub> =100	°C	4		%

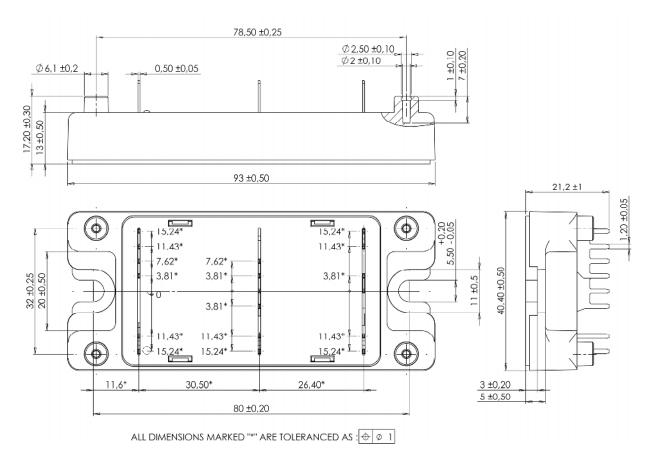
$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$
 T: Therm

Thermistor temperature T: Thermistor value at T

APTC60AM35SCTG-Rev 6 September, 2014



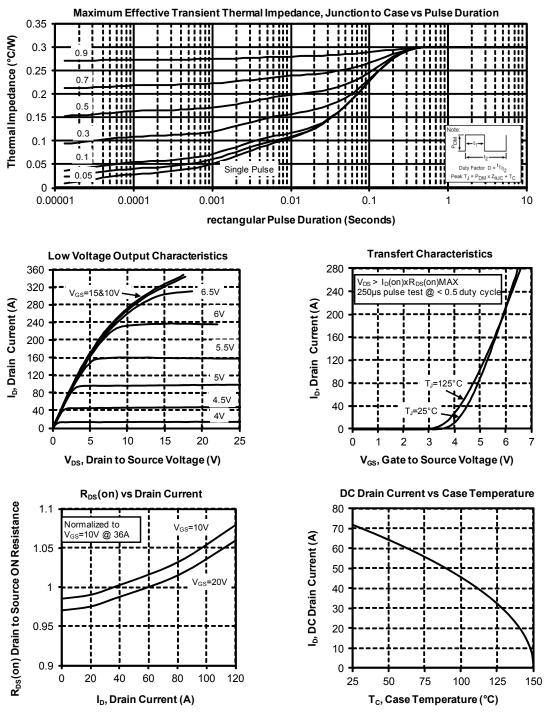
### SP4 Package outline (dimensions in mm)



See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

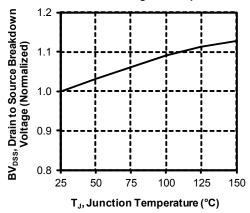


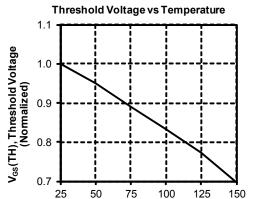
### **Typical CoolMOS Performance Curve**





**Breakdown Voltage vs Temperature** 

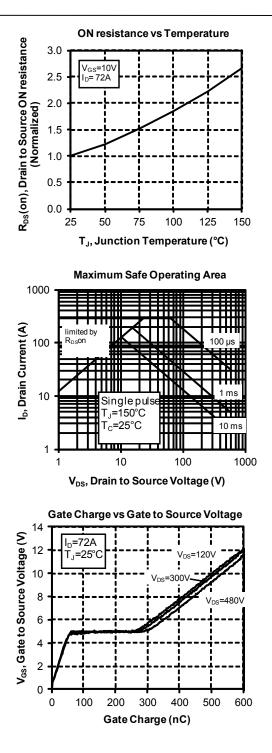




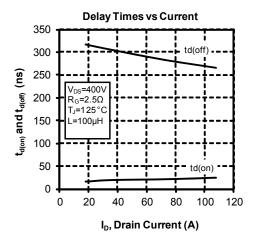
T<sub>c</sub>, Case Temperature (°C)

**Capacitance vs Drain to Source Voltage** 100000 Ciss C, Capacitance (pF) 10000 Cos 1000 Crs 100 10 0 20 30 40 50 10 V<sub>DS</sub>, Drain to Source Voltage (V)

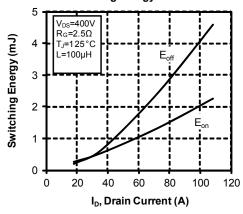
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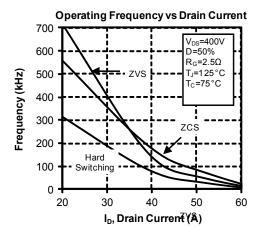


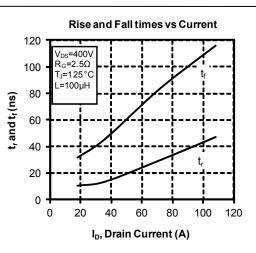




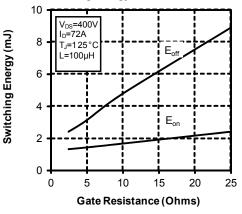






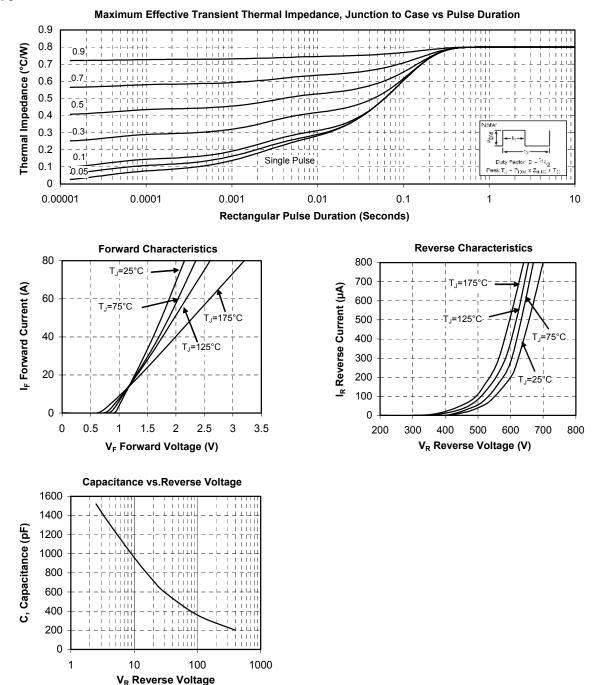


Switching Energy vs Gate Resistance





#### **Typical SiC Diode Performance Curve**



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