





## ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

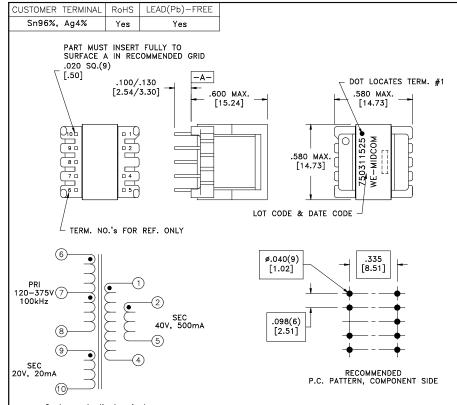
PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-5	tie(1+2, 4+5), @20°C	0.233 ohms ±10%
D.C. RESISTANCE	6-8	@20°C	1.38 ohms ±10%
D.C. RESISTANCE	9-10	@20°C	0.600 ohms ±10%
INDUCTANCE	6-8	10kHz, 100mVAC, Ls	700uH ±10%
SATURATION CURRENT		20% rolloff from initial	1A
LEAKAGE INDUCTANCE	6-8	tie(1+2+4+5, 9+10), 100kHz, 100mVAC, Ls	10uH typ., 15uH max.
DIELECTRIC	1-10	2500VAC, 1 second	2000VAC, 1 minute
DIELECTRIC	6-10	625VAC, 1 second	500VAC, 1 minute
TURNS RATIO		(7-8):(6-7)	1:1, ±1%
TURNS RATIO		(6-8):(1-4)	2.972:1, ±1%
TURNS RATIO		(6-8):(2-5)	2.972:1, ±1%
TURNS RATIO		(6-8):(9-10)	5.789:1, ±1%

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE:  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  including temp rise.

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Functional insulation for a primary circuit at a working voltage of 400VDC.



Customer to tie terminals 1+2 and 4+5 internally on the pc board.

Packaging Specifications REV. DATE Method: Tray PKG-0101 www.we-online.com/midcom SEE REVISION SHEET FOR REVISION LEVEL 5/10

CONVENTION PLACEMENT

Tolerances unless otherwise specified: Angles: ±1° Decimals: ±.005 [.13] Fractions: ±1/64 Footprint: ±.001 [.03]

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

## **TRANSFORMER**

eiSos p/n: 750311525

PART NO.