4V Drive Nch+Nch MOSFET SP8K24

●Structure

Silicon N-channel MOSFET

Features

- 1) Built-in G-S Protection Diode.
- 2) Small and Surface Mount Package (SOP8).

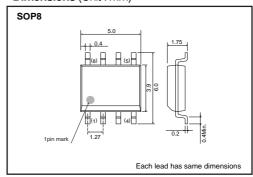
Applications

Power switching , DC / DC converter , Inverter

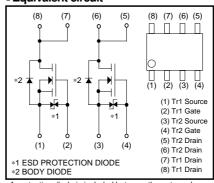
Packaging dimensions

	Package	Taping		
Type	Code	TB		
	Basic ordering unit (pieces)	2500		
SP8K24		0		

●Dimensions (Unit:mm)



●Equivalent circuit



^{*}A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

● Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2.>

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	45	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	Continuous	I_D	±6.0	Α	
	Pulsed	I _{DP} *1	±24	Α	
Source current (Body diode)	Continuous	I _S	1	Α	
	Pulsed	I _{SP} *1	24	Α	
Total power dissipation		P _{D *2}	2	W / TOTAL	
		Γ _D *2	1.4	W / ELEMENT	
Chanel temperature		T _{ch}	150	°C	
Range of Storage temperature		T_{stg}	-55 to +150	°C	

^{*1} PW ≤10μs, Duty cycle ≤ 1%

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^{*2} Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	I _{GSS}	-	_	±10	μА	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V _(BR) DSS	45	_	_	٧	I _D = 1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	_	_	1	μА	Vps= 45V, Vgs=0V	
Gate threshold voltage	V _{GS (th)}	1.0	_	2.5	V	V _{DS} = 10V, I _D = 1mA	
Static drain-source on-state resistance	R _{DS} (on)*	-	18	25	mΩ	I _D = 6.0A, V _{GS} = 10V	
		-	24	34	mΩ	I _D = 6.0A, V _{GS} = 4.5V	
		_	26	37	mΩ	ID= 6.0A, VGS= 4.0V	
Forward transfer admittance	Y _{fs} *	6.0	_	_	S	V _{DS} = 10V, I _D = 6.0A	
Input capacitance	Ciss	_	1400	_	pF	V _{DS} = 10V	
Output capacitance	Coss	_	310	_	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	_	175	_	pF	f=1MHz	
Turn-on delay time	t _{d (on)} *	_	19	_	ns	V _{DD} ≒ 25V	
Rise time	tr *	_	30	_	ns	ID= 3.0A	
Turn-off delay time	t _{d (off)} *	_	72	_	ns	V _{GS} = 10V R _L = 8Ω	
Fall time	t _f *	_	27	_	ns	R _G =10Ω	
Total gate charge	Qg *	_	15.4	21.6	nC	V _{DD} ≒25V, V _{GS} =5V	
Gate-source charge	Q _{gs} *	_	3.7	_	nC	I _D = 6.0A	
Gate-drain charge	Q _{gd} *	_	6.5	_	nC	$R_L=4\Omega$, $R_G=10\Omega$	

^{*}Pulsed

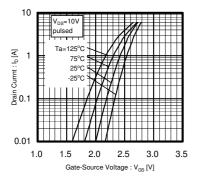
●Body diode characteristics (Source-drain) (Ta=25°C)

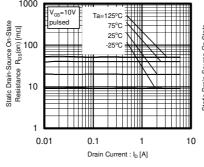
<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward voltage	V _{SD} *	_	_	1.2	V	$I_S = 6.0 \text{A/V}_{GS} = 0 \text{V}$

^{*} pulsed

•Electrical characteristic curves





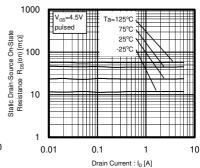


Fig.1 Typical Transfer Characteristics

Fig.2 Static Drain-Source On-State Resistance vs. Drain Current (1)

Fig.3 Static Drain-Source On-State Resistance vs. Drain Current (2)

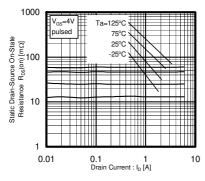


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current (3)

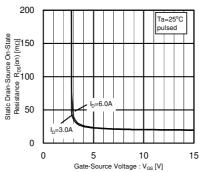


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

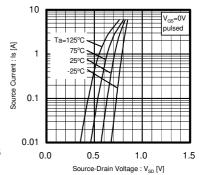


Fig.6 Source-Current vs. Source-Drain Voltage

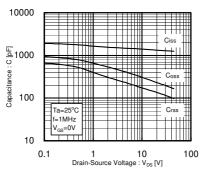


Fig.7 Typical capacitance vs. Source-Drain Voltage

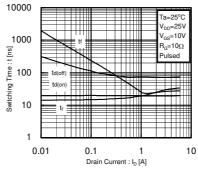


Fig.8 Switching Characteristics

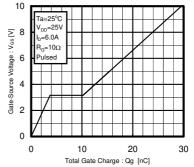


Fig.9 Dynamic Input Characteristics

Measurement circuits

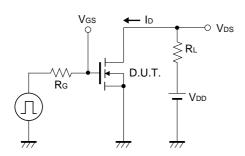


Fig.10 Switching Time Test Circuit

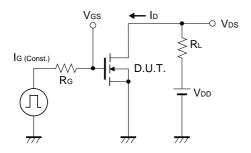


Fig.12 Gate Charge Test Circuit

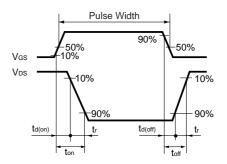


Fig.11 Switching Time Waveforms

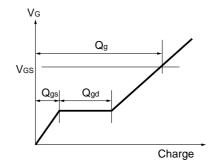


Fig.13 Gate Charge Waveform

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