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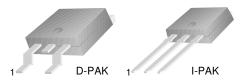
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### KSH2955

# **General Purpose Amplifier Low Speed Switching Applications** D-PAK for Surface Mount Applications Lead Formed for Surface Mount Applications (No Suffix) Straight Lead (I-PAK, "-I " Suffix)

- Electrically Similar to Popular KSE2955T
- DC Current Gain Specified to 10A
- High Current Gain Bandwidth Product:  $f_T = 2MHz (MIN), I_C = -500mA$



1.Base 2.Collector 3.Emitter

### **PNP Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	- 70	V
V <sub>CEO</sub>	Collector-Emitter Voltage	- 60	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 5	V
I <sub>C</sub>	Collector Current	- 10	Α
I <sub>B</sub>	Base Current	- 6	Α
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	20	W
	Collector Dissipation (T <sub>a</sub> =25°C)	1.75	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage	$I_C = -30 \text{mA}, I_B = 0$	-60		V
I <sub>CEO</sub>	Collector Cut-off Current	$V_{CE} = -30V, I_{E} = 0$		- 50	μΑ
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -70V, I_{E} = 0$		- 2	mA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 0.5	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = -4V, I_{C} = -4A$ $V_{CE} = -4V, I_{C} = -10A$	20 5	100	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	$I_C = -4A$ , $I_B = -0.4A$ $I_C = -10A$ , $I_B = -3.3A$		- 1.1 - 8	V V
V <sub>BE</sub> (on)	* Base-Emitter On Voltage	V <sub>CE</sub> = - 4V, I <sub>C</sub> = - 4A		-1.8	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = - 10V, I <sub>C</sub> = - 500mA	2		MHz

<sup>\*</sup> Pulse Test: PW≤300ms, Duty Cycle≤2%

## **Typical Characteristics**

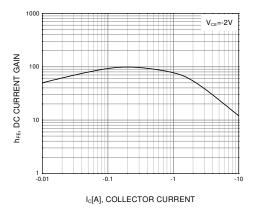


Figure 1. DC current Gain

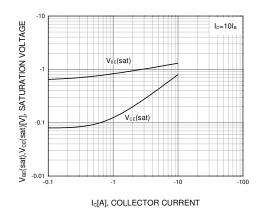


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

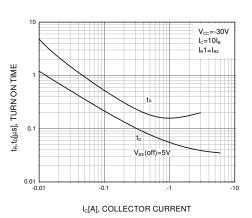


Figure 3. Turn On Time

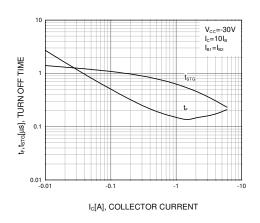


Figure 4. Turn Off Time

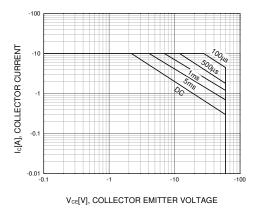


Figure 5. Safe Operating Area

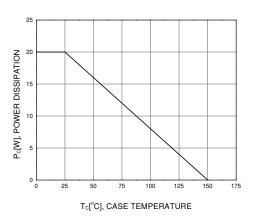
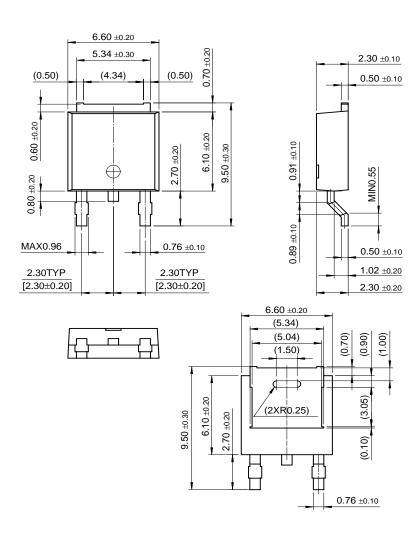


Figure 6. Power Derating

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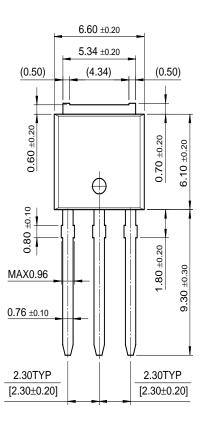
## **Package Dimensions**

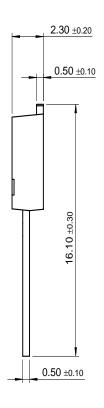
## D-PAK



# Package Dimensions (Continued)

## I-PAK







Dimensions in Millimeters

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