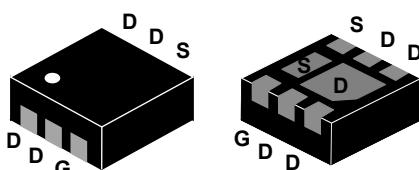
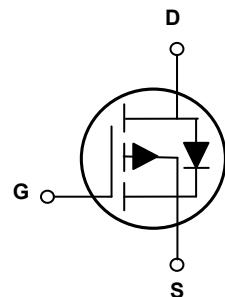


Main Product Characteristics

$V_{(BR)DSS}$	-20V
$R_{DS(ON)}$	28mΩ
I_D	-8.5A



DFN2x2-6L 2EP



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The SSFB2309L utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	V
Drain Current-Continuous ($T_c=25^\circ\text{C}$)	I_D	-8.5	A
Drain Current-Continuous ($T_c=100^\circ\text{C}$)		-5.4	
Drain Current-Pulsed ¹	I_{DM}	-34	A
Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	3.3	W
Power Dissipation-Derated above 25°C		0.026	W/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	38	°C/W
Operating Junction Temperature Range	T_J	-55 To +150	°C
Storage Temperature Range	T_{STG}	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-20	-	-	V
BV_{DSS} Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Reference to 25°C , $\text{I}_D=-1\text{mA}$	-	-0.02	-	$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=-20\text{V}, \text{V}_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	-1	μA
		$\text{V}_{\text{DS}}=-16\text{V}, \text{V}_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$	-	-	-10	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 10\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-4\text{A}$	-	22	28	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-2.5\text{V}, \text{I}_D=-3\text{A}$	-	27	37	
		$\text{V}_{\text{GS}}=-1.8\text{V}, \text{I}_D=-2\text{A}$	-	33	45	
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}, \text{I}_D=-250\mu\text{A}$	-0.3	-0.6	-1	V
$\text{V}_{\text{GS}(\text{th})}$ Temperature Coefficient	$\Delta \text{V}_{\text{GS}(\text{th})}$		-	2	-	$\text{mV}/^\circ\text{C}$
Forward Transconductance	g_{fs}	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_s=-3\text{A}$	-	8.4	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{2,3}	Q_g	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_D=-4\text{A}, \text{V}_{\text{GS}}=-4.5\text{V}$	-	16.1	25	nC
Gate-Source Charge ^{2,3}	Q_{gs}		-	1.8	3	
Gate-Drain Charge ^{2,3}	Q_{gd}		-	3.8	7	
Turn-On Delay Time ^{2,3}	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=-10\text{V}, \text{R}_G=25\Omega, \text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-1\text{A}$	-	8.2	16	nS
Rise Time ^{2,3}	t_r		-	30	57	
Turn-Off Delay Time ^{2,3}	$t_{\text{d}(\text{off})}$		-	71.1	135	
Fall Time ^{2,3}	t_f		-	19.8	38	
Input Capacitance	C_{iss}		-	1440	2100	
Output Capacitance	C_{oss}	$\text{V}_{\text{DS}}=-15\text{V}, \text{V}_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	155	230	pF
Reverse Transfer Capacitance	C_{rss}		-	115	170	
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_s	$\text{V}_{\text{G}}=\text{V}_{\text{D}}=0\text{V}, \text{Force Current}$	-	-	-8.5	A
Pulsed Source Current	I_{SM}		-	-	-17	A
Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=1\text{A}, T_J=25^\circ\text{C}$	-	-	-1	V

Notes:

- Repetitive Rating: Pulsed width limited by maximum junction temperature.
- Pulsed tested: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristics

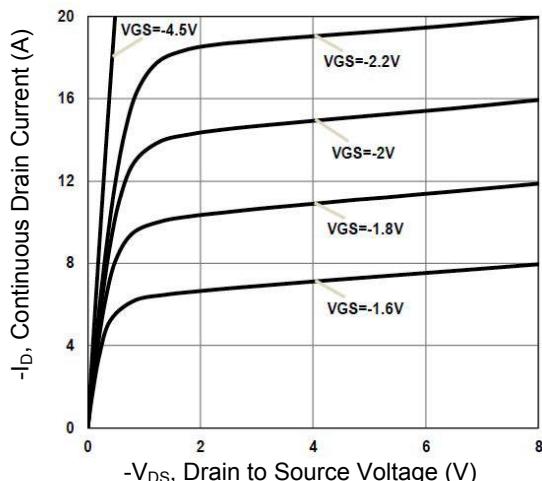


Figure 1. Typical Output Characteristics

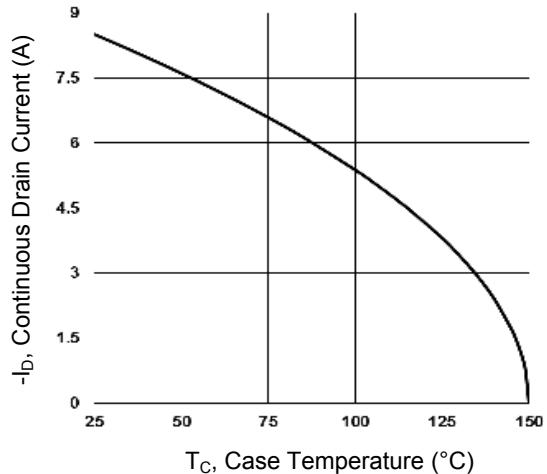


Figure 2. Continuous Drain Current vs. T_c

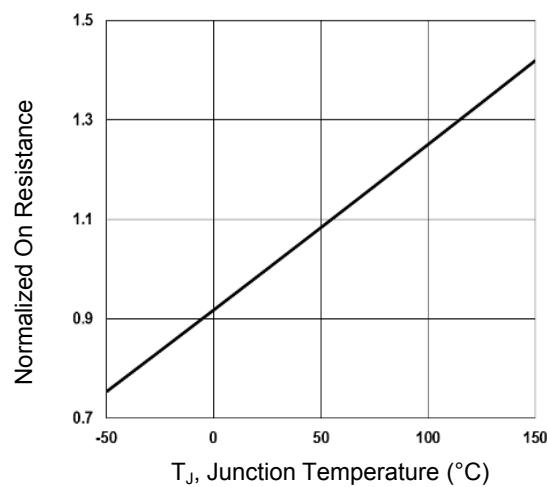


Figure 3. Normalized $R_{DS(on)}$ vs. T_J

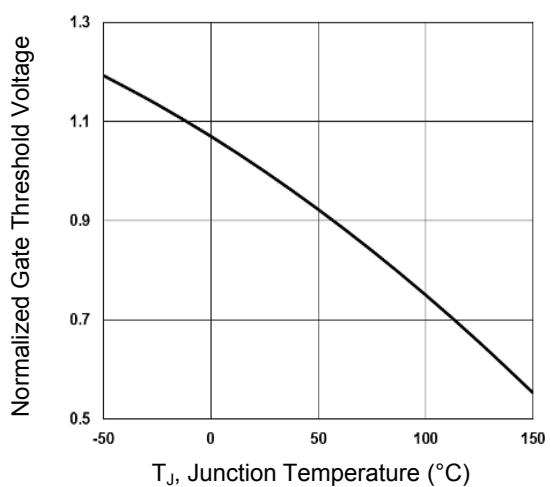


Figure 4. Normalized V_{th} vs. T_J

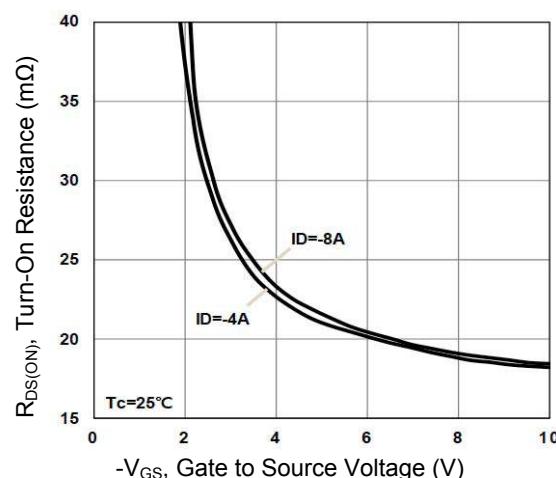


Figure 5. Turn-On Resistance vs. V_{GS}

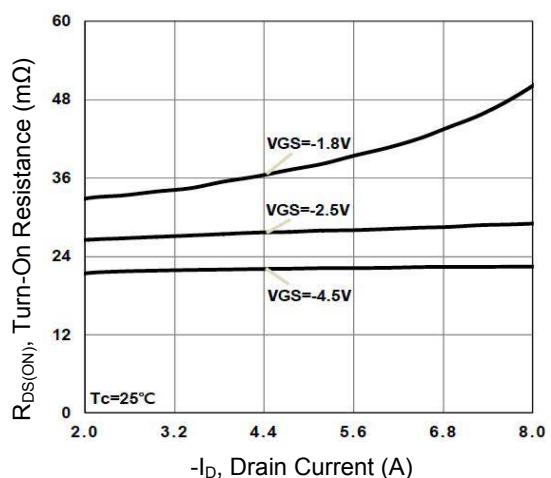


Figure 6. Turn-On Resistance vs. I_D

Typical Electrical and Thermal Characteristics

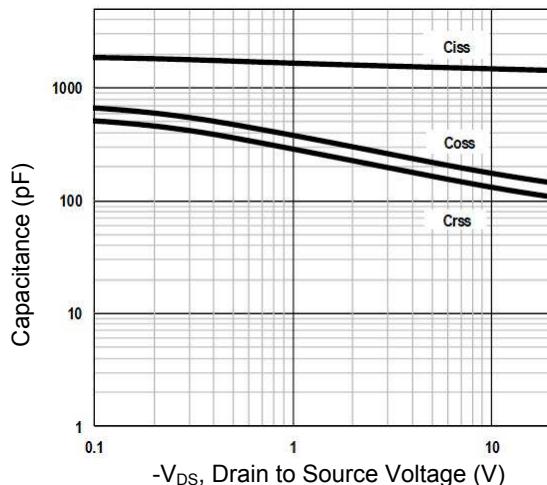


Figure 7. Capacitance Characteristics

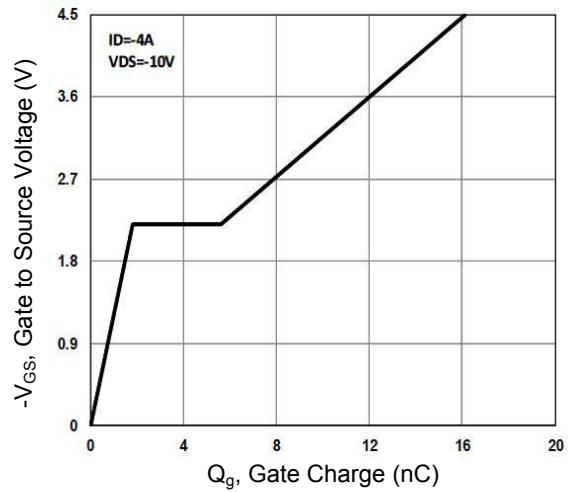


Figure 8. Gate Charge Waveform

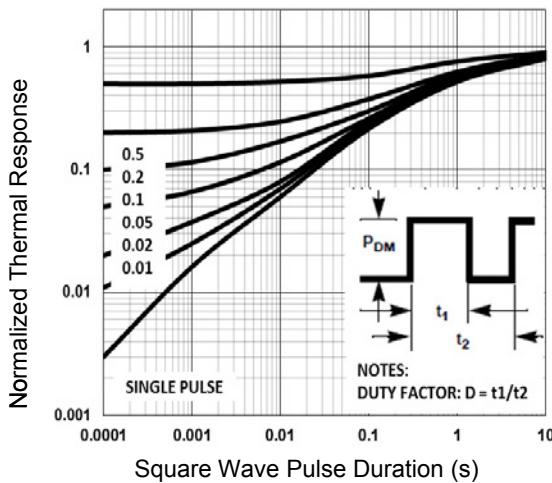


Figure 9. Normalized Transient Impedance

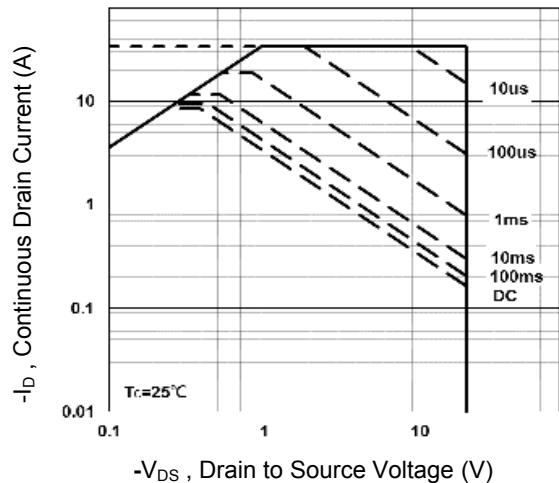


Figure 10. Maximum Safe Operation Area

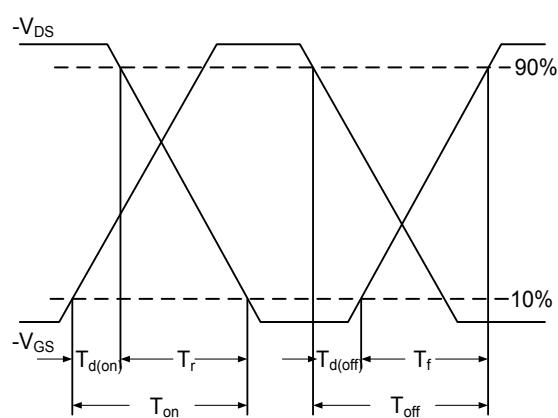


Figure 11. Switching Time Waveform

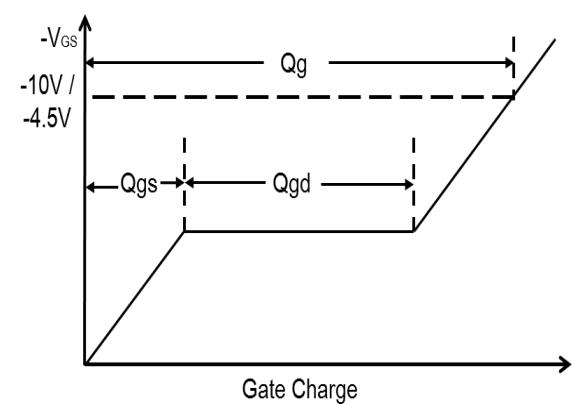
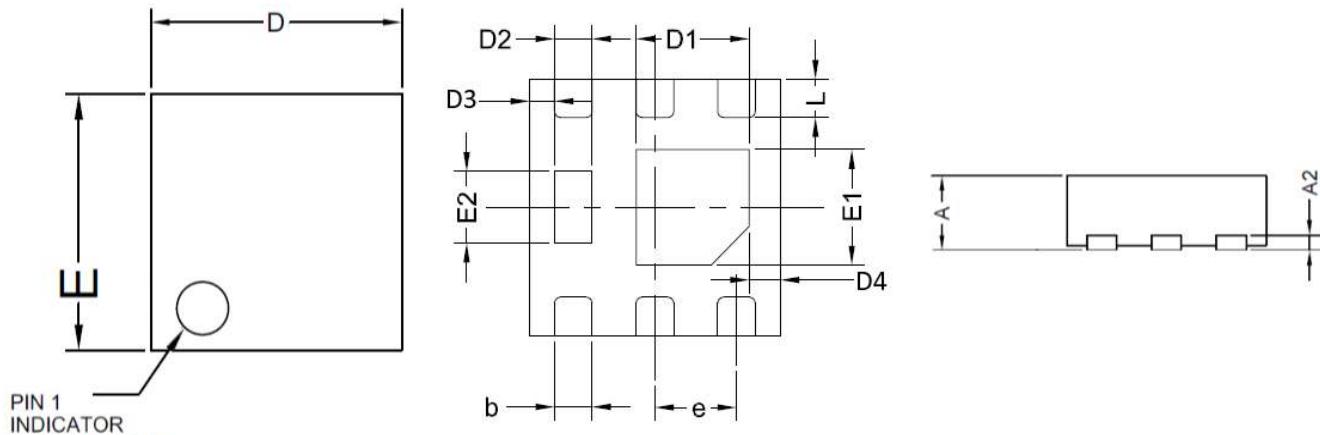


Figure 12. Gate Charge Waveform

Package Outline Dimensions (DFN2x2-6L 2EP)



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	0.500	0.800	0.019	0.031
A2	0.145	0.250	0.006	0.010
b	0.250	0.350	0.010	0.014
D	1.900	2.100	0.075	0.083
D	0.800	1.000	0.031	0.040
D	0.250	0.350	0.010	0.014
² D	0.200 BSC		0.008 BSC	
³ D	0.200 BSC		0.008 BSC	
⁴ E	1.900	2.100	0.075	0.083
E1	0.800	1.050	0.031	0.041
E2	0.460	0.660	0.018	0.026
e	0.650 BSC		0.026 BSC	
L	0.250	0.350	0.010	0.014

Recommended Pad Layout

