

September 2013

BU406 NPN Epitaxial Silicon Transistor

Features

- · High-Voltage Capability
- · High Switching Speed
- · Low Saturation Voltage

Applications

· Horizontal deflection for TV and CRT

Description

The BU406 is a 400 V 7 A Silicon Epitaxial Planar NPN Transistor. The BU406 is designed for high speed switching applications which utilizes the industry standard TO-220 package offering flexibility in design and excellent Power Dissipation.



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	er Marking Package		Packing Method	
BU406	BU406	TO-220 3L	Rail	
BU406TU	BU406	10-220 3L		

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	400	V
V_{CEO}	Collector-Emitter Voltage	200	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	7	Α
I _{CP}	Collector Current (Pulse)	10	Α
Ι _Β	Base Current	4	Α
P _C	Collector Dissipation	60	W
T_J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 to 150	°C

1

Electrical Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Max.	Units
I _{CES}	Collector Cut-Off Current	$V_{CE} = 400 \text{ V}, V_{BE} = 0$		5	mA
		$V_{CE} = 250 \text{ V}, V_{BE} = 0$		100	Α
	Conector out-on ourient	$V_{CE} = 250 \text{ V}, V_{BE} = 0$ at $T_{C} = 150^{\circ}\text{C}$		1	mA
I _{EBO}	Emitter Cut-Off Current	$V_{BE} = 6 \text{ V}, I_{C} = 0$		1	mA
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 5 \text{ A}, I_B = 0.5 \text{ A}$		1	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 5 \text{ A}, I_B = 0.5 \text{ A}$		1.2	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ V}, I_{C} = 0.5 \text{ A}$	10		MHz
t _{OFF}	Turn-Off Time	$I_C = 5 \text{ A}, I_B = 0.5 \text{ A}$		0.75	S

Typical Performance Characteristics

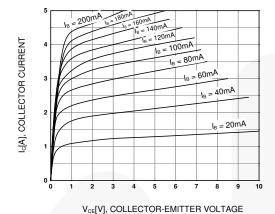


Figure 1. Static Characteristic

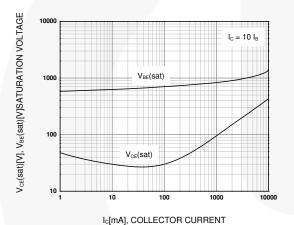


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

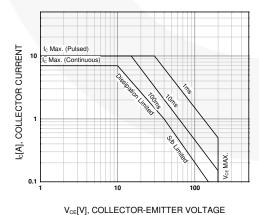


Figure 5. Safe Operating Area

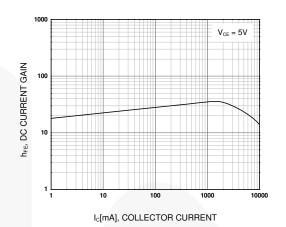


Figure 2. DC Current Gain

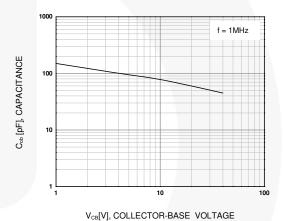


Figure 4. Collector Output Capacitance

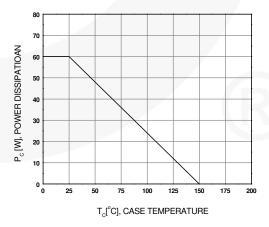
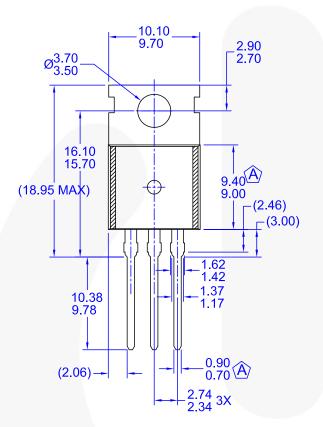
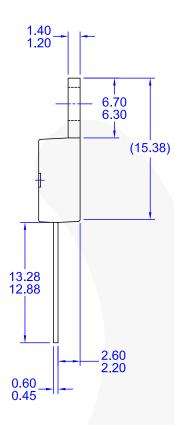


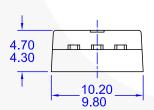
Figure 6. Power Derating

Physical Dimensions

TO-220







NOTES:

- (A) CONFORMS TO JEDEC TO-220 VARIATION AB EXCEPT WHERE NOTED
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D) DRAWING FILE/REVISION: MKT-TO220Y03REV1

Figure 7. TO-220, MOLDED, 3-LEAD, JEDEC VARIATION AB

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Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/dwg/TO/TO220Y03.pdf.

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