# TSUP8M60SH

Taiwan Semiconductor

## 8A, 60V Trench Schottky Rectifier

### FEATURES

• AEC-Q101 qualified

TAIWAN

SEMICONDUCTOR

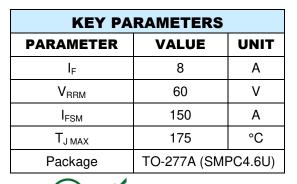
- Patented Trench Schottky technology
- Low power loss, high efficiency
- Ideal for automated placement
- Wettable flank
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

## APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- On-board DC/DC converter
- Automotive

## **MECHANICAL DATA**

- Case: TO-277A (SMPC4.6U)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.104g (approximately)







TO-277A (SMPC4.6U)

Anode 1 O K Anode 2 O Cathode

ABSOLUTE MAXIMUM RATING	<b>S</b> (T <sub>A</sub> = 25°C ur	nless otherwise r	noted)	
PARAMETER		SYMBOL	TSUP8M60SH	UNIT
Marking code on the device			8M60	
Repetitive peak reverse voltage		V <sub>RRM</sub>	60	V
Reverse voltage, total rms value		V <sub>R(RMS)</sub>	42	V
Forward current		I <sub>F</sub>	8	Α
Surge peak forward current single half sine-wave superimposed on rated load	t = 8.3ms		150	
	t = 1.0ms	I <sub>FSM</sub>	297	— A
Junction temperature		TJ	-55 to +175	°C
Storage temperature		T <sub>STG</sub>	-55 to +175	°C



THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance	R <sub>ejl</sub>	7	°C/W
Junction-to-ambient thermal resistance	R <sub>eja</sub>	56	°C/W
Junction-to-case thermal resistance	R <sub>eJC</sub>	12	°C/W

Thermal Performance Note: Units mounted on PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	$I_F = 4A, T_J = 25^{\circ}C$		0.53	-	V
	$I_{F} = 8A, T_{J} = 25^{\circ}C$	V <sub>F</sub>	0.61	0.64	V
	$I_F = 4A, T_J = 125^{\circ}C$		0.44	-	V
	$I_F = 8A, T_J = 125^{\circ}C$		0.56	0.61	V
Reverse current @ rated $V_R^{(2)}$	T <sub>J</sub> = 25°C	I <sub>R</sub>	-	600	μA
	T <sub>J</sub> = 125°C		-	7	mA
Junction capacitance	$1 MHz, V_{R} = 4.0 V$	CJ	547	-	pF

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE	PACKAGE	PACKING	
TSUP8M60SH	TO-277A (SMPC4.6U)	6,000 / Tape & Reel	



100

## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

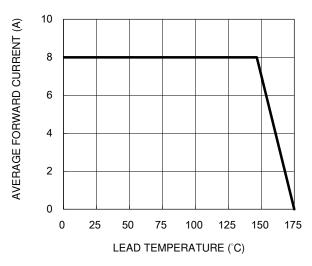
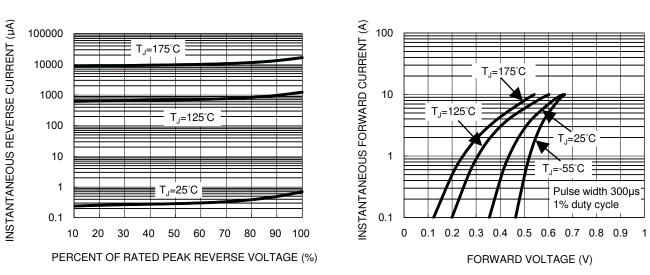


Fig.1 Forward Current Derating Curve

#### Fig.3 Typical Reverse Characteristics



10000

1000

100

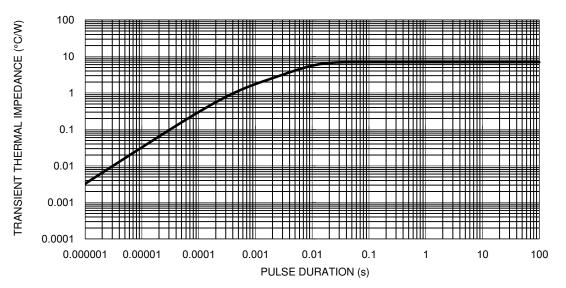
10

1

f=1.0MHz Vsig=50mVp-p

CAPACITANCE (pF)





**Fig.2 Typical Junction Capacitance** 

Ш

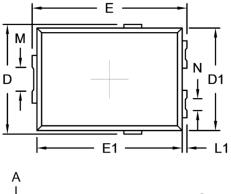
10 REVERSE VOLTAGE (V)

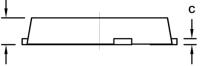
**Fig.4 Typical Forward Characteristics** 

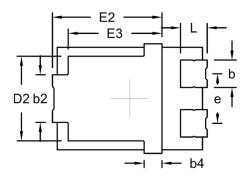


PACKAGE OUTLINE DIMENSIONS

TO-277A (SMPC4.6U)







SUGGESTED PAD LAYOUT

в

D

F

ł

С

1

DIM.	Unit (mm)		Unit (	(inch)
	Min.	Max.	Min.	Max.
A	1.00	1.20	0.039	0.047
b	1.05	1.35	0.041	0.053
b2	1.90	2.20	0.075	0.087
b4	0.75 (NOM.)		0.030	(NOM.)
с	0.15	0.40	0.006	0.016
D	4.45	4.75	0.175	0.187
D1	4.25	4.35	0.167	0.171
D2	3.40	3.70	0.134	0.146
E	6.35	6.65	0.250	0.262
E1	6.05	6.15	0.238	0.242
E2	4.40	4.80	0.173	0.189
E3	3.94 (NOM.)		0.155	(NOM.)
е	2.08 (NOM.)		0.082	(NOM.)
L	0.94	1.24	0.037	0.049
L1	0.05	0.35	0.002	0.014
М	0.65	1.15	0.026	0.045
N	0.25	0.75	0.010	0.030

Package body size D1 and E1 do not include mold flash Mold flash shall not exceed 0.1mm per side

Symbol	Unit (mm)	Unit (inch)
A	4.95	0.195
В	4.95	0.195
С	1.60	0.063
D	1.42	0.056
E	6.95	0.274
F	1.04	0.041

## **MARKING DIAGRAM**

A

1



- E

P/N	= Marking Code
YW	= Date Code

= Factory Code

F



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