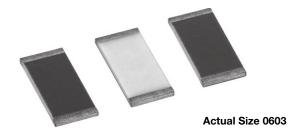
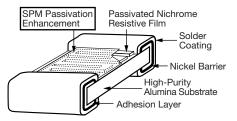
Vishay Dale Thin Film

### Ultra Precision Low TCR Thin Film Resistor, Surface Mount Chip, ± 2 ppm/°C TCR, 0.01 % Tolerance



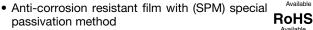
Vishay's proven precision thin film wraparound resistors will meet your exact requirements. These resistors are ideal for precision applications requiring low noise, stability, ultra-low temperature coefficient of resistance, and low voltage coefficient. The chip resistors are available in any resistance ohmic value in the range specified below.

#### CONSTRUCTION



### FEATURES

- TCR of ± 2 ppm/°C standard
- Tolerances to ± 0.01 %



- Stable film and performance characteristics ( $\Delta R \pm 0.04$  % at 70 °C, 10 000 h)
- Non-standard resistance values available
- Very low noise and voltage coefficient (< -30 dB, 0.1 ppm/V)</li>
- UL 94 V-0 flame resistant
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

### **TYPICAL PERFORMANCE**

	ABSOLUTE	
TCR	2	
TOL.	0.01	

STANDARD ELECTRICAL SPECIFICATIONS				
TEST	SPECIFICATIONS	CONDITIONS		
Material	Passivated nichrome	-		
Resistance Range	100 $\Omega$ to 3 M $\Omega$	-		
TCR: Absolute	± 2 ppm/°C	-55 °C to +125 °C		
Tolerance: Absolute	$\pm$ 0.1 % to $\pm$ 0.01 %	+25 °C		
Stability: Absolute	$\Delta R \pm 0.02 \%$	2000 h at 70 °C		
Stability: Ratio	-	-		
Voltage Coefficient	± 0.1 ppm/V (typical)	-		
Working Voltage	75 V to 200 V	-		
Operating Temperature Range	-55 °C to +125 °C	-		
Storage Temperature Range	-55 °C to +155 °C	-		
Noise	< -35 dB (typical)	-		
Shelf Life Stability: Absolute	$\Delta R \pm 0.01 \%$	1 year at +25 °C		

COMPONENT RATINGS				
CASE SIZE	POWER RATING (mW)	WORKING VOLTAGE (V)	RESISTANCE RANGE ( $\Omega$ )	
0603	150	75	100 to 130K	
0805	250	100	100 to 260K	
1206	400	200	100 to 775K	
2010	800	200	150 to 2M	
2512	1000	200	200 to 3M	

Revision: 02-Oct-2019



HALOGEN

FREE

PLTU



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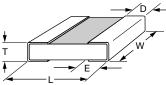


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# Vishay Dale Thin Film

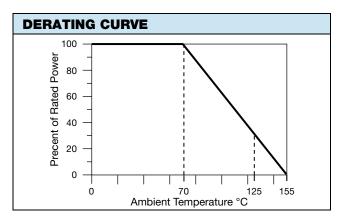
**PLTU** 

### **DIMENSIONS** in inches



			-		
CASE SIZE	L	w	т	D	Е
0603	$0.064 \pm 0.006$	$0.032 \pm 0.005$	0.020 max.	$0.012 \pm 0.005$	$0.015 \pm 0.005$
0805	$0.080 \pm 0.006$	$0.050 \pm 0.005$	0.015 to 0.033	$0.016 \pm 0.008$	$0.015 \pm 0.005$
1206	0.126 ± 0.008	$0.063 \pm 0.005$	0.015 to 0.033	0.020 + 0.005 / - 0.010	0.020 + 0.005 / - 0.010
2010	$0.209 \pm 0.009$	$0.098 \pm 0.005$	0.015 to 0.033	$0.020 \pm 0.005$	$0.020 \pm 0.005$
2512	$0.259 \pm 0.009$	$0.124 \pm 0.005$	0.015 to 0.033	$0.020 \pm 0.005$	$0.020 \pm 0.005$

ENVIRONMENTAL TESTS - TYPICAL			
ENVIRONMENTAL TEST	10 kΩ ∆R ± (%)	100 kΩ Δ <b>R ±</b> (%)	
Thermal Shock	0.02	0.02	
Short Time Overload	0.01	0.01	
Low Temperature Operation	0.01	0.01	
Resistance to Solder Heat	0.01	0.01	
Moisture Resistance	0.02	0.02	
High Temperature Exposure	0.02	0.02	
Load Life (10 000 h, +70 °C)	0.04	0.04	
TCR	± 2 ppm/°C	± 2 ppm/°C	



GLOBAL PA	GLOBAL PART NUMBER INFORMATION				
P L T		0 3 U		) 0 1 Q	B T 1
GLOBAL CASE MODEL SIZE	TCR CHARACTERISTIC	RESISTANCE	TOLERANCE	TERMINATION	PACKAGING
PLTU 0603 0805 1206 2010 2512	U = ± 2 ppm/°C	First 3 digits are significant figures and the last digit specifies the number of zeros to follow. Example: 100 $\Omega$ 1000 = 100 $\Omega$ 1001 = 1 k $\Omega$ Use R to indicate decimal point for value below 1 k $\Omega$ (max. 5 digits). 982R6 = 982.6 $\Omega$ Values above 1 k $\Omega$ (max. 4 digits). 1532 = 15.3 k $\Omega$ 1003 = 100 k $\Omega$	$L = \pm 0.01 \% (^{2})$ $Q = \pm 0.02 \%$ $A = \pm 0.05 \%$ $B = \pm 0.1 \%$ $D = \pm 0.5 \%$ $F = \pm 1 \%$	solder w/Ni barrier	$\label{eq:WS} \begin{split} & WS = WAFFLE \; PACK \\ & WI = 100 \; \mathrm{min., 1} \; \mathrm{mult.} \\ & (\mathrm{item} \; \mathrm{single} \; \mathrm{lot} \; \mathrm{date} \; \mathrm{code}) \\ & WP = 100 \; \mathrm{min., 1} \; \mathrm{mult.} \\ & (\mathrm{package} \; \mathrm{unit} \; \mathrm{single} \; \mathrm{lot} \; \mathrm{date} \; \mathrm{code}) \\ & TAPE \; AND \; REEL \\ & T0 = 100 \; \mathrm{min., 100} \; \mathrm{mult.} \\ & T1 = 1000 \; \mathrm{min., 100} \; \mathrm{mult.} \\ & T3 = 300 \; \mathrm{min., 300} \; \mathrm{mult.} \\ & T5 = 500 \; \mathrm{min., 500} \; \mathrm{mult.} \\ & T5 = 500 \; \mathrm{min., 500} \; \mathrm{mult.} \\ & TF = Full \; reel \\ & TS = 100 \; \mathrm{min., 1} \; \mathrm{mult.} \\ & TI = 100 \; \mathrm{min., 1} \; \mathrm{mult.} \\ & titem \; \mathrm{single} \; \mathrm{lot} \; \mathrm{date} \; \mathrm{code}) \\ & TP = 100 \; \mathrm{min., 1} \; \mathrm{mult.} \\ & (\mathrm{item} \; \mathrm{single} \; \mathrm{lot} \; \mathrm{date} \; \mathrm{code}) \\ & TP = \mathrm{code} \; \mathrm{min., 1} \; \mathrm{mult.} \\ & (\mathrm{package} \; \mathrm{unit} \; \mathrm{single} \; \mathrm{lot} \; \mathrm{date} \; \mathrm{code}) \\ & TP = \mathrm{code} \; \mathrm{min., 1} \; \mathrm{mult.} \\ & (\mathrm{package} \; \mathrm{unit} \; \mathrm{single} \; \mathrm{lot} \; \mathrm{date} \; \mathrm{code}) \\ & TP = \mathrm{code} \; \mathrm{min., 1} \; \mathrm{mult.} \\ & (\mathrm{max} \; \mathrm{code}) \\ & max \; \mathrm{min., 1} \; \mathrm{mult.} \\ & max \; \mathrm{max} \; $

<sup>(1)</sup> Preferred packaging code

 $^{(2)}\,$  L and Q tolerances are available only for resistance values  $\geq$  250  $\Omega$ 

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