FAIRCHILD SEMICONDUCTOR™

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100201
Low Power 2-Input OR/NOR Gate/Inverter

General Description

The 100201 is a 2-input OR/NOR Gate and a single Inverter Gate in an eight pin SOIC package. All inputs have 50 kΩ pull-down resistors and all outputs are buffered. The 100201 is ideal for single gate needs or for use as the feedback loop of a crystal oscillator circuit.

Features

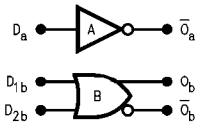
- Small 8 lead 150 mil SOIC package
- 2000V ESD protection
- 300 MHz minimum F toggle
- Temperature compensated
- Voltage compensated operating range = –4.2V to –5.7V V_{EE}

Ordering Code:

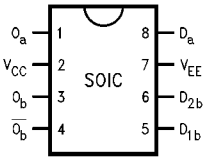
Order Number	Package Number	Package Description
100201SC	M08A	8-Lead Small Outline Intergrated Circuit, JEDEC MS-012, 0.150" Narrow Body

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering code.

Logic Symbol



Connection Diagram



Pin Descriptions

Pin Names	Description
D_a, D_{1b}, D_{2b}	Data Inputs
O_b	Data Outputs
$\overline{O}_a$, $\overline{O}_b$	Complementary Data Outputs

Absolute Maximum Ratings(Note 1)

Storage Temperature (T_{STG})	−65°C to +150°C
Maximum Junction Temperature (T_J)	+150°C
V_{EE} Pin Potential to Ground Pin	−7.0V to +0.5V
Input Voltage (DC)	V_{EE} to +0.5V
Output Current (DC Output HIGH)	−50 mA
ESD (Note 2)	≥2000V

Recommended Operating Conditions

Case Temperature (T_C)	
Commercial	0°C to +85°C
Industrial	−40°C to +85°C
Supply Voltage (V_{EE})	−5.7V to −4.2V

Note 1: Absolute maximum ratings are those values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: ESD testing conforms to MIL-STD-883, Method 3015.

Commercial Version DC Electrical Characteristics

V_{EE} = −4.2V to −5.7V, V_{CC} = GND, T_C = 0°C to +85°C (Note 3)

Symbol	Parameter	Min	Typ	Max	Units	Conditions	
V_{OH}	Output HIGH Voltage	−1025	−955	−870	mV	$V_{IN} = V_{IH(Max)}$ or $V_{IL(Min)}$	Loading with 50Ω to −2.0V
V_{OL}	Output LOW Voltage	−1830	−1705	−1620	mV		
V_{OHC}	Output HIGH Voltage	−1035			mV	$V_{IN} = V_{IH(Min)}$ or $V_{IL(Max)}$	Loading with 50Ω to −2.0V
V_{OLC}	Output LOW Voltage			−1610	mV		
V_{IH}	Input HIGH Voltage	−1165		−870	mV	Guaranteed HIGH Signal for All Inputs	
V_{IL}	Input LOW Voltage	−1830		−1475	mV	Guaranteed LOW Signal for All Inputs	
I_{IL}	Input LOW Current	0.50			µA	$V_{IN} = V_{IL(Min)}$	
I_{IH}	Input HIGH Current			240	µA	$V_{IN} = V_{IH(Max)}$	
I_{EE}	Power Supply Current	−29	−17	−15	mA	Inputs Open	

Note 3: The specified limits represent the "worst case" value for the parameter. Since these values normally occur at the temperature extremes, additional noise immunity and guardbanding can be achieved by decreasing the allowable system operating ranges. Conditions for testing shown in the tables are chosen to guarantee operation under "worst case" conditions.

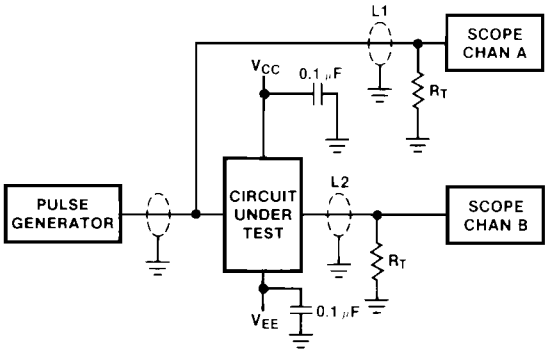
SOIC AC Electrical Characteristics

V_{EE} = −4.2V to −5.7V, V_{CC} = GND

Symbol	Parameter	T_C = 0°C		T_C = +25°C		T_C = +85°C		Units	Conditions
		Min	Max	Min	Max	Min	Max		
t_{PLH}	Propagation Delay	0.4	1.10	0.4	1.15	0.4	1.20	ns	Figures 1, 2
t_{PHL}	Data to Output								(Note 4)
t_{TLH}	Transition Time	0.40	1.20	0.40	1.20	0.40	1.20	ns	Figures 1, 2
t_{THL}	20% to 80%, 80% to 20%								

Note 4: The propagation delay specified is for single output switching. Delays may vary up to 100 ps with multiple outputs switching.

Test Circuitry



Notes:

V_{CC}, V_{CCA} = +2V, V_{EE} = −2.5V

L1 and L2 = equal length 50Ω impedance lines

R_T = 50Ω terminator internal to scope

Decoupling 0.1 μF from GND to V_{CC} and V_{EE}

All unused outputs are loaded with 50Ω to GND

C_L = Fixture and stray capacitance ≤ 3 pF

FIGURE 1. AC Test Circuit

Switching Waveforms

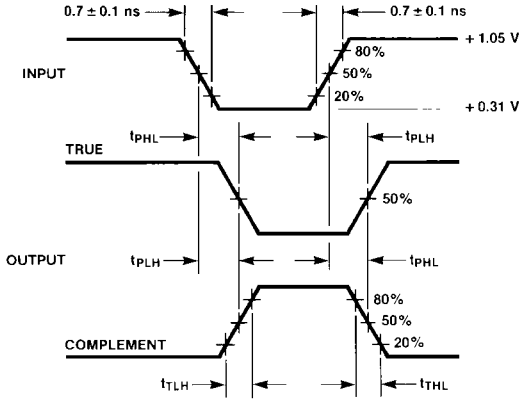
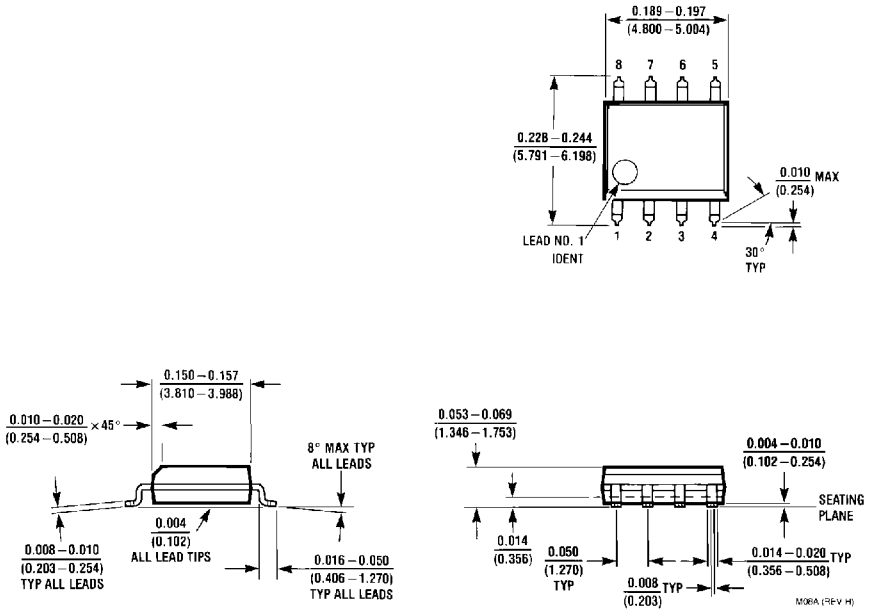


FIGURE 2. Propagation Delay and Transition Times

Physical Dimensions inches (millimeters) unless otherwise noted



8-Lead Small Outline Intergrated Circuit, JEDEC MS-012, 0.150" Narrow Body
Package Number M08A

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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