



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVpss	R _{DS(ON)} Max	I _D Max T _A = +25°C
100\/	23mΩ @ V _{GS} = 10V	7.4A
100V	30mΩ @ V _{GS} = 6V	6.5A

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

Features and Benefits

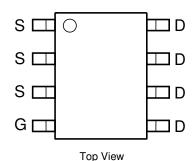
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

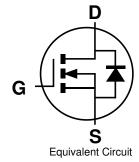
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.074 grams (Approximate)







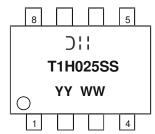
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT10H025SSS-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



);; = Manufacturer's Marking T1H025SS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 19 = 2019) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	100	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	T _A = +25°C T _A = +70°C	lo	7.4 5.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	45	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	ls	3.2	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		lsм	45	Α
Avalanche Current, L = 0.1mH		las	25	Α
Avalanche Energy, L = 0.1mH		Eas	31.25	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	RθJA	91	°C/W	
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	65	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	12.9	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	8.5	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (TA = +25°C, unless otherwise specified.)

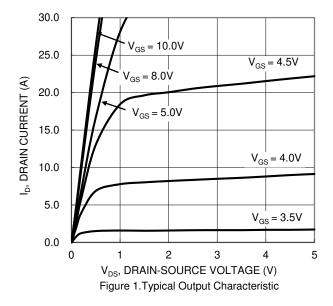
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	100	l	_	٧	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current	I _{DSS}			1	μA	$V_{DS} = 80V, V_{GS} = 0V$
Gate-Source Leakage	Igss		_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	D	_	17	23	mΩ	V _{GS} = 10V, I _D = 20A
Static Drain-Source On-nesistance	R _{DS(ON)}	_	22	30	11177	VGS = 6V, ID = 12.5A
Diode Forward Voltage	V _{SD}	_	0.9	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	-	1544	_		V _{DS} = 50V, V _{GS} = 0V, f = 1MHz
Output Capacitance	Coss	1	250	_	pF	
Reverse Transfer Capacitance	Crss	-	20.4	_		
Gate Resistance	Rg	1	1.26	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (VGS = 10V)	Qg	1	21.4	_		V 50V I- 00A
Total Gate Charge (VGS = 6V)	Q_g	-	13.4	_	nC	
Gate-Source Charge	Qgs	1	4.6	_	110	$V_{DD} = 50V, I_D = 20A$
Gate-Drain Charge	Q_{gd}		6.0	_		
Turn-On Delay Time	td(on)	_	8.2	_		$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 20A, R_{g} = 11\Omega$
Turn-On Rise Time	tr	_	11.2	_		
Turn-Off Delay Time	t _{D(OFF)}	_	27.5	_	ns	
Turn-Off Fall Time	tF	_	13.7	_		
Body Diode Reverse Recovery Time	t _{RR}	_	37.5	_	ns	L- 20A di/dt 100A/ug
Body Diode Reverse Recovery Charge	Qrr	_	50.9	_	nC	I _F = 20A, di/dt = 100A/μs

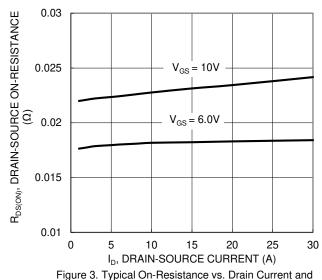
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect. Notes:

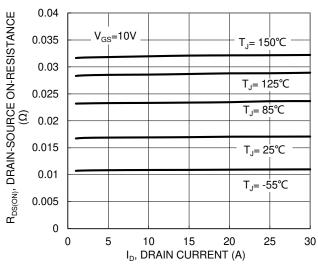
8. Guaranteed by design. Not subject to product testing.











Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature

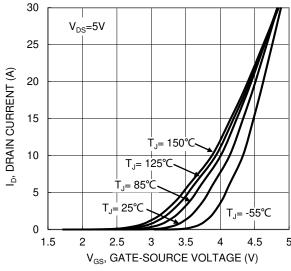
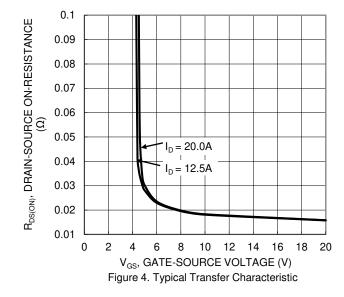


Figure 2. Typical Transfer Characteristic



2 R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE 1.8 $V_{GS} = 10V, I_D = 20A$ 1.6 (NORMALIZED) 1.4 1.2 $V_{GS} = 6.0V, I_D = 12.5A$ 8.0 0.6 0 25 50 100 75 125 150 T_J, JUNCTION TEMPERATURE (°C)

Figure 6. On-Resistance Variation with Temperature





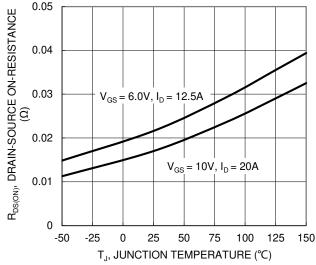
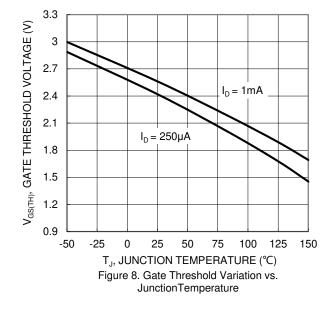
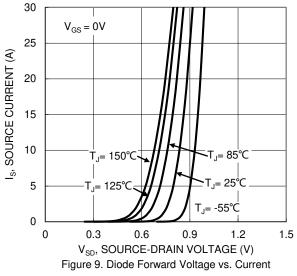
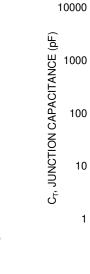
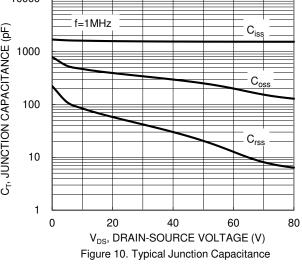


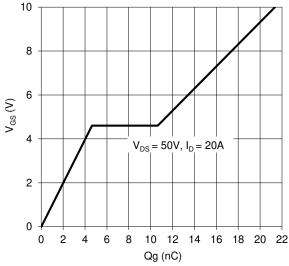
Figure 7. On-Resistance Variation with Temperature











ID, DRAIN CURRENT (A) T_C=25°C Single Pulse 0.01 DUT on 1*MRP board V_{GS}=10V 0.001 0.1 10 100 1000 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area Figure 11. Gate Charge

100

10

0.1

R_{DS(ON)} LIMITED

P_w=1ms



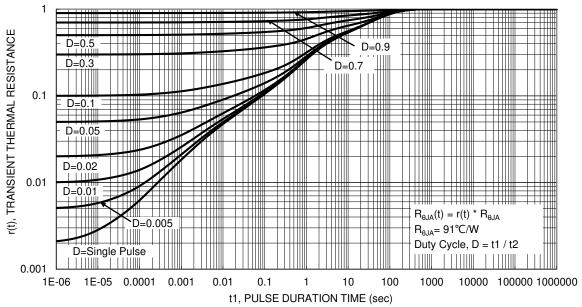


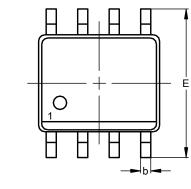
Figure 13. Transient Thermal Resistance

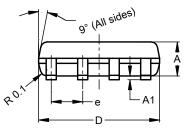


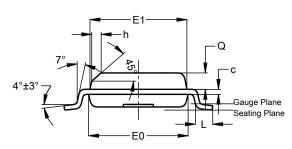
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







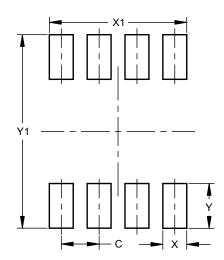


SO-8						
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A 1	0.10	0.20	0.15			
q	0.30	0.50	0.40			
C	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Е	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е			1.27			
h			0.35			
L	0.62	0.82	0.72			
Ø	0.60	0.70	0.65			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)			
С	1.27			
Х	0.802			
X1	4.612			
Υ	1.505			
V1	6.50			



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