# VS-150EBU04HF4

**Vishay Semiconductors** 

# Ultrafast Soft Recovery Diode, 150 A FRED Pt®



PowerTab<sup>®</sup>

PRODUCT SUMMARY					
Package	PowerTab <sup>®</sup>				
I <sub>F(AV)</sub>	150 A				
V <sub>R</sub>	400 V				
V <sub>F</sub> at I <sub>F</sub>	0.9 V				
t <sub>rr</sub> (typ.)	See recovery table				
T <sub>J</sub> max.	175 °C				
Diode variation	Single die				

### FEATURES

- Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- AEC-Q101 qualified
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

## **DESCRIPTION / APPLICATIONS**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V <sub>R</sub>		400	V	
Continuous forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 104 °C	150		
Single pulse forward current	I <sub>FSM</sub>	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	1500	А	
Maximum repetitive forward current	I <sub>FRM</sub>	Square wave, 20 kHz	300		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	BOL TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 200 μA	400	-	-	
		I <sub>F</sub> = 150 A	-	1.07	1.3	v
Forward voltage	V <sub>F</sub>	V <sub>F</sub> I <sub>F</sub> = 150 A, T <sub>J</sub> = 175 °C -	-	0.9	1.1	
		I <sub>F</sub> = 150 A, T <sub>J</sub> = 125 °C	-	0.96	1.17	
Reverse leakage current		V <sub>R</sub> = V <sub>R</sub> rated	-	-	50	μA
neverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	4	mA
Junction capacitance	CT	V <sub>R</sub> = 400 V - 100		100	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 3.5 - r		nH		

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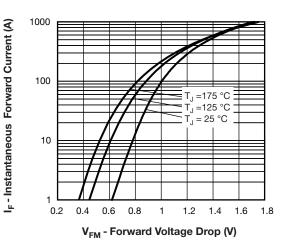
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DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Boyono rocovory timo	+	T <sub>J</sub> = 25 °C		-	93	-	20
Reverse recovery time t <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	172	-	ns	
Deels receiver a surrent	Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 150 A V <sub>R</sub> = 200 V dI <sub>F</sub> /dt = 200 A/μs	-	11	-	A
Peak recovery current		T <sub>J</sub> = 125 °C		-	20	-	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	490	-	
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C	]	-	1740	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.35	K/W
Thermal resistance, junction to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	r./ vv
Weight			-	-	5.02	g
weight			-	0.18	-	oz.
Mounting torque			1.2 (10)	-	2.4 (20)	N · m (lbf · in)
Marking device		Case style PowerTab <sup>®</sup>		150EE	3U04H	

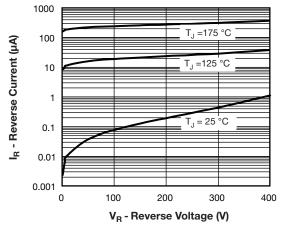
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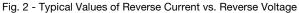
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Fig. 1 - Maximum Forward Voltage Drop Characteristics





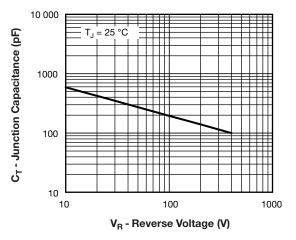


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

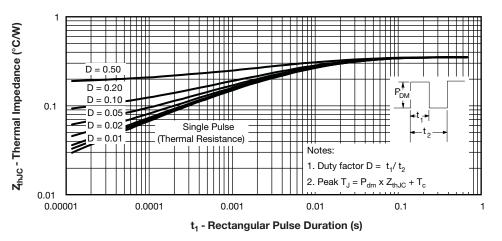
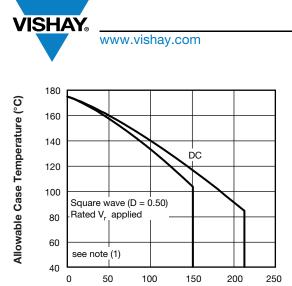


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics



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I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

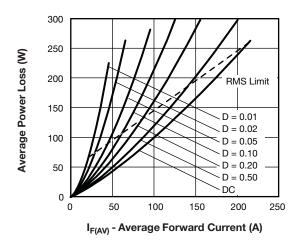


Fig. 6 - Forward Power Loss Characteristics

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = Rated  $V_R$

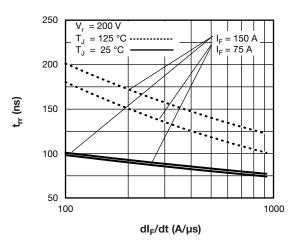
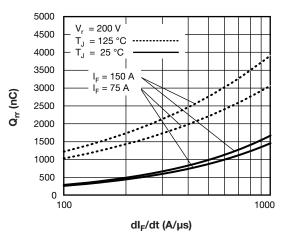
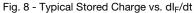


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt





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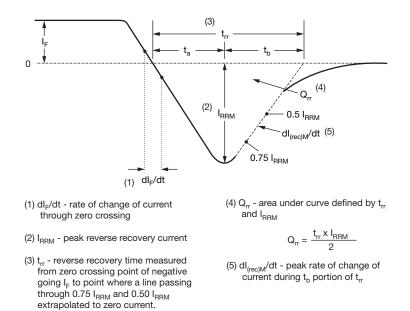


Fig. 9 - Reverse Recovery Waveform and Definitions

## **ORDERING INFORMATION TABLE**

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Device code	VS-	150	Е	В	U	04	н	F4
		2	3	4	5	6	7	8
	1 · 2 · 3 ·	Cur		niconduc ng (150 e				
	4 - 5 -		verTab <sup>®</sup> afast rec					
	6 - 7 - 8 -	H =	AEC-Q	ng (04 = 101 qua ntal digit	lified			

F4 = RoHS-compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-150EBU04HF4	25	375	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95240			
Part marking information	www.vishay.com/doc?95467			
Application note	www.vishay.com/doc?95179			
SPICE model	www.vishay.com/doc?95623			

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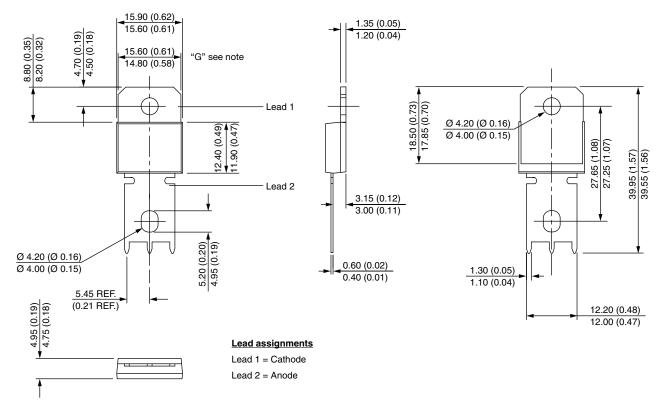
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## **DIMENSIONS** in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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