



ABS210

2A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

Product Summary (@TA = +25°C)

| V _{RRM} (V) | I _O (A) | V _F (V) | I _R (μA) |
|----------------------|--------------------|--------------------|---------------------|
| 1000 | 2 | 1.1 | 5 |

Features and Benefits

- Glass Passivated Die Construction
- Miniature Package Saves Space on PC Boards
- High Current Capability
- Ideal for SMT Manufacturing
- Low Forward Voltage Drop
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

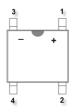
Suitable for AC to DC bridge full wave rectification for SMPS, LED lighting, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

Mechanical Data

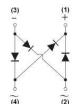
- Case: SOPA-4
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202. Method 208 ©3
- Polarity: As Marked on Body
- Weight: 0.10 grams (Approximate)



Top View



Pin Diagram



Internal Schematic

Ordering Information (Note 4)

| Part Number | Compliance | Case | Packaging | | |
|-------------|------------|--------|-------------------|--|--|
| ABS210-13 | Commercial | SOPA-4 | 5,000/Tape & Reel | | |

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Y = Last Digit of Year (ex: 7 = 2017) M = See Month/Code Table Below

D = Day 1 to 9 = 1 to 9; Day 10 to 31 = A to V

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



$\hline \textbf{Maximum Ratings} \ (@T_A = +25^{\circ}C, \, \text{unless otherwise specified.})$

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|--|--|-------|------------------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 1000 | ٧ |
| RMS Reverse Voltage | V _{R(RMS)} | 700 | V |
| Average Rectified Output Current (Note 6) @ T _A = +50°C | lo | 2.0 | Α |
| Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I _{FSM} | 60 | Α |
| I^2 t Rating for Fusing (1ms < t < 8.3ms) | l ² t | 14.9 | A ² S |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Typical Thermal Resistance, Junction to Ambient (Note 6) (Per Element) | $R_{\theta JA}$ | 62.5 | °C/W |
| Typical Thermal Resistance, Junction to Lead (Per Element) | R _{0JL} | 25 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

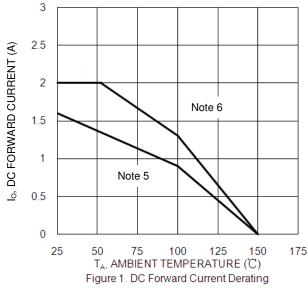
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

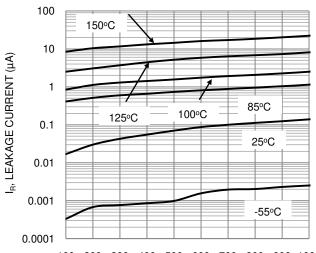
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|--|----------------|-------|-----|----------|------|---|
| Reverse Breakdown Voltage (Note 7) | $V_{(BR)R}$ | 1,000 | _ | _ | V | $I_R = 5\mu A$ |
| Forward Voltage (Per Element) | V _F | _ | _ | 1.1 | V | $I_F = 2A, T_A = +25^{\circ}C$ |
| Leakage Current (Note 7) (Per Element) | I _R | | _ | 5 500 | μΑ | $V_R = 1,000V, T_A = +25^{\circ}C$ $V_R = 1,000V, T_A = +125^{\circ}C$ |
| Total Capacitance (Per Element) | Ст | _ | 17 | _ | pF | $V_R = 4V$, $f = 1.0MHz$ |

Notes:

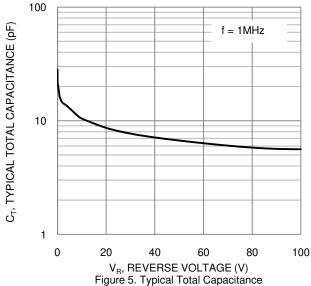
- 5. Device mounted on FR-4 substrate, 1"*1", 2oz, single-sided, PC boards with 0.1"*0.15" copper pad.
 6. Device mounted on FR-4 substrate, 1"*1", 2oz, single-sided, PC boards with 0.56"*0.73" copper pad.
 7. Short duration pulse test used to minimize self-heating effect.

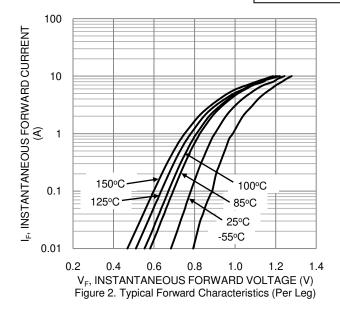






100 200 300 400 500 600 700 800 900 1000 V_R, INSTANTANEOUS REVERSE VOLTAGE (V) Figure 3. Typical Reverse Characteristics (Per Leg)





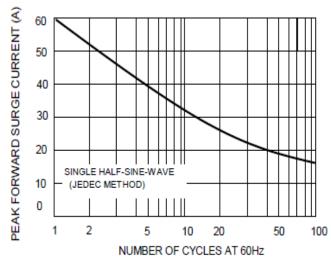


Figure 4. Maximum Non-Repetitive Surge Current

March 2017

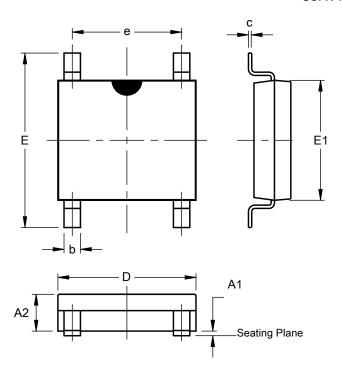
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOPA-4

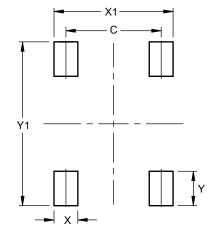


| SOPA-4 | | | | | | |
|----------------------|------|------|-----|--|--|--|
| Dim | Min | Max | Тур | | | |
| A1 | | 0.20 | | | | |
| A2 | 1.20 | 1.50 | | | | |
| b | 0.50 | 0.70 | | | | |
| С | 0.15 | 0.25 | | | | |
| D | 4.80 | 5.30 | | | | |
| Е | 6.00 | 6.80 | | | | |
| E1 | 4.20 | 4.60 | | | | |
| е | 3.80 | 4.20 | | | | |
| All Dimensions in mm | | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOPA-4



| Dimensions | Value (in mm) | | |
|------------|------------------|--|--|
| С | 4.00 | | |
| Х | 1.00 | | |
| X1 | 5.00 | | |
| Υ | 1.45 | | |
| Y1 | 6.90 | | |



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