#### Transistors

# 2SC6037J

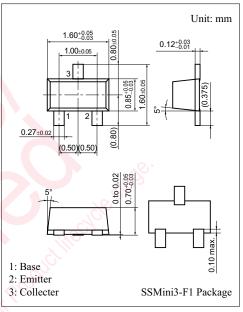
### Silicon NPN epitaxial planar type

#### For general amplification Complementary to 2SA2161J

#### Features

- $\bullet$  Low collector-emitter saturation voltage  $V_{\mbox{CE(sat)}}$
- SS-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

Absolute Maximum Ratings $T_a = 25^{\circ}C$							
Symbol	Rating	Unit					
V <sub>CBO</sub>	15	V					
V <sub>CEO</sub>	12	V					
V <sub>EBO</sub>	5	V					
I <sub>C</sub>	500	mA					
I <sub>CP</sub>	1	А					
P <sub>C</sub>	125	mW					
T <sub>j</sub>	125	°C					
T <sub>stg</sub>	-55 to +125	°C					
	Symbol   V <sub>CBO</sub> V <sub>CEO</sub> V <sub>EBO</sub> I <sub>C</sub> I <sub>CP</sub> P <sub>C</sub> T <sub>j</sub>	$\begin{array}{ c c c c } \hline Symbol & Rating \\ \hline V_{CBO} & 15 \\ \hline V_{CEO} & 12 \\ \hline V_{EBO} & 5 \\ \hline I_C & 500 \\ \hline I_{CP} & 1 \\ \hline P_C & 125 \\ \hline T_j & 125 \\ \hline \end{array}$					



Marking Symbol : 4U

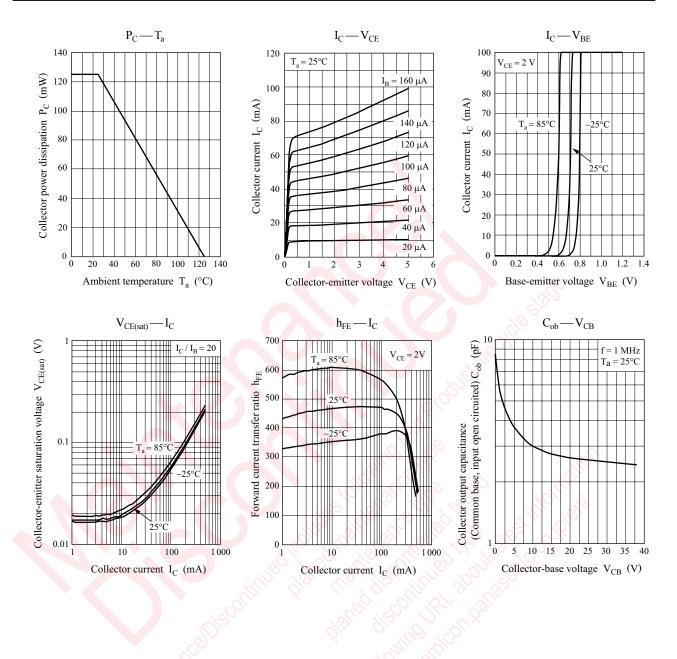
#### Electrical Characteristics $T_a = \frac{25 \text{°C} \pm 3 \text{°C}}{25 \text{°C} \pm 3 \text{°C}}$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_{\rm C} = 10 \ \mu {\rm A}, I_{\rm E} = 0$	15			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	12			V
Emitter-base voltage (Collector open)	V <sub>EBO</sub>	$I_{\rm E} = 10 \ \mu A, I_{\rm C} = 0$	65			V
Collector-base cutoff current (Emitter open)	I <sub>CBO</sub>	$V_{CB} = 10 \text{ V}, I_E = 0$			0.1	μΑ
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = 2 V, I_C = 10 mA$	270		680	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{\rm C} = 200 \text{ mA}, I_{\rm B} = 10 \text{ mA}$			250	mV
Transition frequency	f <sub>T</sub>	$V_{CB} = 2 \text{ V}, I_E = -10 \text{ mA}, f = 200 \text{ MHz}$		200		MHz
Collector output capacitance (Common base, input open circuited)	C <sub>ob</sub>	$V_{CB} = 10 V, f = 1 MHz$		4.5		pF

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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## **Panasonic**



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