AZ DISPLAYS

SPECIFICATIONS FOR
LIQUID CRYSTAL DISPLAY

CUSTOMER APPROVAL

※ PART NO. : ATM0680L2-CT (AZ DISPLAYS)TFT VER1.1

APPROVAL		COMPANY CHOP	
CUSTOMER COMMENTS			

AZ DISPLAYS ENGINEERING APPROVAL		
DESIGNED BY	CHECKED BY	APPROVED BY
GZC	ZZK	GuZH

REVISION RECORD

REVISION	REVISION DATE	PAGE	CONTENTS
VER1.0	**2019-03-22**		**FIRST ISSUE**
VER1.1	**2019-08-30**	**3/18**	**CHANGE SPEC FORMAT**

※ **CONTENTS**

1. GENERAL SPECIFICATIONS

Item	Specification	Remark
1. LCD size	6.86 inch(Diagonal)	
2. Driver element	a-Si TFT active matrix	
3. Resolution	480x(RGB)x1280	
4. Display mode	Normally Black, IPS, Transmissive,	
5. Dot pitch (W*H)	0.04182mm(W) x 0.12546mm(H)	
6. Pixel pitch(W*H)	0.12546mm(W) x 0.12546mm(H)	
7. Active area(W*H)	60.22mm(W) x 160.59mm(H)	
8. Module size (W*H)	66.6mm(W) x 181mm(H) x 6.6mm(D)	Note 1
9. Surface treatment	Clear	
10. Color arrangement	RGB-stripe	
11. Color	16.7M	
12. Viewing angle (L/R/T/B)	80/80/80/80	
13. Interface	MIPI 4-lane	
14. LCD controller	EK79030 or compatible	
15. LCM brightness	380 cd/m2 Typ.	
16. Backlight driving condition	120mA @8.7V	
17. Touch panel	Capacitive	
18. Touch controller	FT7311	
19. Operation temperature	-20~70 ℃	
20. Weight	TAB	
21. RoHS	RoHS compliant	

Note 1: **Please refer to mechanical drawing.**

2. PIN ASSIGNMENT

TFT LCD Panel Driving Section

Pin No.	Symbol	Function	Level	Note
1	GND	Ground	P	
2	D0P	MIPI-DSI data Lane 0 positive-end input/output pin	I	
3	D0N	MIPI-DSI data Lane 0 negative-end input/output pin	I	
4	GND	Ground	P	
5	D1P	MIPI-DSI data Lane 1 positive-end input/output pin	I	
6	D1N	MIPI-DSI data Lane 1 negative-end input/output pin	I	
7	GND	Ground	P	
8	CLKP	MIPI-DSI clock Lane positive-end input pin	I	
9	CLKN	MIPI-DSI clock Lane negative-end input pin	I	
10	GND	Ground	P	
11	D2P	MIPI-DSI data Lane 2 positive-end input/output pin	I	
12	D2N	MIPI-DSI data Lane 2 negative-end input/output pin	I	
13	GND	Ground	P	
14	D3P	MIPI-DSI data Lane 3 positive-end input/output pin	I	
15	D3N	MIPI-DSI data Lane 3 negative-end input/output pin	I	
16~17	GND	Ground	P	
18~19	VCC_1V8	I/O power supply(1.8V)	P	
20~22	NC	No connection		
23	BIST	Normal Operation/BIST pattern select		
24	RST	Reset signal	I	
25	STBYB	Standby mode control	P	
26	NC	No connection		
27	GND	Ground	P	
28~29	K	Power for LED backlight(Cathode)	P	
30	GND	Ground	P	
31	NC	No connection		
32~33	GND	Ground	P	
34	NC	No connection		
35~36	A	Power for LED backlight(Anode)	P	
37	GND	Ground	P	
38~39	VDD	Power supply	P	
40	NC	No connection		

I: input, O: output, P: Power

Tout Panel Section

Recommended connector: Hirose FH12-6S-1SH(55) or compatible
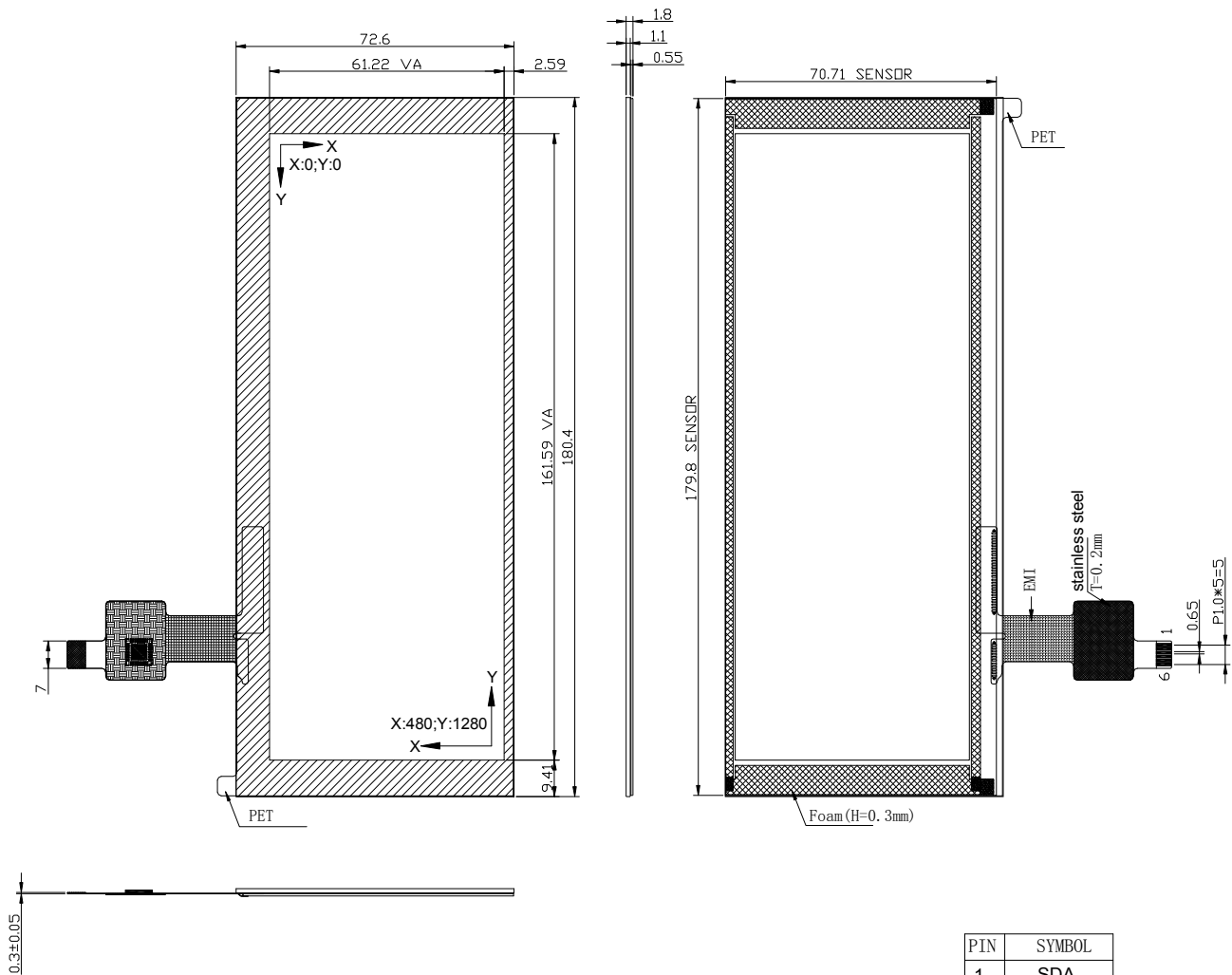
Note:
1. Structure:COVERLENS+FPC+ITO GLASS
2. IC:FT7311
3. VDD:2.8V-3.3V
4. VIO :2.8V-3.3V
5. Interface:IIC, 5-point touch
6. General tolerance:±0.3

PIN	SYMBOL
1	SDA
2	SCL
3	/RST
4	INT
5	VDD
6	GND

3. Operating Specification

3.1.1 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power Voltage	V_{DD}	-0.3	3.6	V	
I/O Power Voltage	V_{CC}	-0.3	3.0	V	
Supply Voltage	VSP	4.5	6.0	V	
Supply Voltage	VSN	-4.5	-6.0	V	
Operation Temperature	T_{op}	-20	70	℃	
Storage Temperature	T_{ST}	-30	80	℃	
LED Reverse Voltage	V_R	-	5.0	V	Each LED Note 2
LED Forward Current	I_F		50	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 30mA

3.1.2 Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Logis Power Voltage	V_{DD}	3.0	3.3	3.6	V	Note 1
I/O Power Voltage	IOVcc	1.6	1.8	2.0	V	
Input Logic High Voltage	V_{IH}	$0.7DV_{DD}$	--	DV_{DD}	V	Note 2
Input Logic Low Voltage	V_{IL}	0	--	$0.3DV_{DD}$	V	Note 2

Note 1: V_{DD} setting should match the signals output voltage of customer's system board.
**Note 2**: DCLK,HS,VS,RESET,DE,R0~R5,G0~G5,B0~B5,.

3.1.3 Backlight driving conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V_L	8.0	8.7	10.0	V	Note 1
Current for LED Backlight	I_L=30mA*4	--	120	--	mA	
LED life time	--	20000	--	--	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L =120mA.
Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and I_L =120mA.

3.2 Power on/off sequence
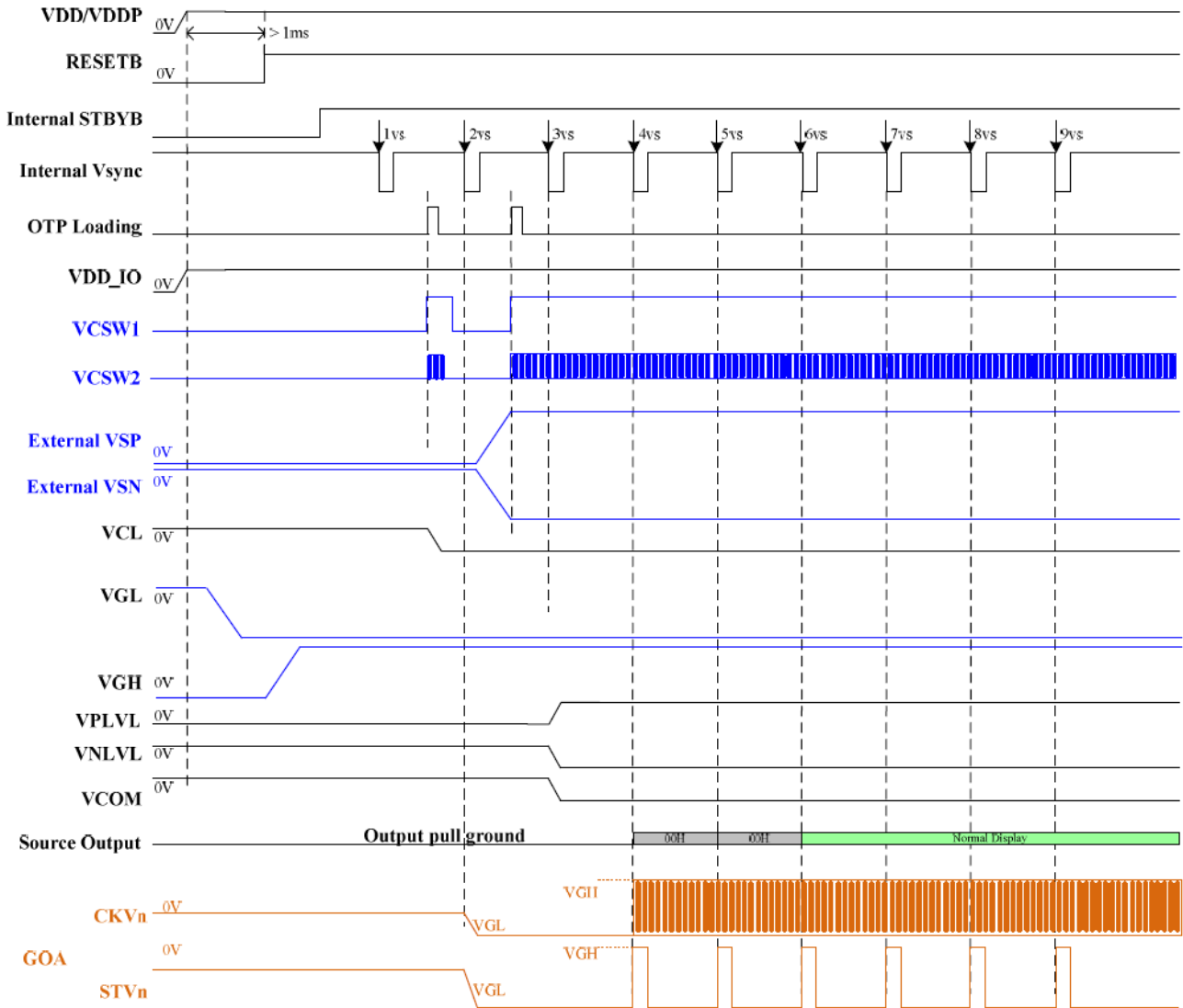
3.3 Inpot Signal Timing

3.3.1 DC electrical characteristics

(Test condition: VCI=1.6~3.6V, TA=-20℃~+85℃,VSS=VSSA=0V)

| Parameter		Symbol	Spec.			Unit	Note
			Min.	Typ.	Max.		
VDDIO Input high level voltage		VIH	0.8 x VDDIO		VDDIO	V	
VDDIO input low level voltage		VIL	VSS		0.2 x VDDIO	V	
Input Leakage Current		Ileak	(-1)		(+1)	µA	
VGL_REG2 output voltage		VGL_REG2		TBD		V	
VGMP output voltage		VGMP		TBD		V	
VGMN output voltage		VGMN		TBD		V	
VCI1 output voltage		VCI1		TBD		V	
VGL output voltage		VGL_O	-16		-6	V	
VGH output voltage		VGH_O	8		19	V	
VCL output voltage		VCL	-2.1	-2.4	-3	V	
VOM output voltage		VCOM	-2.75	-1.48	-0.2	V	
Input terminal resistance		ZID		100		ohm	
Source output level deviation	Graycode = 0 ~ 14 Graycode = 241 ~ 255			TBD		mV	
	Graycode = 15 ~ 31 Graycode = 208 ~ 240			TBD		mV	
	Graycode = 32 ~ 207			TBD		mV	
Source output offset deviation	Graycode = 0 ~ 14 Graycode = 241 ~ 255	-		TBD		mV	
	Graycode = 15 ~ 31 Graycode = 208 ~ 240	-		TBD		mV	
	Graycode = 32 ~ 207	-		TBD		mV	
Current consumption	Analog Operating	IAOP		TBD		mA	
	Analog Stand-by	IAST		TBD		mA	
Rush current		Ivddpeak		TBD		mA	
VOTP operation current		Ivpp		TBD		mA	
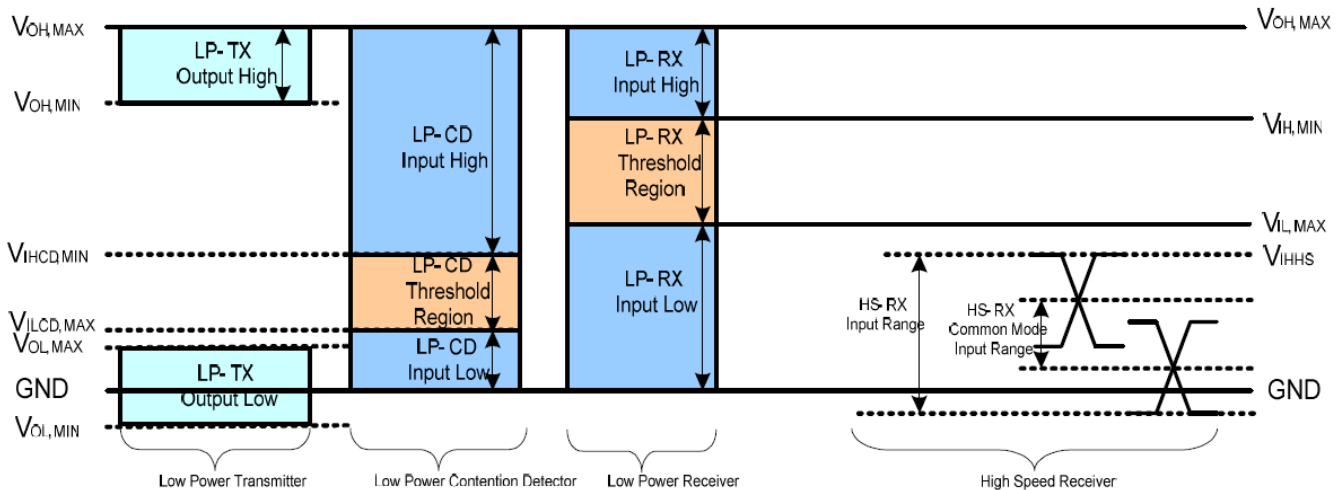
3.3.2 AC CHARACTERISTICS
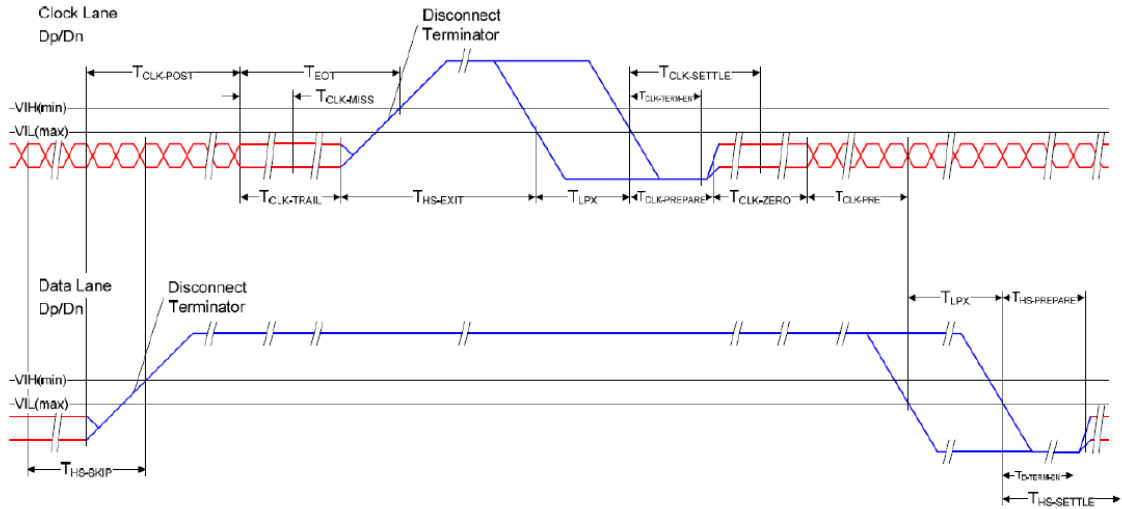
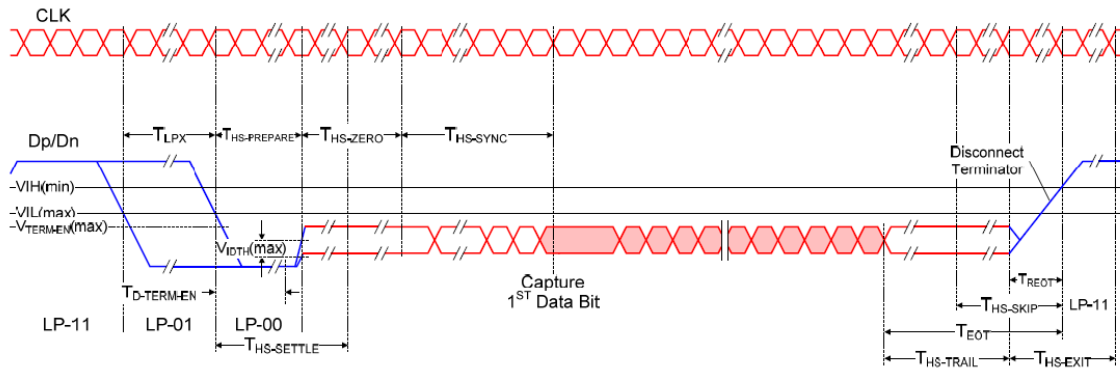Figure 13.1: Switching the clock lane between clock transmission and low-power mode

Figure 13.2: Timing of high-speed data transmission in bursts

3.3.3 MIPI Input Timing Table

For 480RGBx1280

:	Symbol	Value			Unit
		Min.	Typ.	Max.	
MIPI (4 Lane) @Frame rate=60Hz			386		Mbps
Lane) @Frame rate=60Hz			515		Mbps
DCLK frequency @Frame rate=60Hz	F$_{DCLK}$		64.4		MHz
HSYNC period time	T$_H$		824		DCLK
Horizontal display area	T$_{HD}$		480		DCLK
HSYNC pulse width	T$_{HPW}$		24	-	DCLK
HSYNC back porch	T$_{HBP}$		160	-	DCLK
HSYNC front porch	T$_{FBP}$		160	-	DCLK
VSYNC period time	T$_V$		1304		H
Vertical display area	T$_{VD}$		1280		H
VSYNC pulse width	T$_{VPW}$		2	-	H
VSYNC back porch	T$_{VBP}$		10	-	H
VSYNC front porch	T$_{VFP}$		12	-	H

4.0 OPTICAL SPECIFICATIONS

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing Angle (CR≥10)	θ_L	Φ=180°(9 O'CLOCK)	70	80	--	degree	Note 1
	θ_R	Φ=0°(3 O'CLOCK)	70	80	--		
	θ_T	Φ=90°(12 O'CLOCK)	70	80	--		
	θ_B	Φ=270°(6 O'CLOCK)	70	80	--		
Response Time	$T_{ON} + T_{OFF}$		--	25	50	msec	Note 3
Contrast Ratio	CR		650	800	--	--	Note 4
Color Chromaticity	W_X	Normal Θ=Φ=0°	0.26	0.31	0.36	--	Note 2
	W_Y		0.28	0.33	0.38	--	Note 5 Note 6
Luminance	L		330	380	--	cd/m^2	Note 6
Luminance Uniformity	YU		75	80	--	%	Note 7

Test Conditions:
1. IL=120mA (Backlight current), the ambient temperature is 25℃.
2. The test systems refer to Note 2.

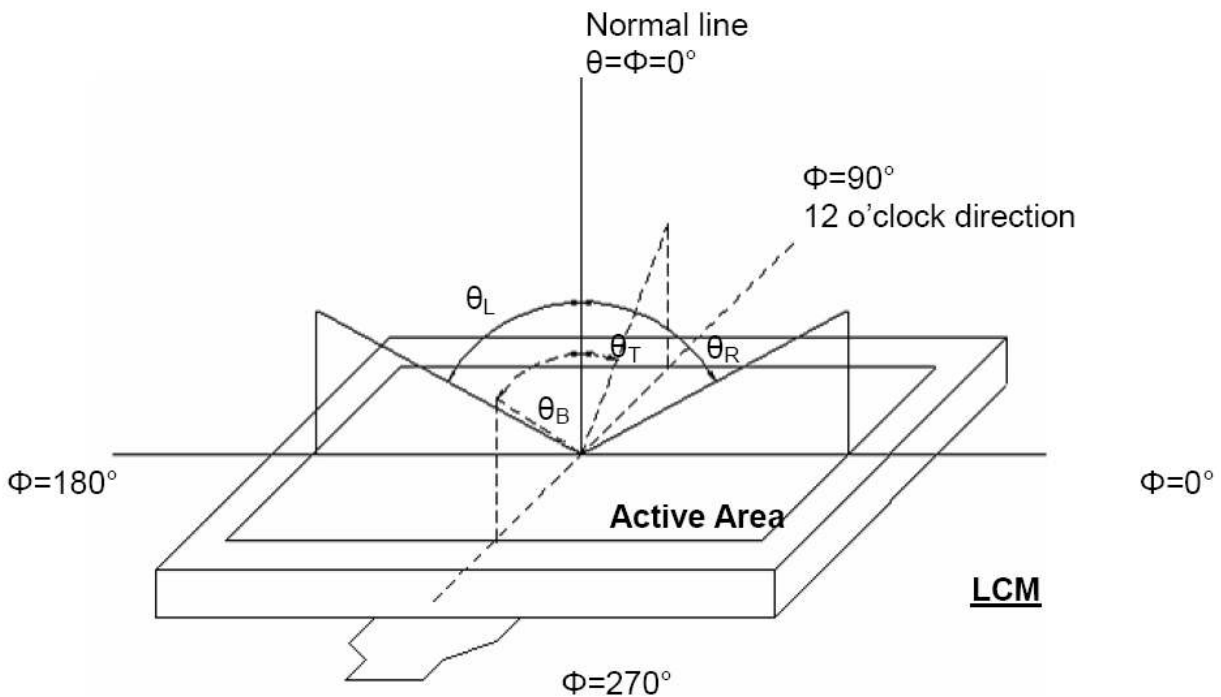**Note 1**: Definition of viewing angle range

Figure 4.1 Definition of viewing angle.

Note 2: Definition of optical measurement system.
The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON
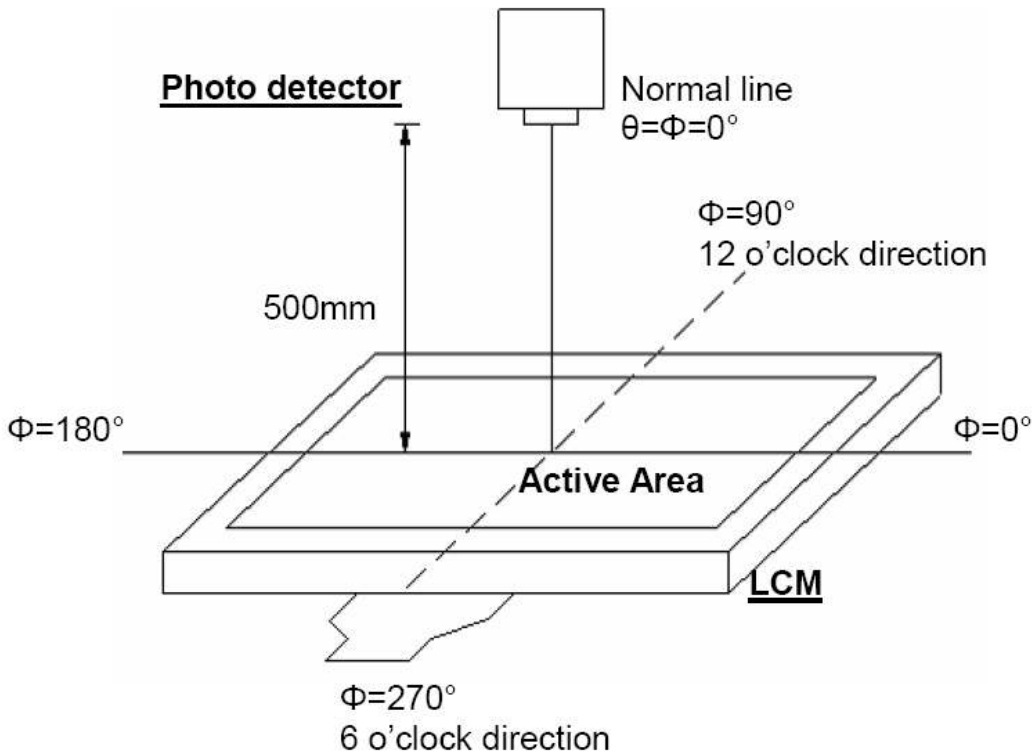
Figure 4.2 Optical measurement system setup

Note 3: Definition of Response time
The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.

Figure 4.3 Definition of response.

Note 4: Definition of contrast ratio

$$Contrast\ ratio(CR) = \frac{Luminance\ measured\ when\ LCD\ on\ the\ "white"\ state}{Luminance\ measured\ when\ LCD\ on\ the\ "black"\ state}$$

Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.
Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel.
Note 7: Definition of Luminance Uniformity
Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$Luminance\ Uniformity\ (Yu) = \frac{B_{min}}{B_{max}}$$

L-------Active area length　　　W----- Active area width

Figure 4.3 Definition of measuring points.

Bmax: The measured maximum luminance of all measurement position.
Bmin: The measured minimum luminance of all measurement position.

5. RELIABILITY TEST

Item	Test Condition Item	Remark
High temperature storage	Ta= 80 ºC 96hrs	Note 1 Note 4
Low temperature storage	Ta=-30 ºC 96hrs	Note 1 Note 4
High temperature operation	Ts= 70 ºC 96hrs	Note 2 Note 4
Low temperature operation	Ts=-20 ºC 96hrs	Note 1 Note 4
High temperature/High humidity operation	90% RH 60ºC 96hrs	Note 4
Thermal Shock	-30℃/30 min ~ +80℃/30 min for a total 50 cycles, Start with cold temperature and end with high temperature.	Note 4
Vibration test	Freq:10~55~10Hz Amplitude:1.5mm 2 hours for each direction of X,Y,Z (6 hours for total)	
Mechanical shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package vibration test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package drop test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro static discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the
display function. After the reliability test, the product only guarantees operation,
but don't guarantee all of the cosmetic specification.
Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours
 at room temperature.

6. PRECAUTION FOR USING LCM

1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
8. Mechanical Considerations
    a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
    b) Do not tamper in any way with the tabs on the metal frame.
    c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
    d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
    e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
    f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
9. Static Electricity
    a) Operator
Wear the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
    b) Equipment
There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: 1×10^{8} ohm).
Only properly grounded soldering irons should be used.
If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.
The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.
    c) Floor
Floor is the important part to drain static electricity, which is generated by operators or equipment.
There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: 1×10^{8} ohm).
    d) Humidity
Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH.
    e) Transportation/storage
The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.
The modules should be kept in antistatic bags or other containers resistant to static for storage.
    f) Soldering
Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage.
Soldering temperature : 280° C $\pm$ 10° C
Soldering time: 3 to 4 sec.
Use eutectic solder with resin flux fill.
If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.
   g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge.

10. Operation
   a) Driving voltage should be kept within specified range; excess voltage shortens display life.
   b) Response time increases with decrease in temperature.
   c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
   d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.

12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.

13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.

14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.

15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.

The brightness of LCD module may be affected by the routing of CCFL cables due to leakage to the chassis through coupling effect. The inverter circuit needs to be designed taking the level of leakage current into consideration. Thorough evaluation is needed for LCD module and inverter built into its host equipment to ensure specified brightness.
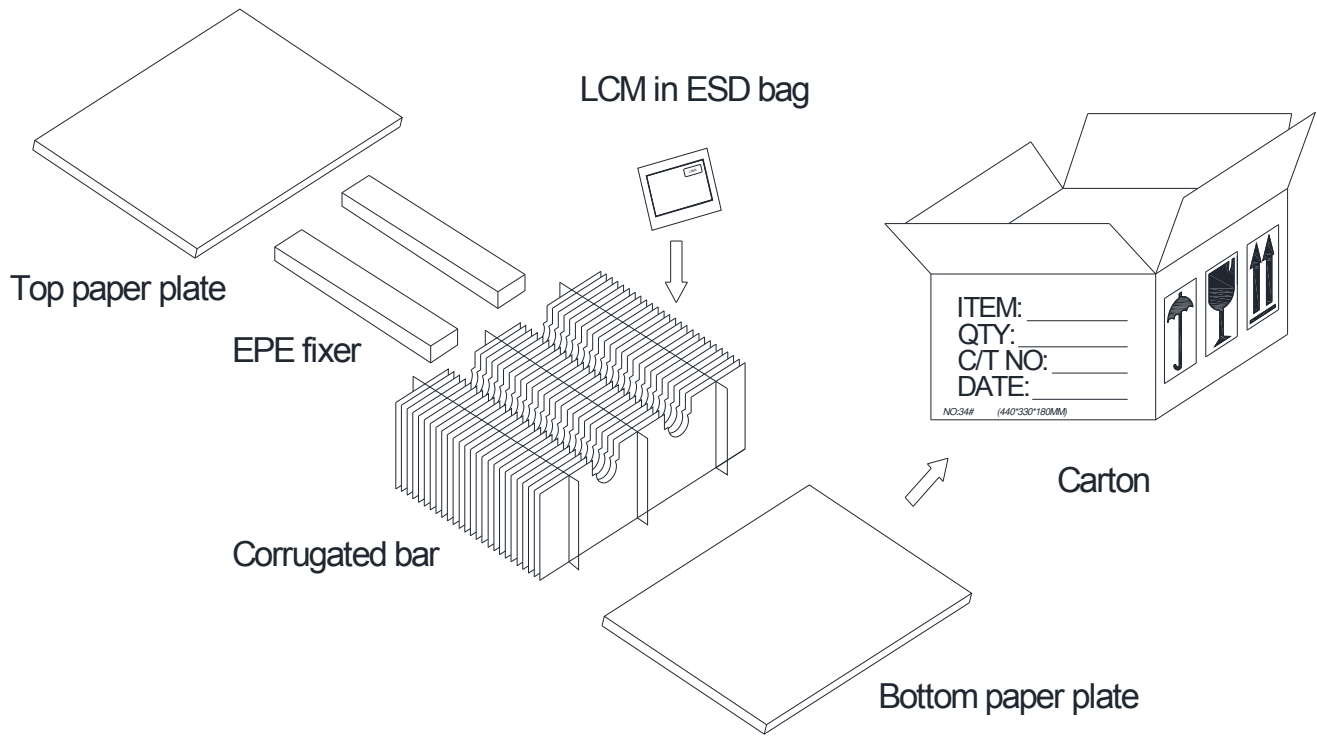
7. MECHANICAL DRAWING
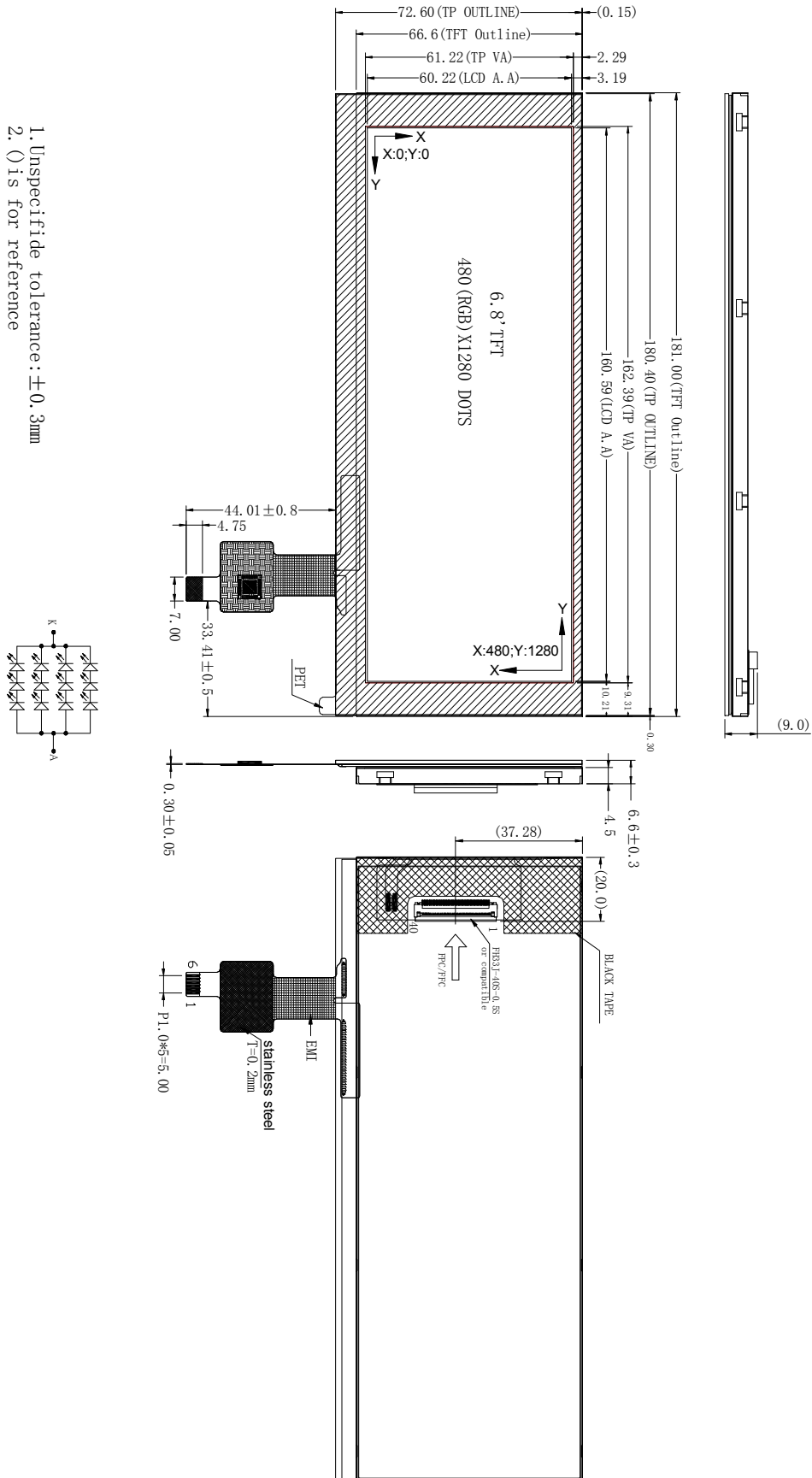
1.Unspecifide tolerance:±0.3mm
2.（）is for reference

8. INITIALIZATION CODE：

PCLK:58.2MHZ
H BLANK PORCH:160
H FRONT PORCH:160
H PULSE WIDTH:24
V BLANK PORCH:10
V FRONT PORCH:12
V PULSE WIDTH:2
MIPI COMMAND: //***************//

```
REGW(0XCD,0XAA);
REGW(0X65,0X08);
REGW(0X3A,0X14);
REGW(0X32,0X00);
REGW(0X36,0X02);
REGW(0X67,0X82);
REGW(0X69,0X20);
REGW(0X6D,0X01);
REGW(0X68,0X00);
REGW(0X53,0X19,0X17,0X15,0X12,0X12,0X12,0X13,0X15,0X15,0X10,0X0C,0X0A,0X0A,0X0C,0X0B,0X0C,0X09,0X07,0X06);
REGW(0X54,0X19,0X16,0X14,0X11,0X11,0X11,0X13,0X15,0X15,0X10,0X0B,0X09,0X09,0X0B,0X0B,0X0C,0X09,0X07,0X06);
REGW(0X29,0X10);
REGW(0X2A,0X0C);
REGW(0X6C,0X81);
REGW(0X55,0X00,0X0F,0X00,0X0F,0X00,0X0F,0X00,0X0F);
REGW(0X56,0X00,0X0F,0X00,0X0F,0X00,0X0F,0X00,0X0F,0X00,0X0F,0X00,0X0F,0X00,0X0F,0X00,0X0F);
REGW(0X57,0X00,0X00,0X00,0X00);
REGW(0X30,0X00);
REGW(0X39,0X11);
REGW(0X33,0X08);
REGW(0X35,0X25);
REGW(0X4F,0X3D);
REGW(0X4E,0X35);
REGW(0X41,0X35);
REGW(0X73,0X30);
REGW(0X74,0X10);
REGW(0X76,0X40);
REGW(0X77,0X00);
REGW(0X28,0X31);
REGW(0X7C,0X80);
REGW(0X2E,0X04);
REGW(0X4C,0X80);
 REGW(0X47,0X16);
REGW(0X48,0X6A);
REGW(0X50,0XC0);
REGW(0X78,0X6E);
REGW(0X2D,0X31);
REGW(0X63,0X04);
REGW(0X4D,0X00);
REGW(0X11);
DELAY(120);
```

9. PACKAGE DRAWING

LCM in ESD bag

Top paper plate

EPE fixer

Corrugated bar

Bottom paper plate

ITEM: _____
QTY: _____
C/T NO: _____
DATE: _____
NO:34#   (440*330*180MM)

Carton

Carton size	44x34x18	cm
Quantity per carton	40	pcs

10. INSPECTION SPECIFICATION

1. SCOPE SPECIFICATIONS CONTAIN
    1.1 DISPLAY QUALITY EVALUATION
    1.2 MECHANICS SPECIFICATION

2. SAMPLING PLAN
    UNLESS THERE IS OTHER AGREEMENT, THE SAMPLING PLAN FOR INCOMING INSPECTION SHALL FOLLOW MIL-STD-105E.
    2.1 LOT SIZE: QUANTITY PER SHIPMENT AS ONE LOT (DIFFERENT MODEL AS DIFFERENT LOT).
    2.2 SAMPLING TYPE: NORMAL INSPECTION, SINGLE SAMPLING.
    2.3 SAMPLING LEVEL: LEVEL II.
    2.4 AQL: ACCEPTABLE QUALITY LEVEL
            MAJOR DEFECT: AQL=0.65
            MINOR DEFECT: AQL=1.0

3. PANEL INSPECTION CONDITION
    3.1 ENVIRONMENT:
        ROOM TEMPERATURE: 25±5°C.
        HUMIDITY: 65±5% RH.
        ILLUMINATION: 300 ~ 700 LUX.
    3.2 INSPECTION DISTANCE:
        35±5 CM
    3.3 INSPECTION ANGLE:
        THE VISION OF INSPECTOR SHOULD BE PERPENDICULAR TO THE SURFACE OF THE MODULE.
    3.4 INSPECTION TIME：
        PERCEPTIBILITY TEST TIME: 20 SECONDS MAX.

4. DISPLAY QUALITY
    4.1 FUNCTION RELATED:
        THE FUNCTION DEFECTS OF LINE DEFECT, ABNORMAL DISPLAY, AND NO DISPLAY ARE CONSIDERED MAJOR DEFECTS.
    4.2 BRIGHT/DARK DOTS:

Defect Type	Specification	Major	Minor
Bright Dots	N≤ 2		●
Dark Dots	N≤ 3		●
Total Bright and Dark Dots	N≤ 4		●

Note: 1:
    The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.
    Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
    The bright dot defect must be visible through 2% ND filter
    Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern.
    4.3 Pixel Definition:

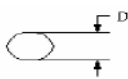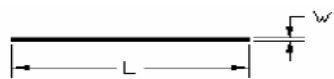|										
R	G	B	R	G	B	R	G	B		Dot Defect
R	G	B	R	G	B	R	G	B		Adjacent Dot Defect
R	G	B	R	G	B	R	G	B		Cluster

  Note 1:
    If pixel or partial sub-pixel defects exceed 50% of the affected pixel or sub-pixel area, it shall be considered as1 defect.
  Note 2:
    There should be no distinct non-uniformity visible through 2% ND Filter within 2 sec inspection times.

4.4Visual Inspection specifications:

Defect Type		Specification Size	Count(N)	Major	Minor
Dot Shape (Particle、Scratch and Bubbles in display area)		D ≤0.25 mm	Ignored		●
		0.25mm < D ≤ 0.5mm	N ≤ 3		
		D > 0.5mm	N=0		
Newton Ring (Only for Touch panel)		D≤70mm	N≤4		●
		D>70mm	N=0		
TSP Fish Eyes (Only for Touch panel) (Bubble/Dent)		0.1mm<D≤0.2mm	N≤4		●
		0.2mm<D≤0.3mm	N≤3		
		0.3<D≤0.4	N≤2		
Line Shape (Particles、Scratch、Lint and Bubbles in display area)		W ≤ 0.01 mm	Ignored		●
		0.01mm< W ≤ 0.05mm and L ≤ 3mm	N ≤ 3		
		W > 0.05mm or L > 3 mm	N=0		
Bubble in cell (active area)		It should be found by eyes			●
Bezel	Scratch	No harm			●
	Dirt				●
	Wrap	No harm			●
	Sunken	No harm			●
Label	No label	No			●
	Inverted label				●
	Broken				●
	Dirt	Word can be read.			●
	Not clear	No			●
	Word out of shape				●
	Mistake	No			●
	Position	Be attached on right position			●
Screw	Not enough	No			●
	Limp	No			●

Connector	Connection status	No bend on pins and damage		●
FPC/FFC	Broken	No		●

Note: Extraneous substance and scratch not affecting the display of image, for instance, extraneous substance under polarizer film but outside the display area, or scratch on metal bezel and backlight module or polarizer film outside the display area, shall not be considered as defective or non-conforming.