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MOSFET - Power, Single N-Channel, D²PAK7

100 V, 4.1 mΩ, 203 A

NTBGS004N10G

Features

- Low R_{DS(on)}
- High Current Capability
- Wide SOA
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

• Hot Swap in 48 V Systems

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

			-		
Parameter			Symbol	Value	Unit
Drain-to-Source Voltag	rain-to-Source Voltage		V _{DSS}	100	V
Gate-to-Source Voltage	e-to-Source Voltage		V _{GS}	±20	V
$\begin{array}{l} \text{Continuous Drain} \\ \text{Current } \textbf{R}_{\theta JC} \\ \text{(Note 2)} \end{array}$	Steady State	T _C = 25°C	Ι _D	203	A
Power Dissipation $R_{\theta JC}$ (Note 2)	olulo		P _D	340	W
Continuous Drain Current R _{θJA} (Notes 1, 2)	Steady State	T _A = 25°C	۱ _D	21	A
Power Dissipation $R_{\theta JA}$ (Notes 1, 2)	Siale	State	PD	3.7	W
Pulsed Drain Current	$T_A = 25^{\circ}C$, $t_p = 10 \ \mu s$		I _{DM}	2983	А
Operating Junction and	Storage T	emperature	T _J , T _{stg}	–55 to + 175	°C
Source Current (Body Diode)			IS	283	А
Single Pulse Drain-to-Source Avalanche Energy ($I_L = 106 A_{pk}$, L = 0.1 mH)			E _{AS}	561	mJ
Lead Temperature for S (1/8" from case for 10 s)		urposes	ΤL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface-mounted on FR4 board using a 1 in², 1 oz. Cu pad.

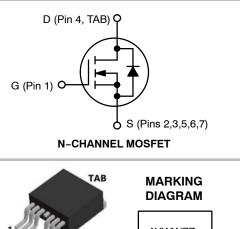
The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

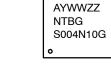


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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
100 V	4.1 mΩ @ 10 V	203 A





A = Assembly Location

Y = Year

D²PAK7 CASE 418AY

WW = Work Week

ZZ = Assembly Lot Code

NTBGS004N10G = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping [†]
NTBGS004N10G	D ² PAK7 (Pb-Free)	800 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 2)	$R_{ ext{ heta}JC}$	0.44	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{ ext{ heta}JA}$	40	

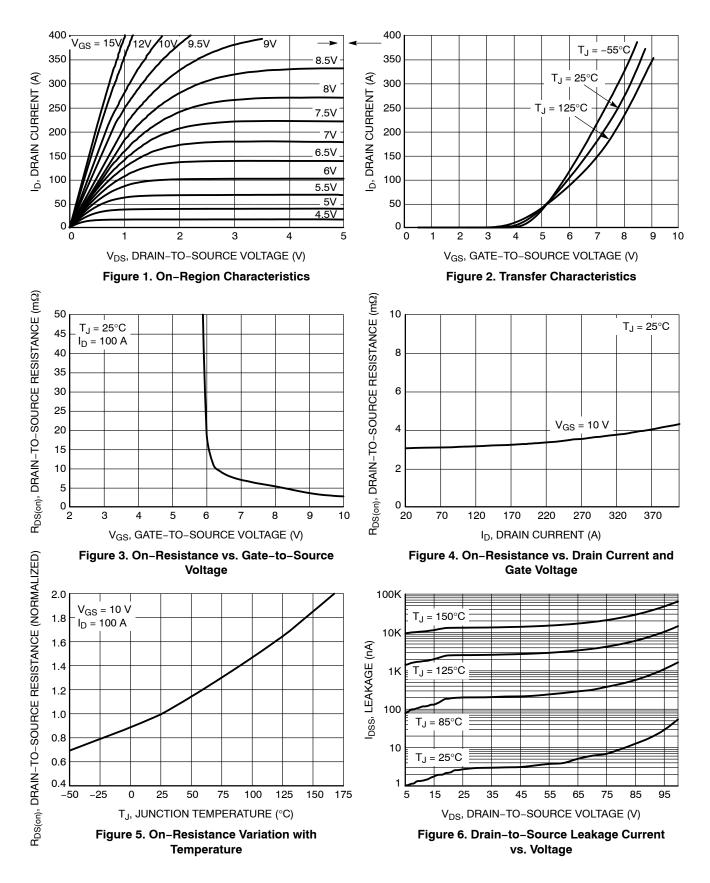
ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μ A		100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} / T _J	I_D = 250 µA, ref to 25°C			81.3		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V,$	$T_J = 25^{\circ}C$			1.0	μA
		V _{DS} = 80 V	T _J = 125°C			100	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = 20 V				100	nA
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D =	= 500 μA	2.0		4.0	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J	I _D = 500 μA, ref	to 25°C		-9.3		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D	= 100 A		3.0	4.1	mΩ
Forward Transconductance	9 FS	V _{DS} = 5 V, I _D =	= 100 A		71		S
Gate-Resistance	R _G	T _A = 25°	С		0.42		Ω
CHARGES, CAPACITANCES & GATE RESIS	CHARGES, CAPACITANCES & GATE RESISTANCE						
Input Capacitance	C _{ISS}				12100		
Output Capacitance	C _{OSS}	V _{GS} = 0 V, V _{DS} = 50	V, f = 1 MHz		1170		рF
Reverse Transfer Capacitance	C _{RSS}				165		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 50 V; I _D = 100 A			178		
Threshold Gate Charge	Q _{G(TH)}				79		nC
Gate-to-Source Charge	Q _{GS}				66		
Gate-to-Drain Charge	Q _{GD}				43		
Plateau Voltage	V _{GP}				6.0		V
SWITCHING CHARACTERISTICS (Note 4)							
Turn-On Delay Time	t _{d(ON)}				44		
Rise Time	t _r	V _{GS} = 10 V, V _{DS}	s = 50 V.		41		1
Turn-Off Delay Time	t _{d(OFF)}	$I_{\rm D} = 100 \text{ A}, \text{ R}_{\rm G} = 4.7 \Omega$			81		ns
Fall Time	t _f				29		
DRAIN-SOURCE DIODE CHARACTERISTICS							
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$		0.88	1.2	
		$I_{\rm S} = 100 \text{A}$ $T_{\rm J} = 125^{\circ}\text{C}$			0.77		V
Reverse Recovery Time	t _{RR}	V _{GS} = 0 V, dI _S /dt = 100 A/µs, I _S = 50 A			74		
Charge Time	ta				46		ns
Discharge Time	t _b				29		
Reverse Recovery Charge	Q _{RR}				151		nC

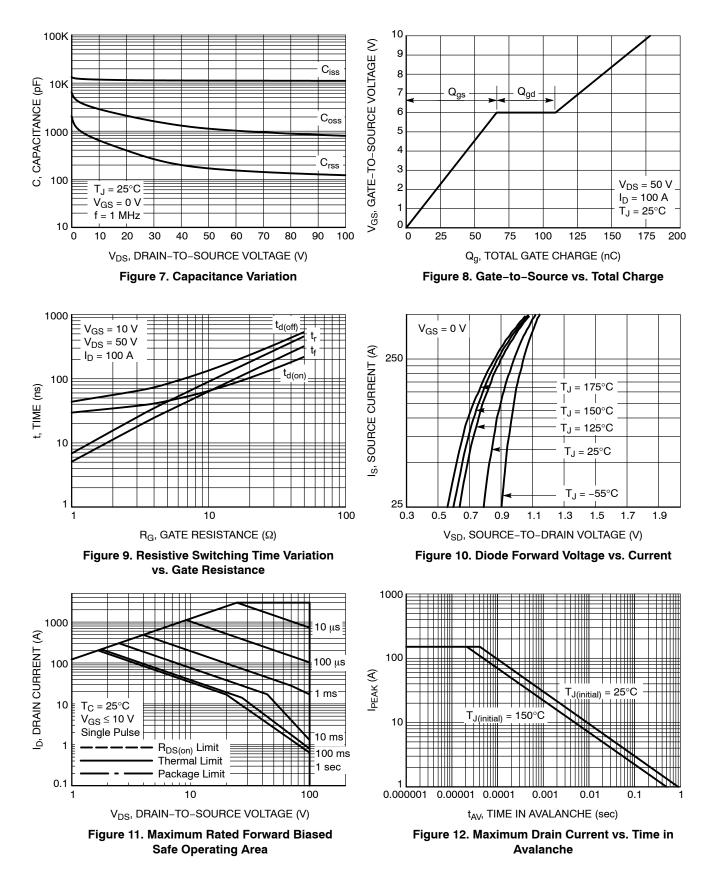
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. Pulse Test: pulse width $\leq 300 \ \mu$ s, duty cycle $\leq 2\%$.

4. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

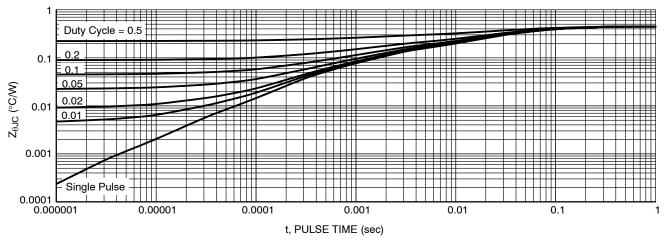
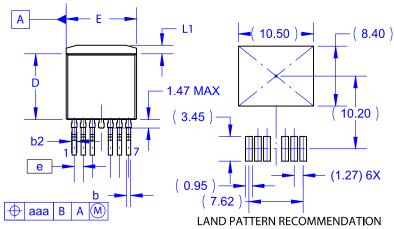


Figure 13. Transient Thermal Impedance

PACKAGE DIMENSIONS

D²PAK7 (TO-263 7 LD) CASE 418AY **ISSUE C**

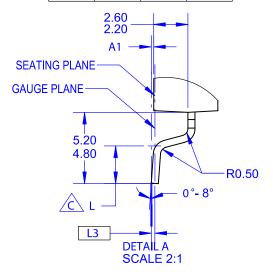


NOTES:

A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED. B. ALL DIMENSIONS ARE IN MILLIMETERS.

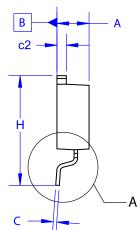
OUT OF JEDEC STANDARD VALUE. D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994. E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS. F. LAND PATTERN RECOMMENDATION PER IPC. TO127P1524X465-8N.

MILLIMETERS					
DIM	MIN	NOM	MAX		
Α	4.30	4.50	4.70		
A1	0.00	0.10	0.20		
b2	0.70	0.80	0.90		
b	0.50	0.60	0.70		
С	0.40	0.50	0.60		
c2	1.20	1.30	1.40		
D	9.00	9.20	9.40		
D1	7.70	~	~		
Е	9.70	9.90	10.20		
E1	8.38	8.58	8.78		
е	~	1.27	~		
Н	15.10	15.40	15.70		
L	2.44	2.64	2.84		
L1	1.00	1.20	1.40		
L3	~	0.25	~		
aaa	~	~	0.25		



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