

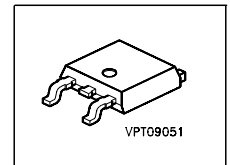
Cool MOS™ Power Transistor

Feature

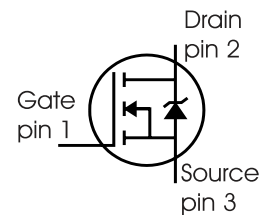
- New revolutionary high voltage technology
- Worldwide best $R_{DS(on)}$ in TO-252
- Ultra low gate charge
- Periodic avalanche rated
- Extreme dv/dt rated
- Ultra low effective capacitances
- Improved transconductance

$V_{DS} @ T_{jmax}$	560	V
$R_{DS(on)}$	0.6	Ω
I_D	7.6	A

P-TO252-3-1



Type	Package	Ordering Code	Marking
SPD08N50C3	P-TO252-3-1	Q67040-S4569	08N50C3



Maximum Ratings, at $T_C = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current	I_D	7.6	A
$T_C = 25^\circ\text{C}$		7.6	
$T_C = 100^\circ\text{C}$		4.6	
Pulsed drain current, t_p limited by T_{jmax}	$I_{D\ puls}$	22.8	
Avalanche energy, single pulse	E_{AS}	230	mJ
$I_D=5.5\text{A}, V_{DD}=50\text{V}$			
Avalanche energy, repetitive t_{AR} limited by T_{jmax} ¹⁾	E_{AR}	0.5	
$I_D=7.6\text{A}, V_{DD}=50\text{V}$			
Avalanche current, repetitive t_{AR} limited by T_{jmax}	I_{AR}	7.6	A
Gate source voltage	V_{GS}	± 20	V
Gate source voltage AC ($f > 1\text{Hz}$)	V_{GS}	± 30	
Power dissipation, $T_C = 25^\circ\text{C}$	P_{tot}	83	W
Operating and storage temperature	T_j, T_{stg}	-55... +150	$^\circ\text{C}$

Maximum Ratings

Parameter	Symbol	Value	Unit
Drain Source voltage slope $V_{DS} = 400\text{ V}, I_D = 7.6\text{ A}, T_j = 125\text{ °C}$	dv/dt	50	V/ns

Thermal Characteristics

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Thermal resistance, junction - case	R_{thJC}	-	-	1.5	K/W
Thermal resistance, junction - ambient, leaded	R_{thJA}	-	-	75	
SMD version, device on PCB: @ min. footprint @ 6 cm ² cooling area ²⁾	R_{thJA}	-	-	75 50	
Soldering temperature, 1.6 mm (0.063 in.) from case for 10s ³⁾	T_{sold}	-	-	260	°C

Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=0.25mA$	500	-	-	V
Drain-Source avalanche breakdown voltage	$V_{(BR)DS}$	$V_{GS}=0V, I_D=7.6A$	-	600	-	
Gate threshold voltage	$V_{GS(th)}$	$I_D=350\mu A, V_{GS}=V_{DS}$	2.1	3	3.9	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=500V, V_{GS}=0V,$ $T_j=25\text{ °C},$ $T_j=150\text{ °C}$	-	0.5	1 100	μA
Gate-source leakage current	I_{GSS}	$V_{GS}=20V, V_{DS}=0V$	-	-	100	
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=4.6A,$ $T_j=25\text{ °C}$ $T_j=150\text{ °C}$	-	0.5 1.5	0.6 -	Ω
Gate input resistance	R_G	$f=1MHz, \text{ open Drain}$	-	1.2	-	

Electrical Characteristics , at $T_j = 25\text{ }^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Transconductance	g_{fs}	$V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$ $I_D = 4.6\text{A}$	-	6	-	S
Input capacitance	C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$	-	750	-	pF
Output capacitance	C_{oss}		-	350	-	
Reverse transfer capacitance	C_{rss}		-	12	-	
Effective output capacitance, ⁴⁾ energy related	$C_{o(er)}$	$V_{GS} = 0\text{V}$, $V_{DS} = 0\text{V to } 400\text{V}$	-	56	-	pF
Effective output capacitance, ⁵⁾ time related	$C_{o(tr)}$		-	30	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 400\text{V}$, $V_{GS} = 0/10\text{V}$, $I_D = 7.6\text{A}$, $R_G = 12\Omega$	-	6	-	ns
Rise time	t_r		-	5	-	
Turn-off delay time	$t_{d(off)}$		-	60	-	
Fall time	t_f		-	7	-	

Gate Charge Characteristics

Gate to source charge	Q_{gs}	$V_{DD} = 400\text{V}$, $I_D = 7.6\text{A}$	-	3	-	nC
Gate to drain charge	Q_{gd}		-	17	-	
Gate charge total	Q_g	$V_{DD} = 400\text{V}$, $I_D = 7.6\text{A}$, $V_{GS} = 0\text{ to } 10\text{V}$	-	32	-	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 400\text{V}$, $I_D = 7.6\text{A}$	-	5	-	V

¹ Repetitive avalanche causes additional power losses that can be calculated as $P_{AV} = E_{AR} \cdot f$.

² Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical without blown air.

³ Soldering temperature for TO-263: 220°C, reflow

⁴ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

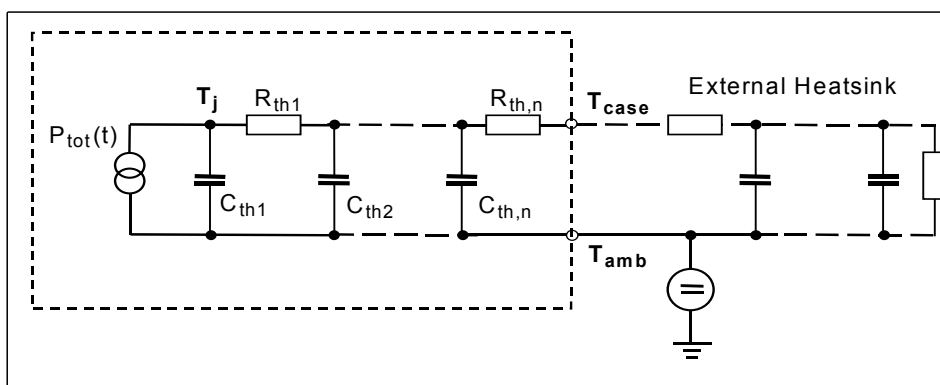
⁵ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

Electrical Characteristics, at $T_j = 25\text{ }^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Inverse diode continuous forward current	I_S	$T_C=25^\circ\text{C}$	-	-	7.6	A
Inverse diode direct current, pulsed	I_{SM}		-	-	22.8	
Inverse diode forward voltage	V_{SD}	$V_{GS}=0\text{V}, I_F=I_S$	-	1	1.2	V
Reverse recovery time	t_{rr}	$V_R=400\text{V}, I_F=I_S,$	-	370	-	ns
Reverse recovery charge	Q_{rr}	$di_F/dt=100\text{A}/\mu\text{s}$	-	3.6	-	μC
Peak reverse recovery current	I_{rrm}		-	25	-	A
Peak rate of fall of reverse recovery current	di_{rr}/dt		-	700	-	$\text{A}/\mu\text{s}$

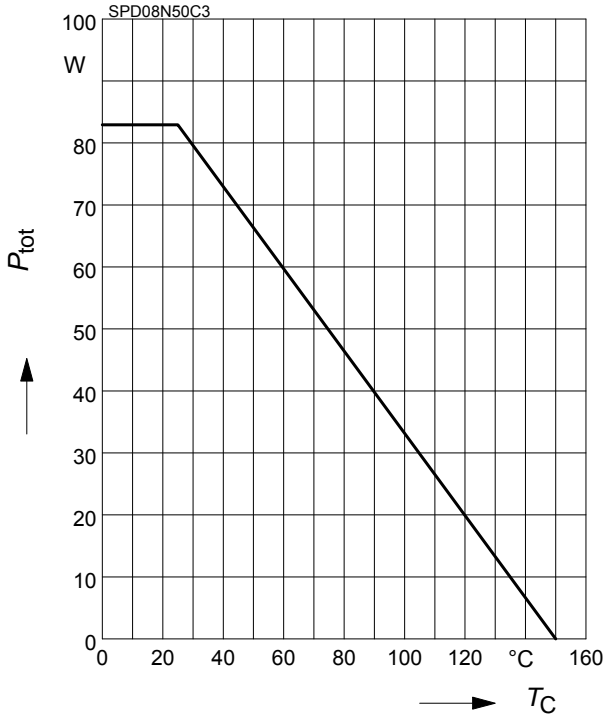
Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
	typ.			typ.	
Thermal resistance			Thermal capacitance		
R_{th1}	0.024	K/W	C_{th1}	0.00012	Ws/K
R_{th2}	0.046		C_{th2}	0.0004578	
R_{th3}	0.085		C_{th3}	0.000645	
R_{th4}	0.308		C_{th4}	0.001867	
R_{th5}	0.317		C_{th5}	0.004795	
R_{th6}	0.112		C_{th6}	0.045	



1 Power dissipation

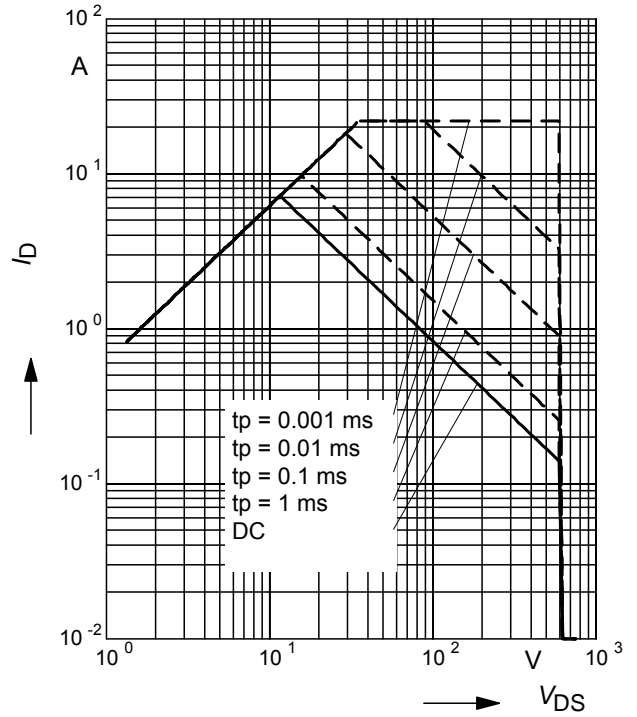
$P_{tot} = f(T_C)$



2 Safe operating area

$I_D = f(V_{DS})$

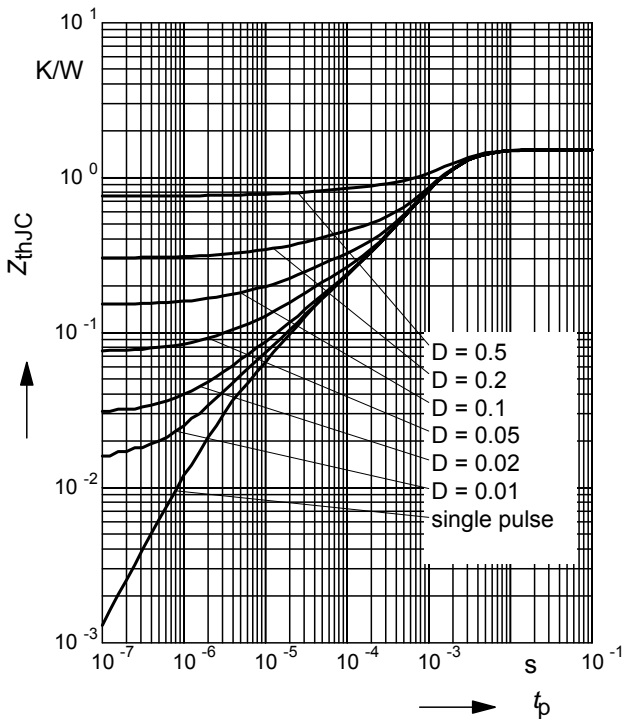
parameter : $D = 0$, $T_C = 25^\circ C$



3 Transient thermal impedance

$Z_{thJC} = f(t_p)$

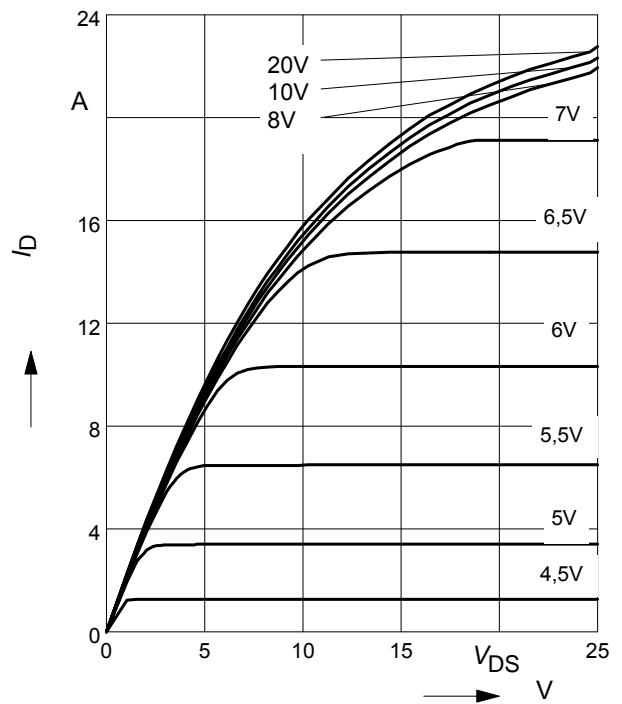
parameter: $D = t_p/T$



4 Typ. output characteristic

$I_D = f(V_{DS})$; $T_j = 25^\circ C$

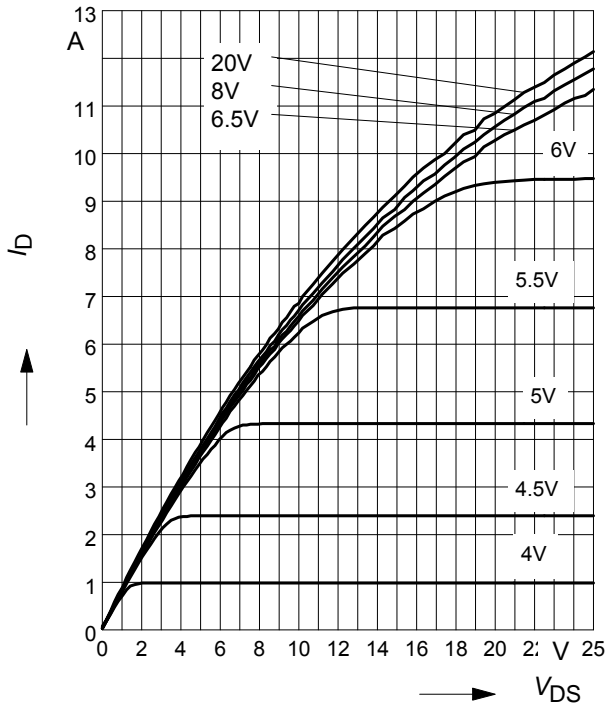
parameter: $t_p = 10 \mu s$, V_{GS}



5 Typ. output characteristic

$I_D = f(V_{DS}); T_j = 150^\circ\text{C}$

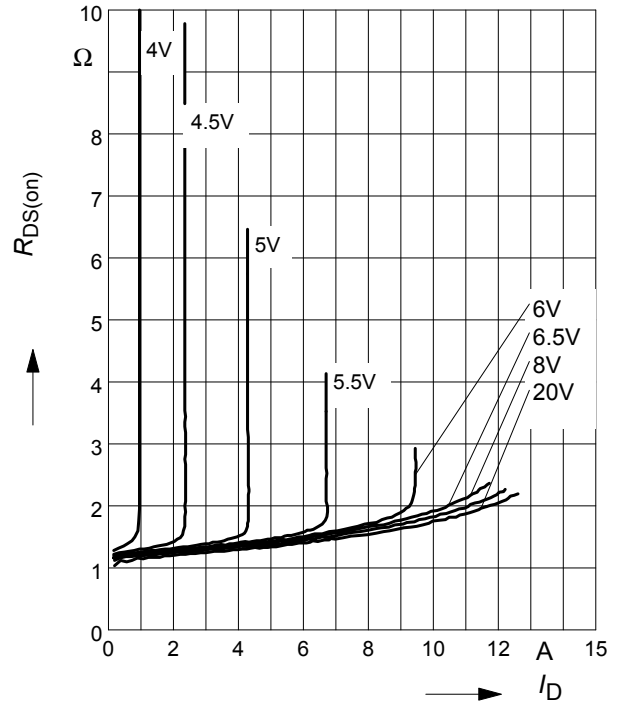
parameter: $t_p = 10 \mu\text{s}, V_{GS}$



6 Typ. drain-source on resistance

$R_{DS(on)} = f(I_D)$

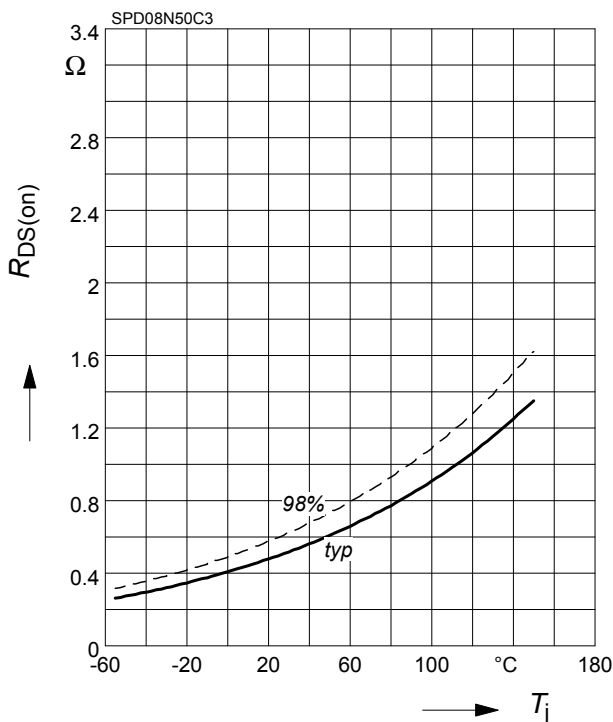
parameter: $T_j = 150^\circ\text{C}, V_{GS}$



7 Drain-source on-state resistance

$R_{DS(on)} = f(T_j)$

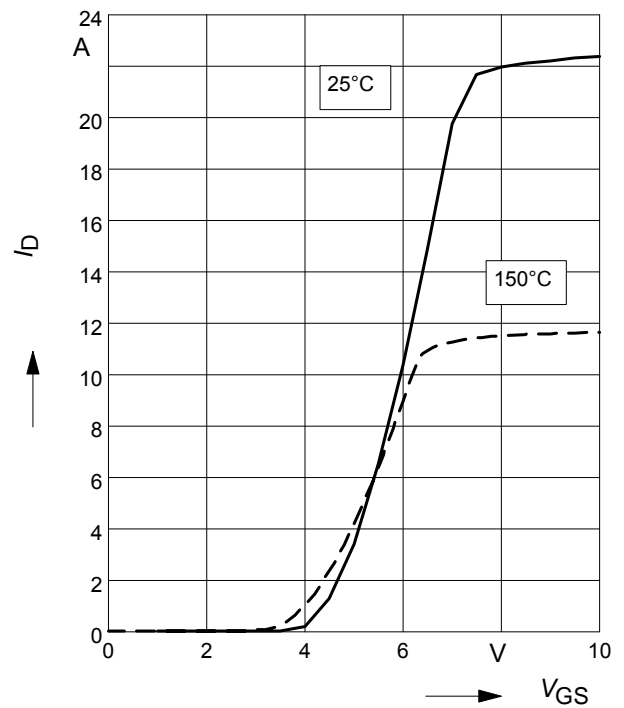
parameter: $I_D = 4.6 \text{ A}, V_{GS} = 10 \text{ V}$



8 Typ. transfer characteristics

$I_D = f(V_{GS}); V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$

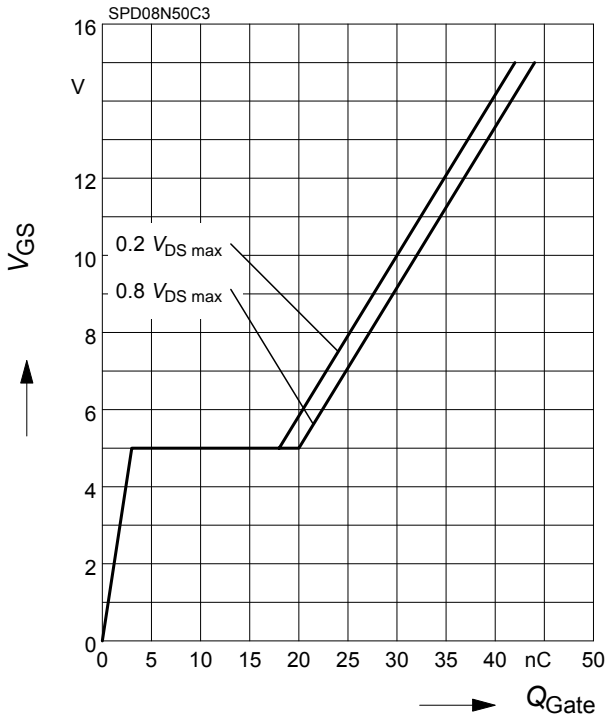
parameter: $t_p = 10 \mu\text{s}$



9 Typ. gate charge

$$V_{GS} = f(Q_{Gate})$$

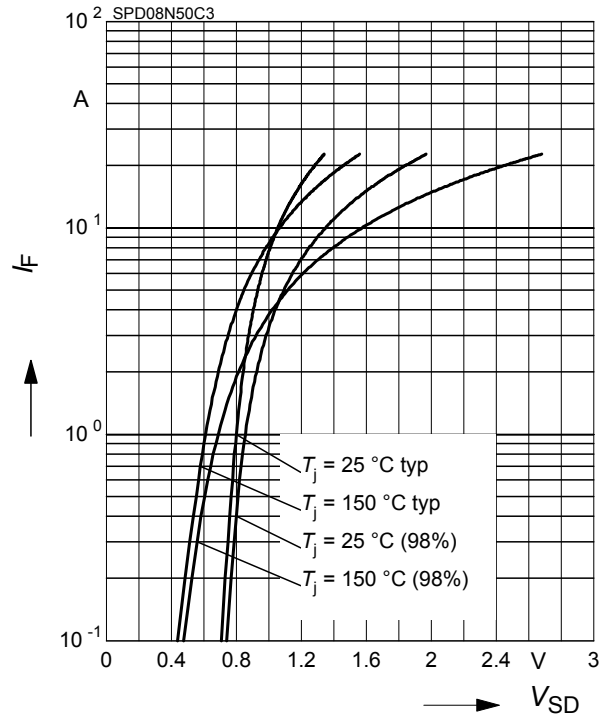
parameter: $I_D = 7.6$ A pulsed



10 Forward characteristics of body diode

$$I_F = f(V_{SD})$$

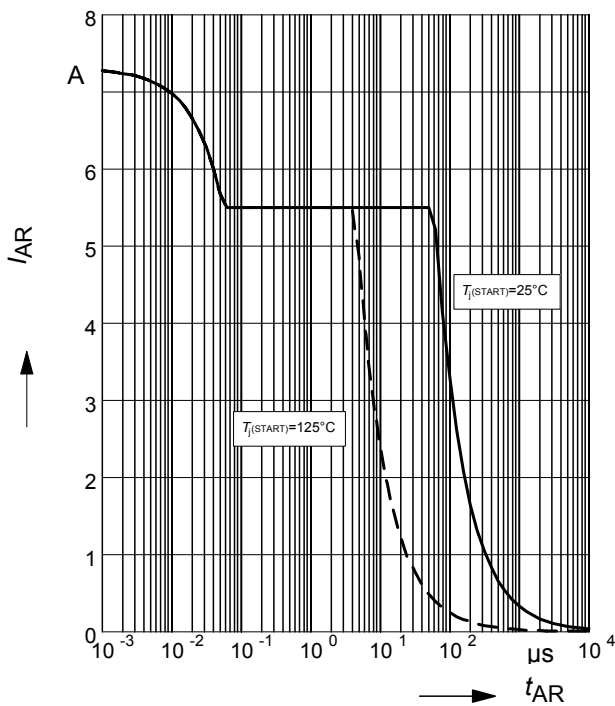
parameter: T_j , $t_p = 10$ μ s



11 Avalanche SOA

$$I_{AR} = f(t_{AR})$$

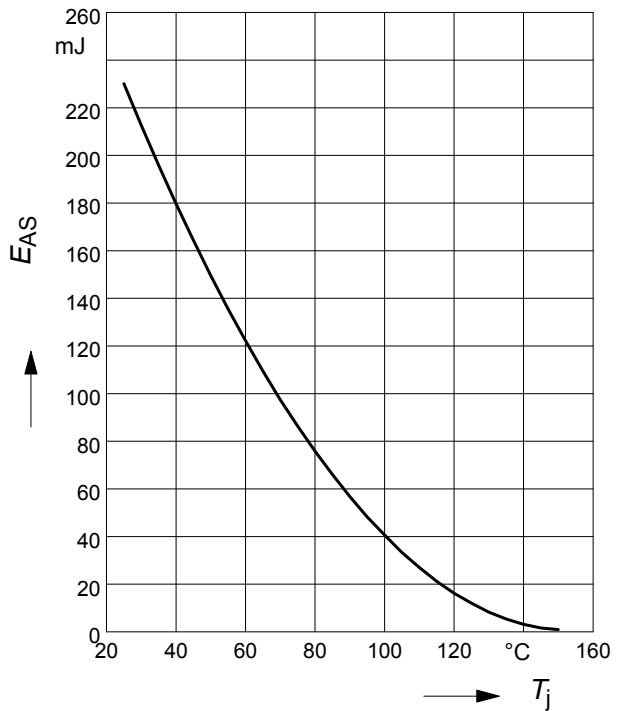
par.: $T_j \leq 150$ °C



12 Avalanche energy

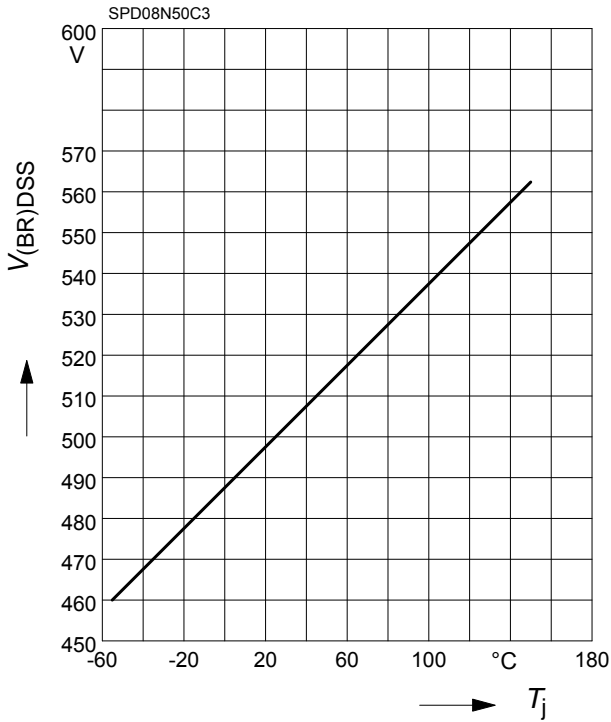
$$E_{AS} = f(T_j)$$

par.: $I_D = 5.5$ A, $V_{DD} = 50$ V



13 Drain-source breakdown voltage

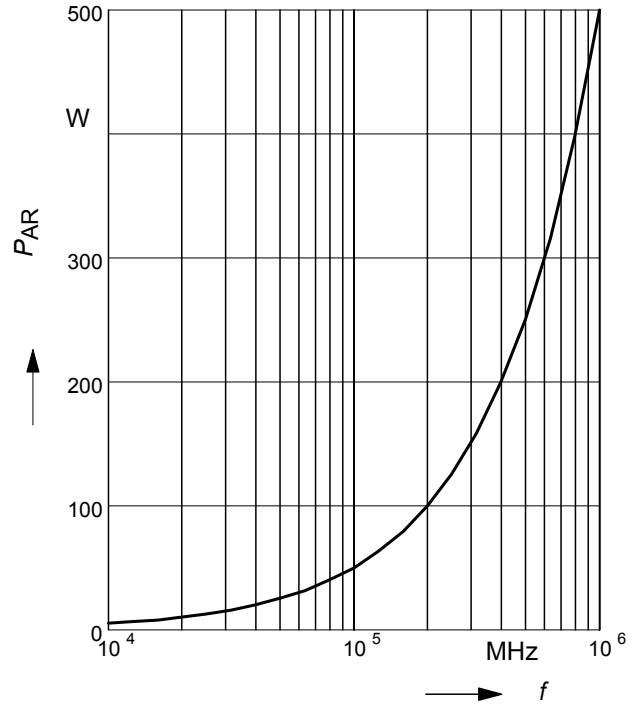
$$V_{(BR)DSS} = f(T_j)$$



14 Avalanche power losses

$$P_{AR} = f(f)$$

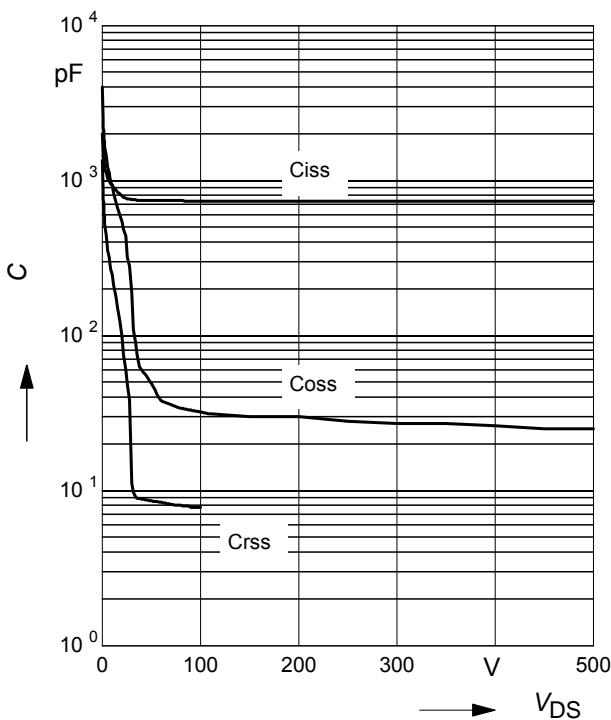
parameter: $E_{AR}=0.5mJ$



15 Typ. capacitances

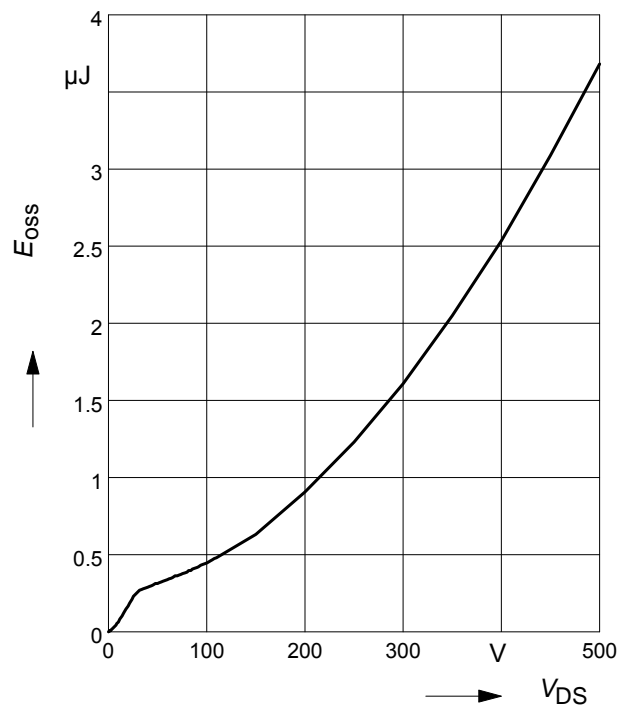
$$C = f(V_{DS})$$

parameter: $V_{GS}=0V, f=1 MHz$

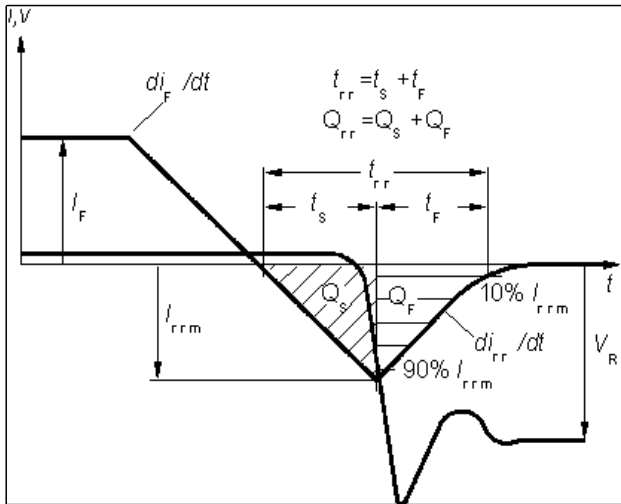


16 Typ. C_{OSS} stored energy

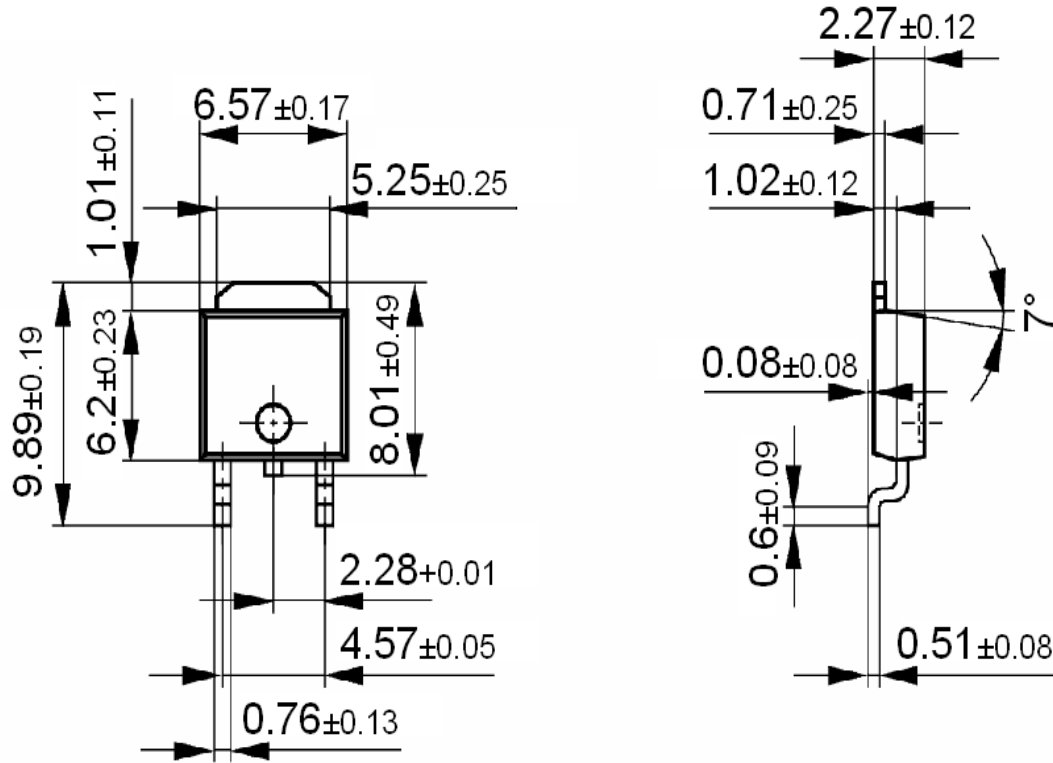
$$E_{OSS} = f(V_{DS})$$



Definition of diodes switching characteristics



P-TO-252-3-1 (D-PAK)



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