



DUAL 40V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
40)/	10.8mΩ @ V _{GS} = 10V	30.2A
40V	15mΩ @ V _{GS} = 4.5V	25.6A

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- Power Management Functions
- **DC-DC Converters**

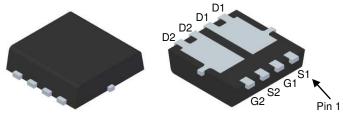
Features

- 100% Unclamped Inductive Switching, Test in Production -Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

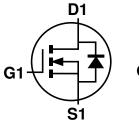
Mechanical Data

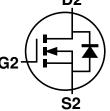
- Case: PowerDI 83333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)

PowerDI3333-8 (Type UXC)









Equivalent Circuit

Ordering Information (Note 5)

Part Number	Case	Packaging
DMT47M2LDVQ-7	PowerDI3333-8 (Type UXC)	2000/Tape & Reel
DMT47M2LDVQ-13	PowerDI3333-8 (Type UXC)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



M2D = Product Type Marking Code YYWW = Date Code Marking \overline{YY} = Last Two Digits of Year (ex: 19 for 2019) WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	40	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current (Note 7), V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	30.2 24.2	А
Continuous Drain Current (Note 6), $V_{GS} = 10V$ $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		I _D	11.9 9.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	120	Α	
Maximum Continuous Body Diode Forward Current (Note 7)	Is	16.4	Α	
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1	I _{SM}	120	Α	
Avalanche Current, L = 0.1mH (Note 8)	I _{AS}	22.1	Α	
Avalanche Energy, L = 0.1mH (Note 8)	E _{AS}	24.4	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	P_{D}	2.34	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ hetaJA}$	53.7	°C/W	
Total Power Dissipation (Note 7) $T_C = +25^{\circ}C$		P_{D}	14.8	W
Thermal Resistance, Junction to Case (Note 7)	$R_{ heta JC}$	8.43	°C/W	
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	1.2	1.4	2.3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	ם		8.4	10.8	0	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Nesistance	R _{DS(ON)}	_	10.9	15	mΩ	$V_{GS} = 4.5V, I_D = 10A$	
Diode Forward Voltage	V_{SD}	_	0.9	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}	1	891	_		$V_{DS} = 20V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss		490	_	pF		
Reverse Transfer Capacitance	C _{rss}		14.8	_			
Gate Resistance	R_g	_	1.87	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g	_	14.0	_			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	6.72	_			
Gate-Source Charge	Qgs	_	1.04	_	nC	$V_{DS} = 20V, I_{D} = 20A$	
Gate-Drain Charge	Q_{gd}	_	2.52	_			
Turn-On Delay Time	t _{D(ON)}	_	3.95	_			
Turn-On Rise Time	t _R	_	5.41	_		$V_{DD} = 20V, V_{GS} = 10V,$ $R_G = 3\Omega, I_D = 20A$	
Turn-Off Delay Time	t _{D(OFF)}	_	15.4	_	ns		
Turn-Off Fall Time	t _F		8.53	_			
Body Diode Reverse Recovery Time	t _{RR}		56.6	_	ns	1 00A di/dt 400A/vs	
Body Diode Reverse Recovery Charge	Q _{RR}		40.0	_	nC	$_{\text{nC}}$ $I_{\text{F}} = 20\text{A}, \text{di/dt} = 100\text{A/}\mu\text{s}$	

6. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

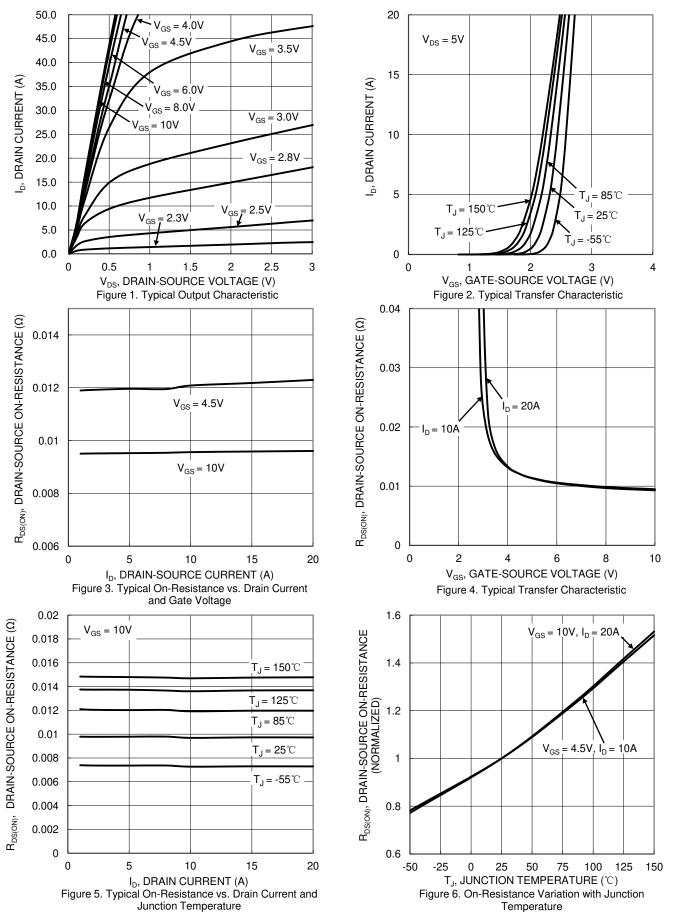
^{8.} I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25$ °C.

^{9.} Short duration pulse test used to minimize self-heating effect.

^{10.} Guaranteed by design. Not subject to product testing.

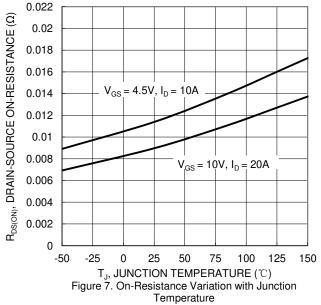


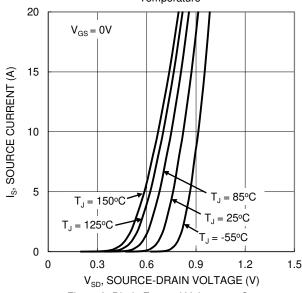


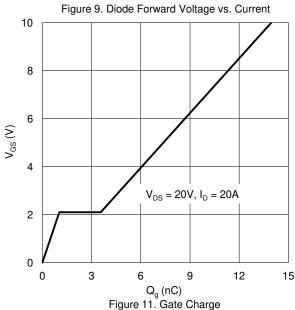


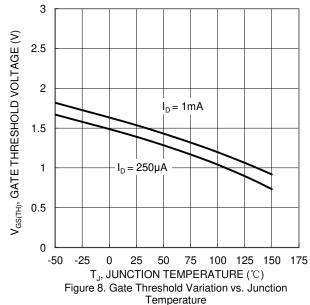


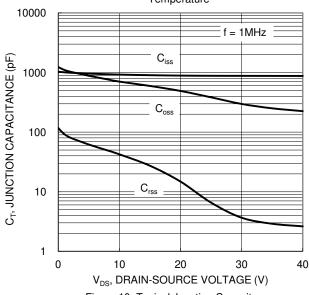


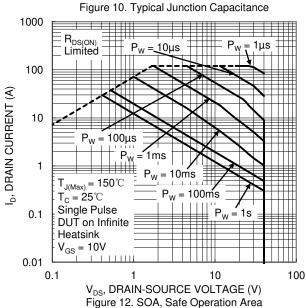














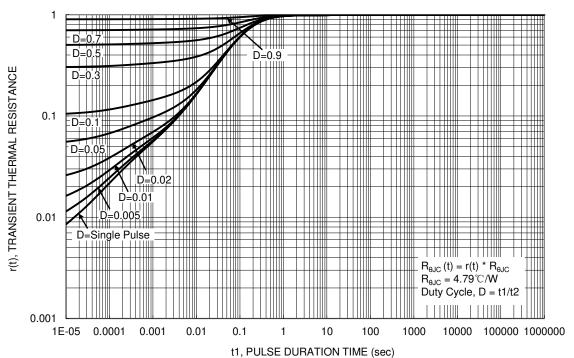


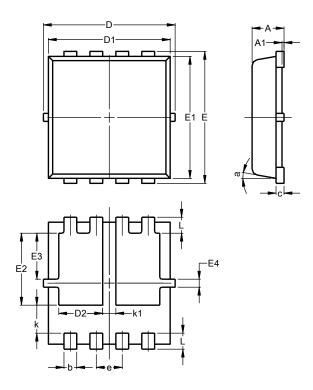
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UXC)

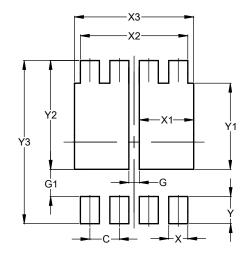


PowerDI3333-8						
(Type UXC)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A 1	0.00	0.05				
۵	0.25	0.40	0.32			
C	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	0.90	1.30	1.10			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	_	_	0.65			
L	0.30	0.50	0.40			
k	0.50	0.90	0.70			
k1	0.13	0.53	0.33			
а	0°	12°	10°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UXC)



Dimensions	Value (in mm)			
С	0.650			
G	0.230			
G1	0.600			
Χ	0.420			
X1	1.200			
X2	2.370			
Х3	2.630			
Υ	0.600			
Y1	1.900			
Y2	2.400			
Y3	3.600			



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