

SUD08P06-155L

Vishay Siliconix

P-Channel 60-V (D-S), 175 °C MOSFET

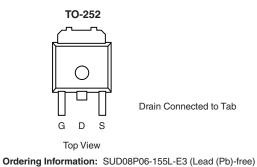
PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)	Q _g (Typ)	
- 60	0.155 at V _{GS} = - 10 V	- 8.4	12.5	
	0.280 at V _{GS} = - 4.5 V	- 7.4	12.5	

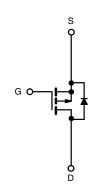
FEATURES

• TrenchFET[®] Power MOSFETS



• 175 °C Rated Maximum Junction Temperature





P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current ($T_{,1} = 175 \text{ °C}$)	T _C = 25 °C	1-	- 8.4		
Commutous Drain Current (1) = 175 C)	T _C = 100 °C	I _D	- 6		
Pulsed Drain Current		I _{DM}	- 18	A	
Continuing Source Current (Diode Conduction)		۱ _S	- 8.4		
Avalanche Current		I _{AS}	- 12		
Single Pulse Avalanche Energy L = 0.1 mH		E _{AS}	7.2	mJ	
Mauinum Davier Diaciastian	T _C = 25 °C	D	25 ^a	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
hunstien te Ambient	t ≤ 10 sec	R _{thJA}	20	25	°C/W
Junction-to-Ambient ^D	Steady State		62	75	
Junction-to-Case		R _{thJC}	5	6	

Notes:

a. See SOA curve for voltage derating.

b. Surface Mounted on 1" x 1" FR-4 boad.

SUD08P06-155L



Vishay Siliconix

SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min	Тур ^а	Max	Unit	
Static	1			1			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	- 1.0	- 2.0	- 3.0		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -60 V, V_{GS} = 0 V$			- 1		
	I _{DSS}	V_{DS} = - 60 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μA	
		$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			- 150	1	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 10			А	
Drain-Source On-State Resistance ^b		V _{GS} = - 10 V, I _D = - 5 A		0.125	0.155	Ω	
	r _{DS(on)}	V_{GS} = - 10 V, I _D = - 5 A, T _J = 125 °C			0.280		
		V_{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C			0.350		
		V _{GS} = - 4.5 V, I _D = - 2 A		0.158	0.280		
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S	
Dynamic	•	•		•			
Input Capacitance	C _{iss}			450		pF	
Output Capacitance	C _{oss}	V _{DS} = - 25 V, V _{GS} = 0 V, f = 1 MHz		65			
Reverse Transfer Capacitance	C _{rss}] [40			
Total Gate Charge	Qg			12.5	19	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3			
Gate-Drain Charge	Q _{gd}	1		3.2			
Gate Resistance	Rg	f = 1 MHz		8.0		Ω	
Turn-On Delay Time ^c	t _{d(on)}			5	10	ns	
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, \text{ R}_{L} = 3.57 \Omega$ $I_{D} \cong -8.4 \text{ A}, \text{ V}_{\text{GEN}} = -10 \text{ V}, \text{ R}_{\text{G}} = 2.5 \Omega$		14	25		
Turn-Off Delay Time ^c	t _{d(off)}			15	25		
Fall Time ^c	t _f	1 1		7	12		
Source-Drain Diode Ratings and Cha	aracteristics	(T _C = 25 °C) ^b					
Pulsed Current	I _{SM}				- 20	А	
Forward Voltage ^b	V _{SD}	$I_{F} = -2 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.9	- 1.3	V	
Reverse Recovery Time	t _{rr}			50	80	ns	
Reverse Recovery Time	Q _{rr}	I _F = - 8 A, di/dt = 100 A/μs		80	120	nC	

Notes:

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

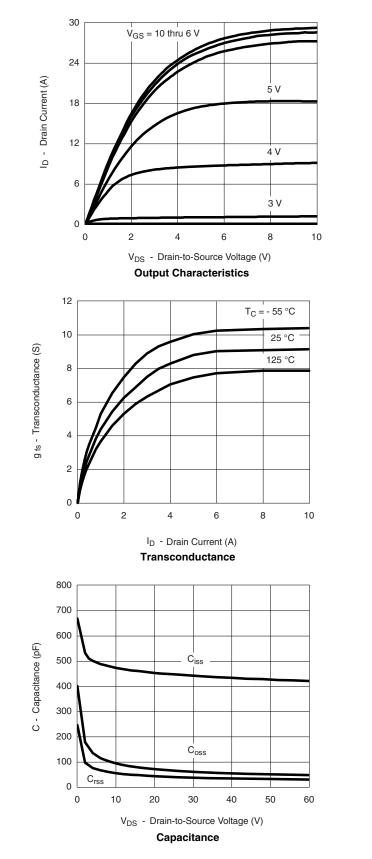
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

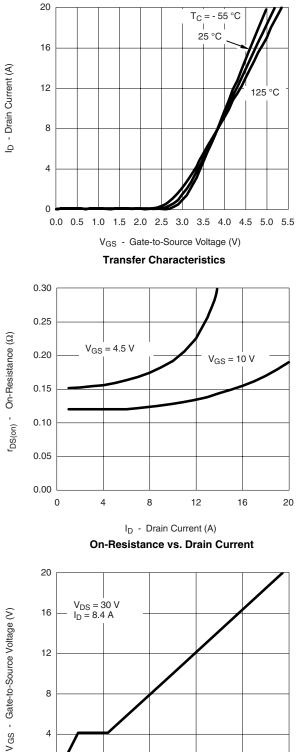


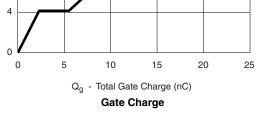
Vishay Siliconix



TYPICAL CHARACTERISTICS 25 °C unless noted





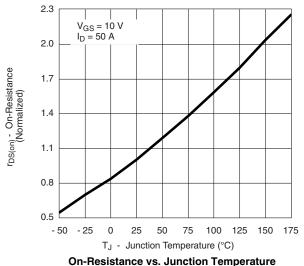


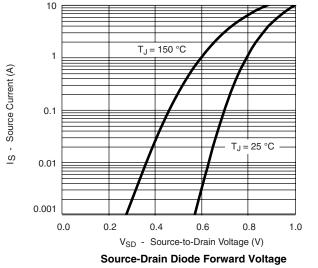
Document Number: 73209 S-71660-Rev. B, 06-Aug-07

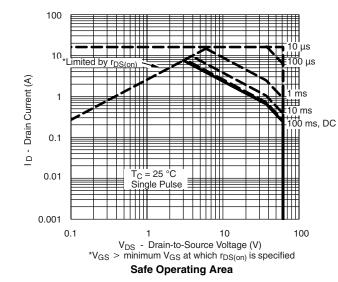
SUD08P06-155L

Vishay Siliconix

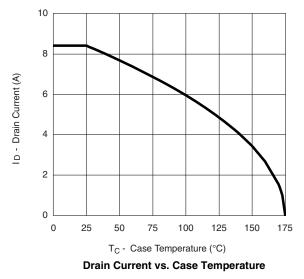
TYPICAL CHARACTERISTICS 25 °C unless noted



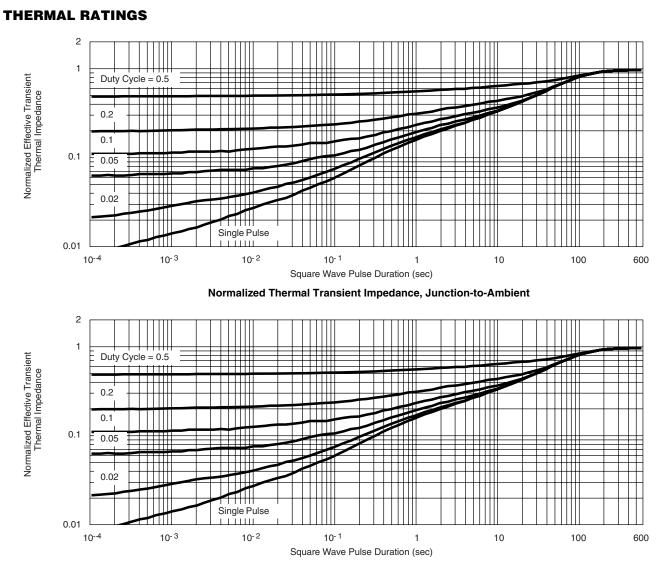




THERMAL RATINGS







Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73209.

VISHAY

SUD08P06-155L

Vishay Siliconix



Vishay

Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.