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## MJD31C NPN Epitaxial Silicon Transistor

### Features

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- General-Purpose Amplifier
- Low-Speed Switching Applications
- · Lead Formed for Surface Mount Application (No Suffix)
- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular TIP31 and TIP31C

### Applications

- Switching Regulators
- Converters
- Power Amplifiers



as output or driver stages in applications.

1.Base 2.Collector 3.Emitter

Designed for general-purpose power and switching, such

### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MJD31CTF	MJD31C	TO-252 3L (DPAK)	Tape and Reel
MJD31CITU	MJD31C-I	TO-251 3L (IPAK)	Rail

Description

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit	
V <sub>CBO</sub>	Collector-Base Voltage	100	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	100	V	
$V_{EBO}$	Emitter-Base Voltage	5	V	
۱ <sub>C</sub>	Collector Current (DC)	3	Α	
I <sub>CP</sub>	Collector Current (Pulse)	5	Α	
Ι <sub>Β</sub>	Base Current	1	Α	
Р	Collector Dissipation ( $T_C = 25^{\circ}C$ )	15.00	w	
$P_{C} \qquad \qquad$		1.56		
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	- 65 to 150	°C	

# MJD31C — NPN Epitaxial Silicon Transistor

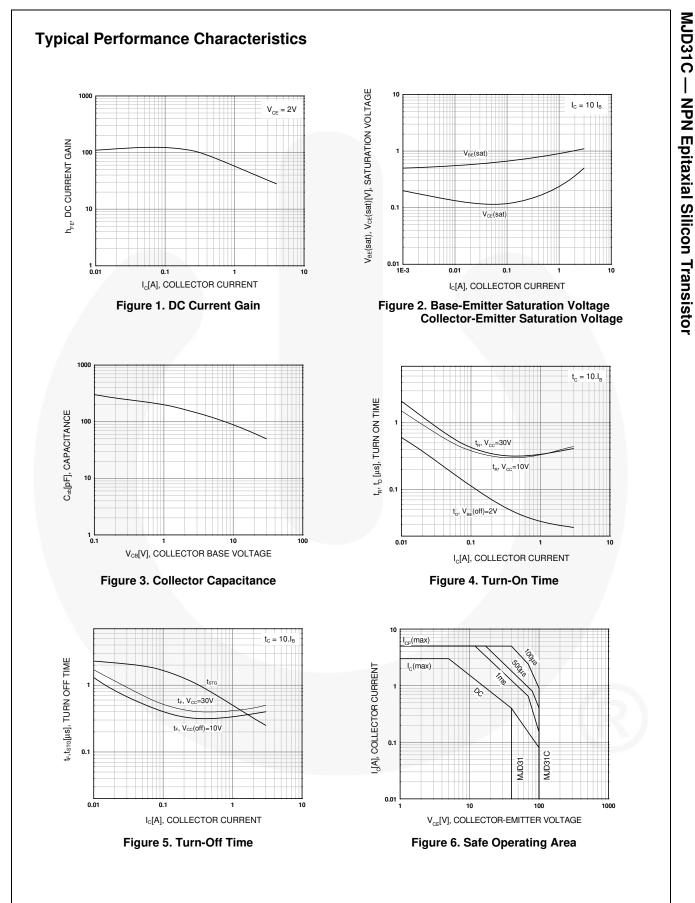
### **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

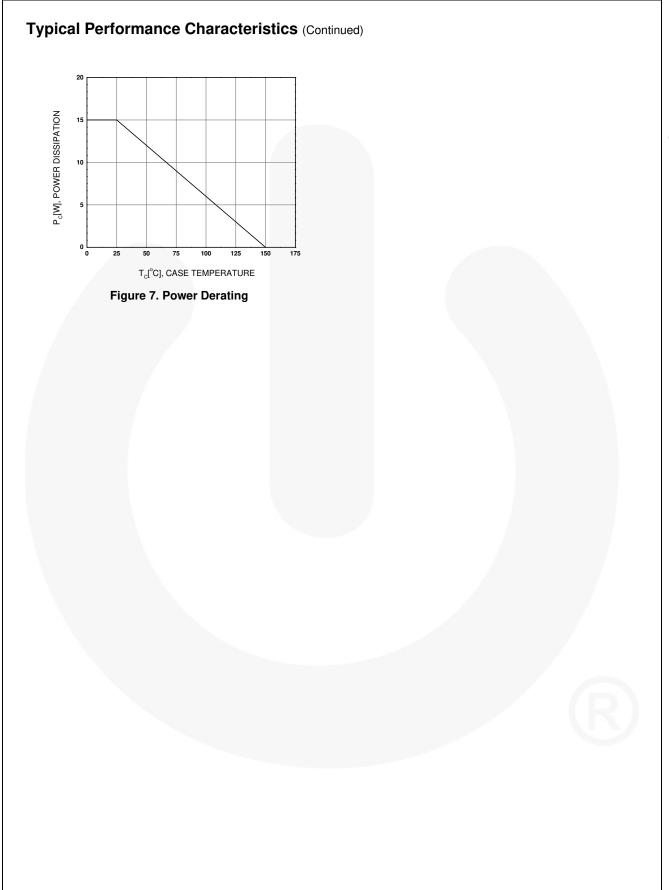
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	I <sub>C</sub> = 30 mA, I <sub>B</sub> = 0	100			V
I <sub>CEO</sub>	Collector Cut-Off Current	$V_{CE} = 60 \text{ V}, \text{ I}_{B} = 0$			50	μA
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = 100 \text{ V}, \text{ V}_{BE} = 0$			20	μA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{BE} = 5 V, I_{C} = 0$			1	mA
h	DC Current Gain <sup>(1)</sup>	$V_{CE} = 4 V, I_{C} = 1 A$	25			
h <sub>FE</sub>		$V_{CE} = 4 \text{ V}, I_{C} = 3 \text{ A}$	10		50	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(1)</sup>	I <sub>C</sub> = 3 A, I <sub>B</sub> = 375 mA			1.2	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(1)</sup>	$V_{CE} = 4 \text{ A}, I_{C} = 3 \text{ A}$			1.8	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 500 \text{ mA}$	3			MHz

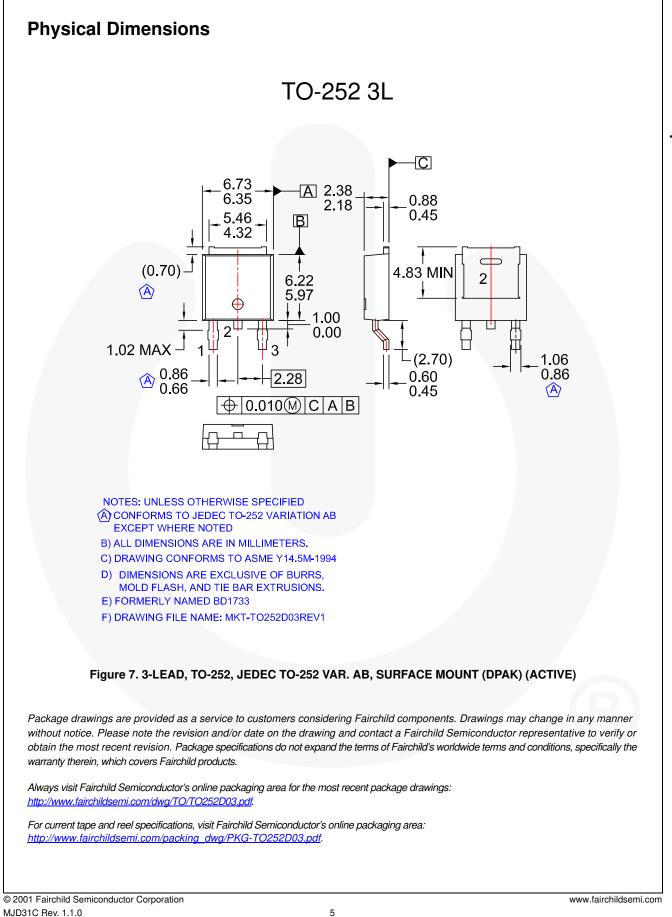
Note:

1. Pulse test:  $pw \le 300 \ \mu s$ , duty cycle  $\le 2\%$ .

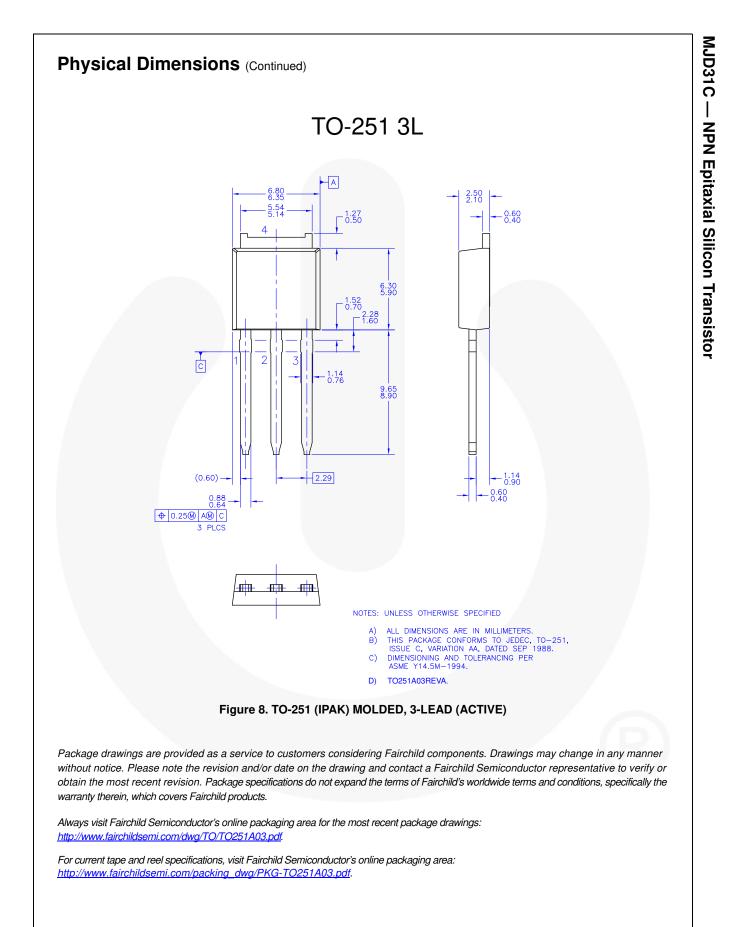


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MJD31C — NPN Epitaxial Silicon Transistor



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