

HIGH POWER SPDT SWITCH GaAs MMIC

■ GENERAL DESCRIPTION

The NJG1681MD7 is a GaAs SPDT switch MMIC suitable for LTE/UMTS/CDMA/GSM applications.

The NJG1681MD7 features very low insertion loss, high isolation and excellent linearity performance down to 1.8V control voltage at high frequency up to 6GHz. In addition, this switch is able to handle high power signals.

The NJG1681MD7 has ESD protection devices to achieve excellent ESD performances. No DC Blocking capacitors are required for all RF ports unless DC is biased externally. And the ultra small & ultra thin EQFN14-D7 package is adopted.

■ PACKAGE OUTLINE



NJG1681MD7

■ APPLICATIONS

LTE, UMTS, CDMA, GSM applications IEEE802.11p application

Antenna switching, bands switching, post PA switching applications

■ FEATURES

Low voltage logic control $V_{CTL(H)}=1.8V$ typ. Low voltage operation $V_{DD}=2.7V$ typ.

Low distortion IIP3=+73dBm typ. @f=829+849MHz, P_{IN}=24dBm IIP3=+71dBm typ. @f=1870+1910MHz, P_{IN}=24dBm 2nd harmonics=-85dBc typ. @ f=0.9GHz, P_{IN}=35dBm 3rd harmonics=-90dBc typ. @ f=0.9GHz, P_{IN}=35dBm ● P-0.1dB

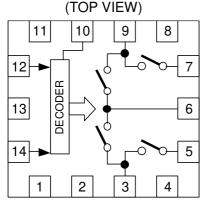
+36dBm min.

Low insertion loss 0.18dB typ. @f=0.9GHz, P_{IN}=35dBm 0.20dB typ. @f=1.9GHz, P_{IN}=33dBm 0.23dB typ. @f=2.7GHz, P_{IN}=27dBm 0.45dB typ. @f=6.0GHz, P_{IN}=27dBm

 Ultra small & ultra thin package EQFN14-D7 (Package size: 1.6 x 1.6 x 0.397mm.)

RoHS compliant and Halogen Free, MSL1

■ PIN CONFIGURATION



Pin connection

1. GND 8. GND 2. NC(GND) 9. P1 3. P2 10. GND 4. GND 11. GND 5. GND 12. VDD 6. PC 13. NC(GND) 14. VCTL 7. GND

Exposed PAD: GND

■ TRUTH TABLE

"H"= $V_{CTL(H)}$, "L"= $V_{CTL(L)}$		
VCTL	Path	
Н	PC-P1	
L	PC-P2	

NOTE: Please note that any information on this datasheet will be subject to change.

■ ABSOLUTE MAXIMUM RATINGS

 $(T_a=+25^{\circ}C, Z_s=Z_l=50\Omega)$

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNITS
RF Input Power	P _{IN}	V _{DD} =2.7V	37	dBm
Supply Voltage	V_{DD}		5.0	V
Control Voltage	V _{CTL}		5.0	V
Power Dissipation	P _D	Four-layer FR4 PCB with through-hole (74.2x74.2mm), Tj=150°C	1300	mW
Operating Temp.	T_{opr}		-40 to +105	°C
Storage Temp.	T_{stg}		-55 to +150	°C

■ ELECTRICAL CHARACTERISTICS 1 (DC)

(General conditions: $T_a=+25$ °C, $V_{DD}=2.7V$, $V_{CTL(L)}=0V$, $V_{CTL(H)}=1.8V$)

PARAMETERS	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	V_{DD}		2.375	2.7	5.0	V
Operating Current	I _{DD}	No RF input, V _{DD} =2.7V	-	95	180	μΑ
Control Voltage (LOW)	V _{CTL(L)}		0	-	0.45	V
Control Voltage (HIGH)	V _{CTL(H)}		1.35	1.8	5.0	V
Control Current	I _{CTL}	V _{CTL(H)} =1.8V	-	4	10	μΑ

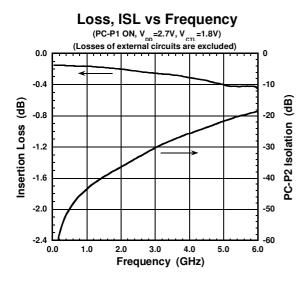
■ ELECTRICAL CHARACTERISTICS 2 (RF)

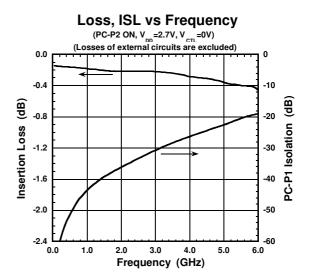
(General conditions: $T_a=+25$ °C, $Z_s=Z_l=50\Omega$, $V_{DD}=2.7V$, $V_{CTL(L)}=0V$, $V_{CTL(H)}=1.8V$) **TYP** MAX MIN **UNITS PARAMETERS SYMBOL CONDITIONS** LOSS₁ Insertion Loss 1 f=0.9GHz, P_{IN}=35dBm 0.18 0.33 dB Insertion Loss 2 LOSS2 f=1.9GHz, P_{IN}=33dBm 0.20 0.40 dB **Insertion Loss 3** LOSS3 f=2.7GHz, P_{IN}=27dBm 0.23 0.43 dB Insertion Loss 4 LOSS4 f=6.0GHz, P_{IN}=27dBm 0.45 0.65 dB f=0.9GHz, P_{IN}=35dBm Isolation 1 ISL₁ 40 45 dB ISL₂ f=1.9GHz, P_{IN}=33dBm Isolation 2 30 35 dB ISL3 f=2.7GHz, P_{IN}=27dBm 25 30 dB Isolation 3 f=6.0GHz, $P_{IN}=27dBm$ ISL4 Isolation 4 16.5 20 dB Input Power at 0.1dB f=0.9GHz, f=1.9GHz, $P_{-0.1dB}$ +36 dBm f=2.7GHz, f=6.0GHz Compression Point f=0.9GHz, P_{IN}=35dBm 2nd Harmonics 1 2fo(1) -85 -70 dBc 2nd Harmonics 2 f=1.9GHz, P_{IN}=33dBm -70 dBc 2fo(2) -90 2nd Harmonics 3 2fo(3) f=2.7GHz, P_{IN}=27dBm -90 -70 dBc f=0.9GHz, P_{IN}=35dBm -90 -70 dBc 3rd Harmonics 1 3fo(1) 3rd Harmonics 2 3fo(2) f=1.9GHz, P_{IN}=33dBm -80 -70 dBc 3rd Harmonics 3 3fo(3) f=2.7GHz, P_{IN}=27dBm -90 -70 dBc Input 3rd order f=829+849MHz, IIP3(1) +65 +73 dBm intercept point1 P_{IN}=24dBm each Input 3rd order f=1870+1910MHz, IIP3(2) +65 +71 dBm P_{IN}=24dBm each intercept point2 VSWR 1 VSWR 1 on-state ports, f=2.7GHz 1.1 1.4 VSWR 2 VSWR 2 on-state ports, f=6.0GHz 1.1 1.4 1 5 Switching time T_{SW} 50% V_{CTL} to 10/90% RF μS

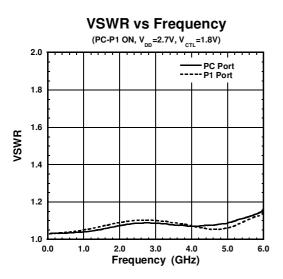
^{*1:} IIP3 are defined by the following equations. IIP3=(3 x Pout-IM3)/2+LOSS

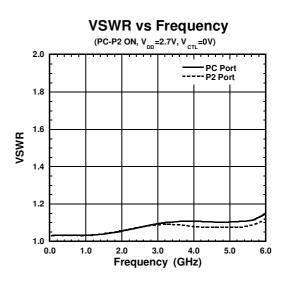
■ TERMINAL INFORMATION

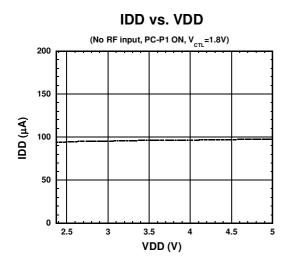
No.	SYMBOL	DESCRIPTION
1	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
2	NC(GND)	No connected terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
3	P2	RF transmitting/receiving port. No DC blocking capacitor is required for this port unless DC is biased externally.
4	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
5	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
6	PC	RF transmitting/receiving port. No DC blocking capacitor is required for this port unless DC is biased externally. Please connect an inductor with GND terminal for ESD protection.
7	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
8	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
9	P1	RF transmitting/receiving port. No DC blocking capacitor is required for this port unless DC is biased externally.
10	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
11	GND	Ground terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
12	VDD	Positive voltage supply terminal. The positive voltage (+2.375~+5V) has to be supplied. Please connect a bypass capacitor with GND terminal for excellent RF performance.
13	NC(GND)	No connected terminal. Please connect this terminal with ground plane as close as possible for excellent RF performance.
14	VCTL	Control signal input terminal. This terminal is set to High-Level (+1.35~+5.0V) or Low-Level (0~+0.45V).
Exposed Pad	GND	Ground terminal.

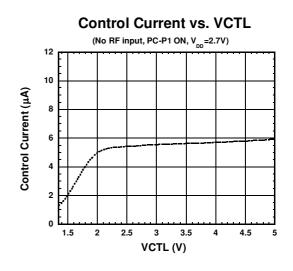




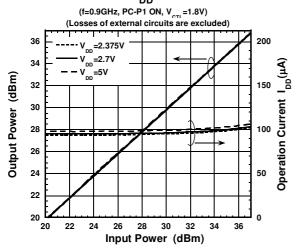




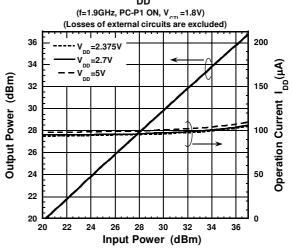




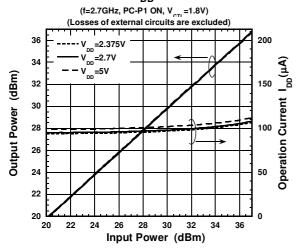
Output Power, $I_{\rm DD}$ vs Input Power



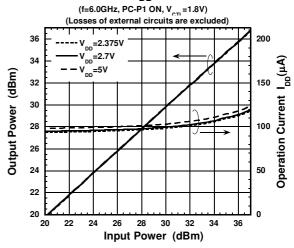
Output Power, I_{DD} vs Input Power



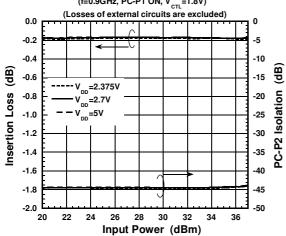
Output Power, \mathbf{I}_{DD} vs Input Power



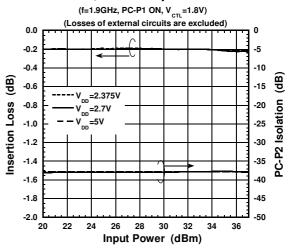
Output Power, I_{DD} vs Input Power



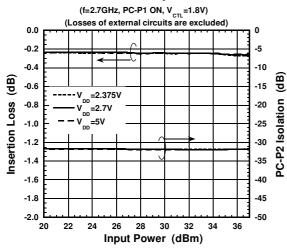
Loss, ISL vs Input Power (f=0.9GHz, PC-P1 ON, V_{CTL}=1.8V) (Losses of external circuits are excluded)



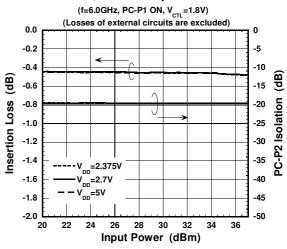
Loss, ISL vs Input Power



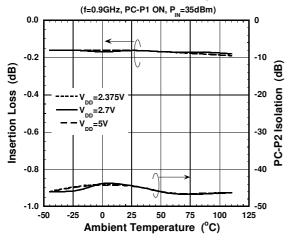
Loss, ISL vs Input Power



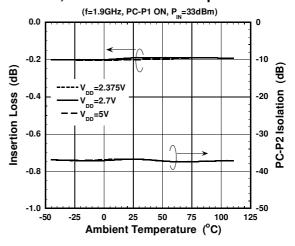
Loss, ISL vs Input Power



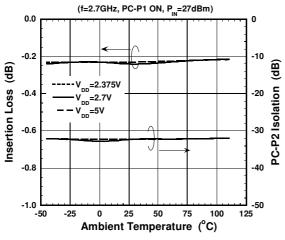
Loss, ISL vs Ambient Temperature



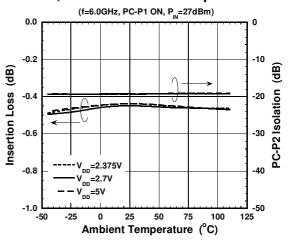
Loss, ISL vs Ambient Temperature



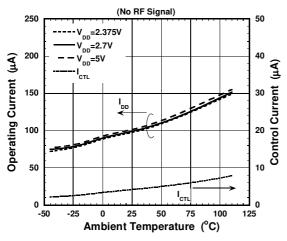
Loss, ISL vs Ambient Temperature



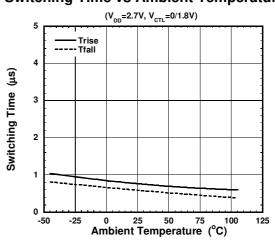
Loss, ISL vs Ambient Temperature



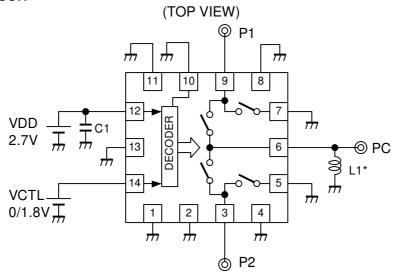
DC Current vs Ambient Temperature



Switching Time vs Ambient Temperature



■ APPLICATION CIRCUIT



Note: No DC blocking capacitors are required on all RF ports, unless DC is biased externally.

* The Inductor L1 is required for enhancing ESD protection level.

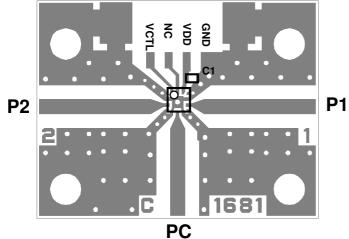
The inductor L1 is recommended in order to keep the DC bias level of each RF port at 0 V level tightly.

■ PARTS LIST

No.	No. Parameters Note	
C1	1000pF	MURATA (GRM15)
L1	68nH	TAIYO-YUDEN (HK1005)

■ PCB LAYOUT





PCB SIZE: 19.4 x 15.0 mm PCB: FR-4, t=0.5mm Capacitor size: 1005

MICROSTRIP LINE WIDTH: 0.98mm

Losses of PCB and connectors, Ta=+25°C

Frequency (GHz)	Loss (dB)
0.9	0.09
1.9	0.18
2.7	0.26
6.0	0.48

■ PRECAUTIONS

- [1] No DC blocking capacitors are required at each RF port normally. When the other device is biased at certain voltage and connected to the NJG1681MD7, a DC block capacitor is required between the device and the switch IC. This is because the each RF port of NJG1681MD7 is biased at 0 V (GND).
- [2] For avoiding the degradation of RF performance, the bypass capacitor (C1) should be placed as close as possible to VDD terminal
- [3] For good RF performance, all GND terminals are must be connected to PCB ground plane of substrate, and through holes for GND should be placed the IC near.

■ RECOMMENDED FOOTPRINT PATTERN (EQFN14-D7 PACKAGE Reference)

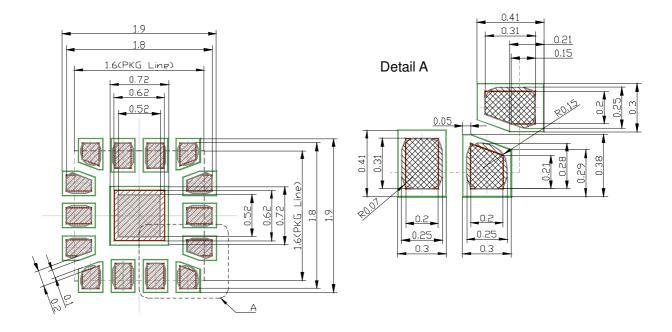
:Land

PKG: 1.6mm x 1.6mm

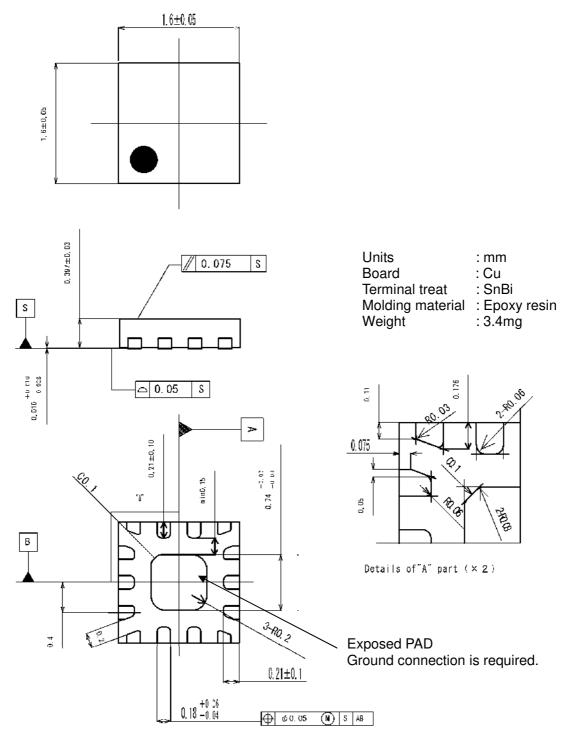
:Mask (Open area) *Metal mask thickness : 100µm

Pin pitch: 0.4mm

:Resist(Open area)



■ PACKAGE OUTLINE (EQFN14-D7)



Cautions on using this product

This product contains Gallium-Arsenide (GaAs) which is a harmful material.

- Do NOT eat or put into mouth.
- Do NOT dispose in fire or break up this product.
- Do NOT chemically make gas or powder with this product.
- To waste this product, please obey the relating law of your country.

This product may be damaged with electric static discharge (ESD) or spike voltage. Please handle with care to avoid these damages.

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 - · Traffic control system
 - Combustion equipment

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- 6. We are making our continuous effort to improve the quality and reliability of our products, but semiconductor products are likely to fail with certain probability. In order to prevent any injury to persons or damages to property resulting from such failure, customers should be careful enough to incorporate safety measures in their design, such as redundancy feature, fire containment feature and fail-safe feature. We do not assume any liability or responsibility for any loss or damage arising from misuse or inappropriate use of the products.
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- 8. Quality Warranty
 - 8-1. Quality Warranty Period
 - In the case of a product purchased through an authorized distributor or directly from us, the warranty period for this product shall be one (1) year after delivery to your company. For defective products that occurred during this period, we will take the quality warranty measures described in section 8-2. However, if there is an agreement on the warranty period in the basic transaction agreement, quality assurance agreement, delivery specifications, etc., it shall be followed.
 - 8-2. Quality Warranty Remedies
 - When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.
 - Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
 - 8-3. Remedies after Quality Warranty Period
 - With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.
- 9. Anti-radiation design is not implemented in the products described in this document.
- 10. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
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- 12. Warning for handling Gallium and Arsenic (GaAs) products (Applying to GaAs MMIC, Photo Reflector). These products use Gallium (Ga) and Arsenic (As) which are specified as poisonous chemicals by law. For the prevention of a hazard, do not burn, destroy, or process chemically to make them as gas or power. When the product is disposed of, please follow the related regulation and do not mix this with general industrial waste or household waste.
- 13. Please contact our sales representatives should you have any questions or comments concerning the products or the technical information.



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