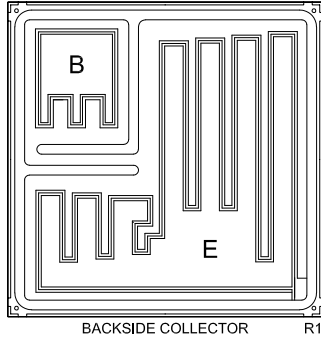


CP247-2N6284

NPN - Darlington Transistor Die

20 Amp, 120 Volt

The CP247-2N6284 is a silicon NPN Darlington power transistor designed for high current applications.



MECHANICAL SPECIFICATIONS:

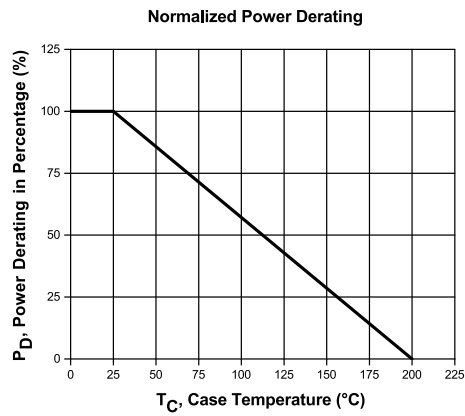
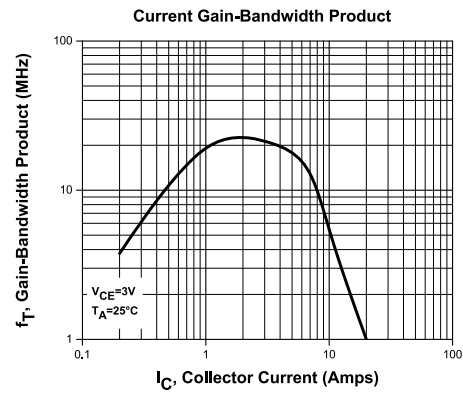
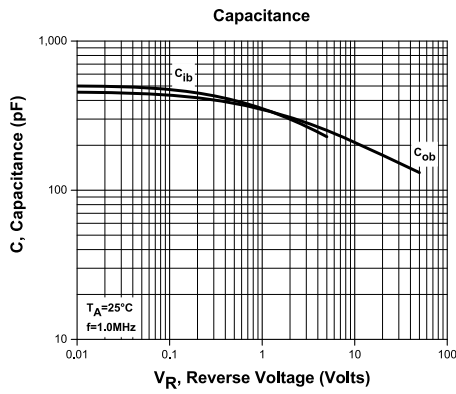
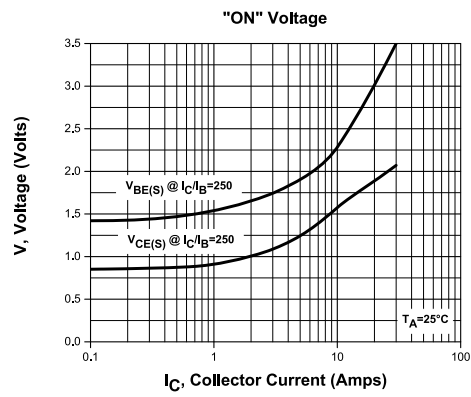
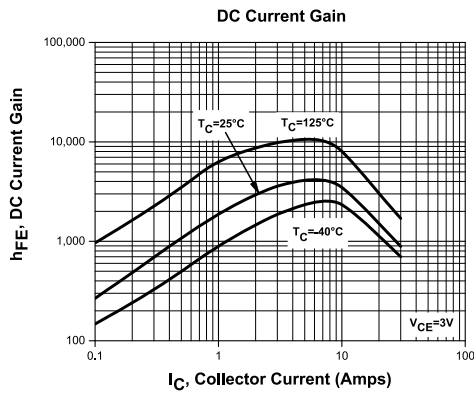
Die Size	211 x 211 MILS
Die Thickness	12.5 MILS
Base Bonding Pad Size	49 x 41 MILS
Emitter Bonding Pad Size	60 x 50 MILS
Top Side Metalization	Al – 50,000Å
Back Side Metalization	Ti/Ni/Ag – 1,000Å/6,000Å/10,000Å
Scribe Alley Width	4 MILS
Wafer Diameter	4 INCHES
Gross Die Per Wafer	246

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	100	V
Collector-Emitter Voltage	V_{CEO}	100	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	I_C	20	A
Peak Collector Current	I_{CM}	40	A
Continuous Base Current	I_B	0.5	A
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +200	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)				
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{CEX}	$V_{CE}=100\text{V}, V_{EB}=1.5\text{V}$		0.5	mA
I_{CEO}	$V_{CE}=50\text{V}$		1.0	mA
I_{EBO}	$V_{EB}=5.0\text{V}$		2.0	mA
BV_{CEO}	$I_C=100\text{mA}$	100		V
$V_{CE(SAT)}$	$I_C=10\text{A}, I_B=40\text{mA}$		2.0	V
$V_{CE(SAT)}$	$I_C=20\text{A}, I_B=200\text{mA}$		3.0	V
$V_{BE(SAT)}$	$I_C=20\text{A}, I_B=200\text{mA}$		4.0	V
$V_{BE(ON)}$	$V_{CE}=3.0\text{V}, I_C=10\text{A}$		2.8	V
h_{FE}	$V_{CE}=3.0\text{V}, I_C=10\text{A}$	750	18K	
h_{FE}	$V_{CE}=3.0\text{V}, I_C=20\text{A}$	100		
h_{fe}	$V_{CE}=3.0\text{V}, I_C=10\text{A}, f=1.0\text{kHz}$	300		
f_T	$V_{CE}=3.0\text{V}, I_C=10\text{A}, f=1.0\text{MHz}$	4.0		MHz
C_{ob}	$V_{CB}=10\text{V}, I_E=0, f=100\text{kHz}$		400	pF

CP247-2N6284

Typical Electrical Characteristics



BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

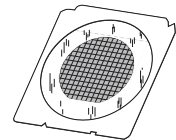
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.
145 Adams Avenue
Hauppauge, NY 11788 USA
Main Tel: (631) 435-1110
Main Fax: (631) 435-1824
Support Team Fax: (631) 435-3388
www.centrasemi.com

Worldwide Field Representatives:
www.centrasemi.com/wwreps

Worldwide Distributors:
www.centrasemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centrasemi.com/terms



<http://www.centrasemi.com>

Product End of Life Notification

PDN ID:	PDN01184
Notification Date:	8/13/21
Last Buy Date:	2/13/22
Last Shipment Date	8/13/22

Summary:CP247 and CP287 die processes are discontinued and now classified as End Life (EOL).

Although Central Semiconductor Corp. makes every effort to continue to produce devices that have been proclaimed EOL (End of Life) by other manufacturers, it is an accepted industry practice to discontinue certain devices when customer demand falls below a minimum level of sustainability. Accordingly, the following product(s) have been transitioned to End of Life status as part of Central's ongoing Product Management Process. Any replacement products are noted below. The effective date for placing last purchase orders will be six (6) months from the date of this notice and twelve (12) months from the notice date for final shipments, and minimum order quantities may apply. The last purchase and shipment dates may be extended if inventory is available.

*** All Plating types (PBFREE,TIN/LEAD) for each item listed are included in this notice.**

<u>Central Part Number</u>	<u>Suggested Replacement</u>
CP247-2N6284-CT	N/A
CP287-MJE13007-CT	N/A

Central would be happy to assist you by providing additional information or technical data to help locate an alternate source if we have no replacement available. Please email your requests to engineering@centrasemi.com.

DISCLAIMER: This End of Life (EOL) notification is in accordance with JEDEC standard JESD48 - Product Discontinuance. Central Semiconductor Corp. will make every effort to offer life-time buy (LTB) opportunities and/or offer replacement devices to existing customers for discontinued devices, however, one or both may not be possible for all devices. Please contact your local Central Semiconductor sales representative for LTB opportunities/additional information.