

V_{DSS}	-12V
$R_{DS(on)}(Max.)$	30m $Ω$
I _D	-4.5A
P_D	1.25W

RT1A045AP

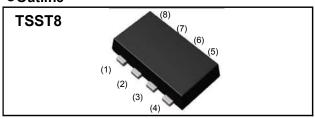
Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSST8).
- 4) Pb-free lead plating; RoHS compliant

Application

DC/DC converters

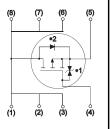
Outline



•Inner circuit

- (1) Drain (5) Source
- (2) Drain (6) Drain
- (3) Drain (7) Drain
- (4) Gate (8) Drain





Packaging specifications

	Packaging	Taping
	Reel size (mm)	180
Tuno	Tape width (mm)	8
Туре	Basic ordering unit (pcs)	3,000
	Taping code	TR
	Marking	SC

● Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	–12	V
Continuous drain current	I _D *1	±4.5	А
Pulsed drain current	I _{D,pulse} *2	±18	А
Gate - Source voltage	V_{GSS}	0 to -8	V
Power dissipation	P _D *3	1.25	W
rower dissipation	P _D *4	0.65	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
Faidnetei	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	100	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	192	°C/W

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol Conditions		Values			Linit	
rarameter Sym		Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = -1mA$	-12	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	-12	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = -12V, V_{GS} = 0V$	-	ı	-10	μА	
Gate - Source leakage current	I _{GSS}	$V_{GS} = -8V, V_{DS} = 0V$	-	-	-10	μА	
Gate threshold voltage	V _{GS (th)}	$V_{DS} = -6V, I_{D} = -1mA$	-0.3	ı	-1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	2.6	-	mV/°C	
		V _{GS} = -4.5V, I _D =-4.5A	-	22	30		
		V _{GS} = -2.5V, I _D = -2.2A	-	28	39		
Static drain - source on - state resistance	R _{DS(on)} *5	V _{GS} = -1.8V, I _D = -2.2A	-	38	57	mΩ	
		V _{GS} = -1.5V, I _D =0.9A	-	50	100		
		V _{GS} = -4.5V, I _D = -4.5A, T _j =125°C	-	34	48		
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	20	_	Ω	
Transconductance	g _{fs} *5	$V_{DS} = -6V, I_D = -4.5A$	5.5	11	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 $\mu s,~Duty~cycle \leq$ 1%

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a FR4(20×20×0.8mm)

^{*5} Pulsed

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Farameter	Parameter Symbol		Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	4200	-	
Output capacitance	C _{oss}	V _{DS} = -6V	-	350	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	330	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq -6V$, $V_{GS} = -4.5V$	-	16	-	
Rise time	t _r *5	I _D = -2.2A	-	60	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 2.7\Omega$	-	400	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	150	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Cymbol	Conditions	Values			Unit	
- Farameter	Symbol Conditions		Min.	Тур.	Max.	OTIIL	
Total gate charge	Q_g^{*5}	$V_{DD} \simeq -6V, I_D = -4.5A$ $V_{GS} = -4.5V$	-	40	-	nC	
Gate - Source charge	Q _{gs} *5	$V_{DD} \simeq -6V, I_{D} = -4.5A$ $V_{GS} = -4.5V$	-	6.5	-	110	
Gate - Drain charge	Q _{gd} *5	V _{GS} = -4.5V	-	6.0	-		

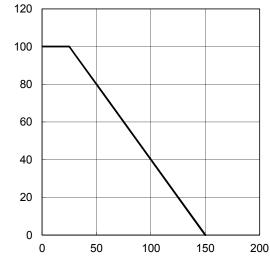
●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions –		Values			Unit
r arameter			Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	– 1	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_s = -4.5A$	-	-	-1.2	V

Power Dissipation: P_D/P_D max. [%]

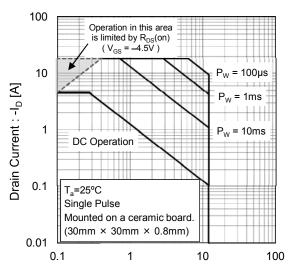
• Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve



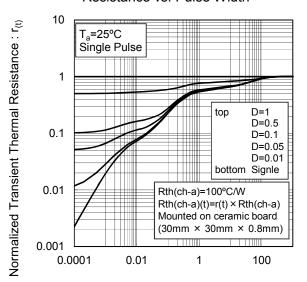
Junction Temperature : Tj [°C]

Fig.2 Maximum Safe Operating Area



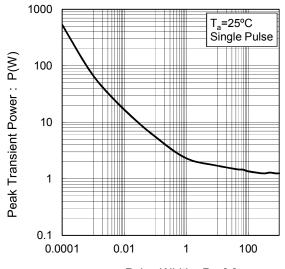
Drain - Source Voltage : -V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



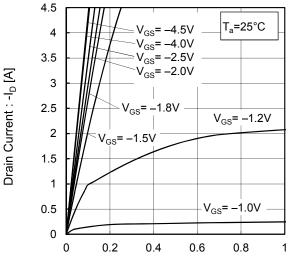
Pulse Width: Pw [s]

Fig.4 Single Pulse Maxmum Power dissipation



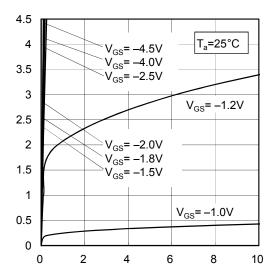
Pulse Width: Pw [s]

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : $-V_{DS}[V]$

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : -V_{DS} [V]

Drain Current : -I_D [A]

Fig.7 Breakdown Voltage

vs. Junction Temperature

30

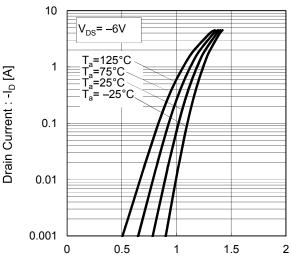
V_{GS}=0V

I_D= -1mA

10

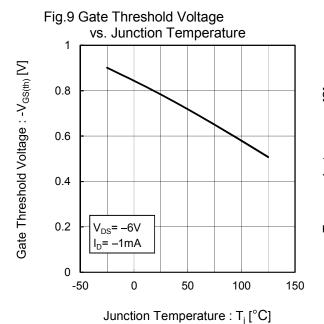
Junction Temperature : T_i [°C]

Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : -V_{GS} [V]

Fig.10 Transconductance vs. Drain Current



100 $V_{DS} = -6V$ $V_{DS} = -6V$

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Drain Current : -I_D [A]

Fig.11 Drain CurrentDerating Curve

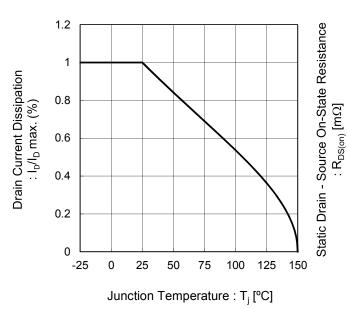
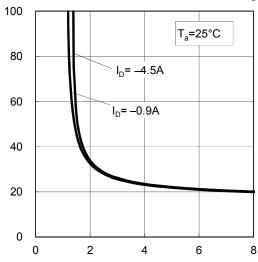


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : -V_{GS} [V]

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

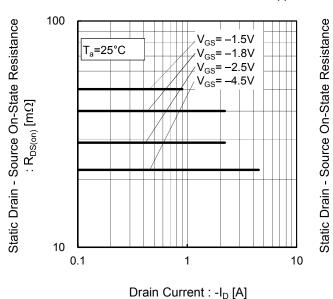
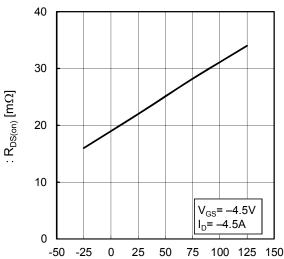


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_j [°C]

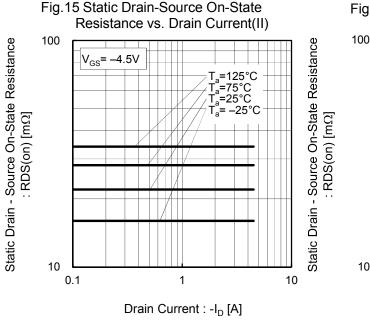
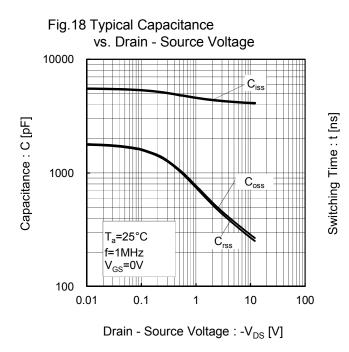


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III) 100 V_{GS}= -2.5V T_a=75°C T_a=25°C T_a= -25°C 10 0.1 1 10 Drain Current : -I_D [A]

Resistance vs. Drain Current(IV) 100 Static Drain - Source On-State Resistance : RDS(on) [m Ω] V_{GS}= -1.8V T_a=125°C T_a=75°C T_a=25°C = -25°C 10 0.1 1 10 Drain Current :-I_D [A]

Fig.17 Static Drain - Source On - State



1000 $T_{a}=25^{\circ}C$ $V_{DD}=-6V$ $V_{GS}=-4.5V$ $R_{G}=10\Omega$ $t_{d(on)}$ $t_{d(on)}$ 0.01 0.1 1 1 10

Drain Current : -I_D [A]

Fig.19 Switching Characteristics

Fig.20 Dynamic Input Characteristics

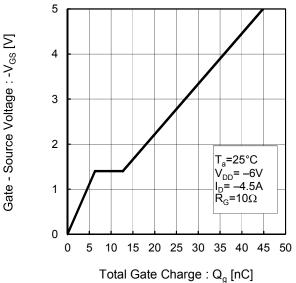


Fig.21 Source Current vs. Source Drain Voltage 10 V_{GS}=0V Source Current: -I_S [A] 0.1 T_a=75°C T = 25°C $T_{a}^{"} = -25^{\circ}C$ 0.01 0.001 0 0.5 1 1.5 Source-Drain Voltage : -V_{SD} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

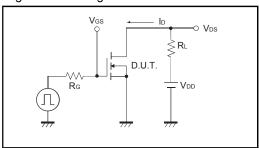


Fig.2-1 Gate Charge Measurement Circuit

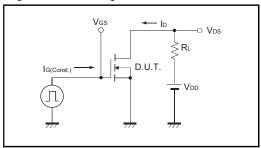


Fig.1-2 Switching Waveforms

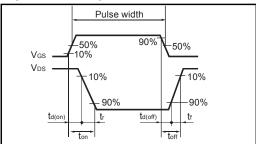
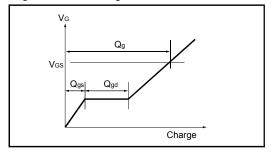
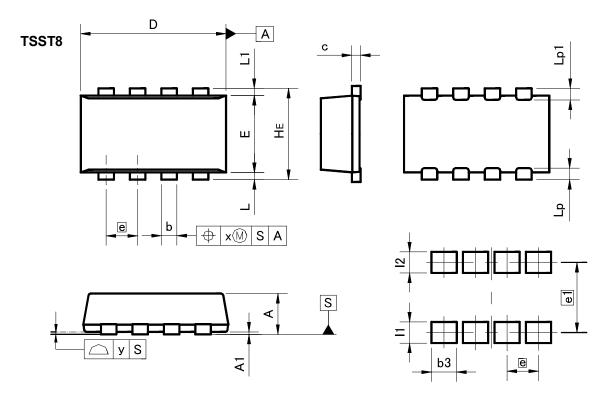


Fig.2-2 Gate Charge Waveform



●Dimensions (Unit : mm)



Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.03	0.033
A1	0.00	0.05	0	0.002
b	0.22	0.42	0.009	0.017
O	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	1.50	1.70	0.059	0.067
е	0.	65	0.03	
HE	1.80	2.00	0.071	0.079
اـ	0.05	0.25	0.002	0.01
L1	0.05	0.25	0.002	0.01
Lp	0.15	0.34	0.006	0.013
Lp1	0.15	0.34	0.006	0.013
х	_	0.10	- 1	0.004
У	_	0.10	_	0.004

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
e1	1.	46	0.0	06
b3	=	0.52	=	0.02
l1	-	0.44	-	0.017
I2	=	0.44	=	0.017

Dimension in mm/inches

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RT1A045AP - Web Page

Distribution Inventory

Part Number	RT1A045AP
Package	TSST8
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes