

FGA50N60LS

General Description

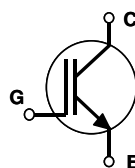
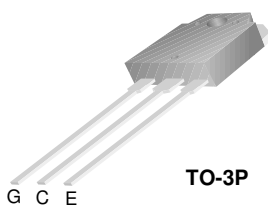
Fairchild's LS series product of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses as well as short circuit ruggedness. The LS series is especially designed for applications in medium frequencies such as switched reluctance motor controls, AC & DC motor controls, general inverters etc.

Features

- Short circuit rated $10\mu\text{s}$ @ $T_C = 100^\circ\text{C}$, $V_{GE} = 15\text{V}$
- Low saturation voltage : $V_{CE(\text{sat})} = 1.6\text{V}$ @ $I_C = 50\text{A}$
- High input impedance
- Optimized for medium operating frequencies (1~5kHz)

Applications

Switched Reluctance Motor Controls , AC & DC motor controls, general purpose inverters, Robotics, and Servo controls



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description	FGA50N60LS	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	100	A
	Collector Current @ $T_C = 100^\circ\text{C}$	50	A
$I_{CM(1)}$	Pulsed Collector Current	150	A
T_{SC}	Short Circuit Withstand Time @ $T_C = 100^\circ\text{C}$	10	μs
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	240	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	96	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	--	0.52	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	40	$^\circ\text{C}/\text{W}$

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	600	--	--	V
ΔBV _{CES} /ΔT _J	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0V, I _C = 1mA	--	0.6	--	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0V	--	--	250	uA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	--	--	± 100	nA
On Characteristics						
V _{GE(th)}	G-E Threshold Voltage	I _C = 50mA, V _{CE} = V _{GE}	3.5	5.5	7.5	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 50A, V _{GE} = 15V	--	1.6	1.8	V
		I _C = 80A, V _{GE} = 15V	--	1.96	--	V
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{CE} = 30V, V _{GE} = 0V, f = 1MHz	--	2660	--	pF
C _{oes}	Output Capacitance		--	250	--	pF
C _{res}	Reverse Transfer Capacitance		--	78	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{CC} = 300 V, I _C = 50A, R _G = 5.9Ω, V _{GE} = 15V, Inductive Load, T _C = 25°C	--	54	--	ns
t _r	Rise Time		--	96	--	ns
t _{d(off)}	Turn-Off Delay Time		--	146	220	ns
t _f	Fall Time		--	326	600	ns
E _{on}	Turn-On Switching Loss		--	1.1	--	mJ
E _{off}	Turn-Off Switching Loss		--	3.2	--	mJ
E _{ts}	Total Switching Loss	--	4.3	6.0	mJ	
t _{d(on)}	Turn-On Delay Time	V _{CC} = 300 V, I _C = 50A, R _G = 5.9Ω, V _{GE} = 15V, Inductive Load, T _C = 125°C	--	56	--	ns
t _r	Rise Time		--	87	--	ns
t _{d(off)}	Turn-Off Delay Time		--	134	215	ns
t _f	Fall Time		--	575	880	ns
E _{on}	Turn-On Switching Loss		--	1.2	--	mJ
E _{off}	Turn-Off Switching Loss		--	5.0	--	mJ
E _{ts}	Total Switching Loss	--	6.2	8.7	mJ	
T _{sc}	Short Circuit Withstand Time	V _{CC} = 300 V, V _{GE} = 15V @ T _C = 100°C	10	--	--	μs
Q _g	Total Gate Charge	V _{CE} = 300 V, I _C = 50A, V _{GE} = 15V	--	167	240	nC
Q _{ge}	Gate-Emitter Charge		--	27	35	nC
Q _{gc}	Gate-Collector Charge		--	68	100	nC
L _e	Internal Emitter Inductance	Measured 5mm from PKG	--	14	--	nH

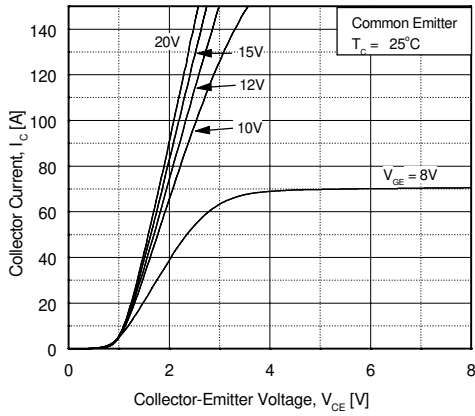


Fig 1. Typical Output Characteristics

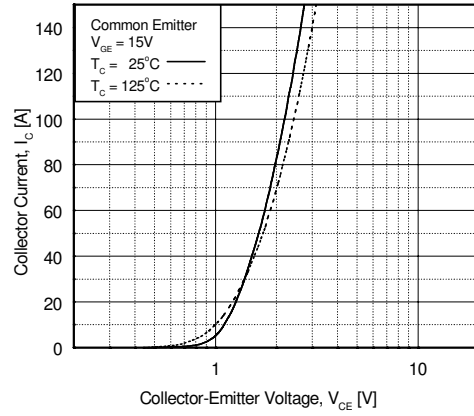


Fig 2. Typical Saturation Voltage Characteristics

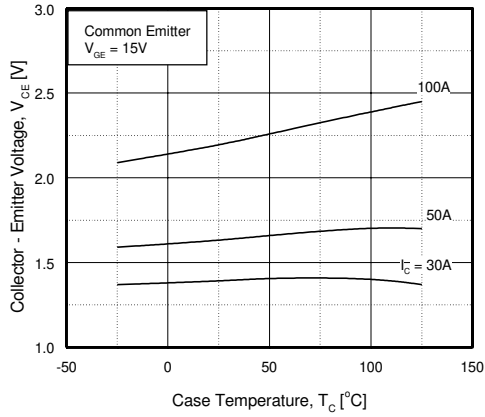


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

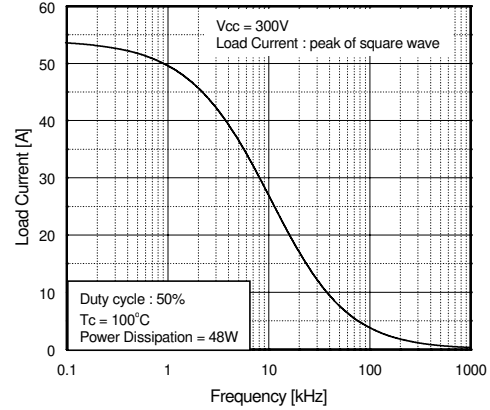


Fig 4. Load Current vs. Frequency

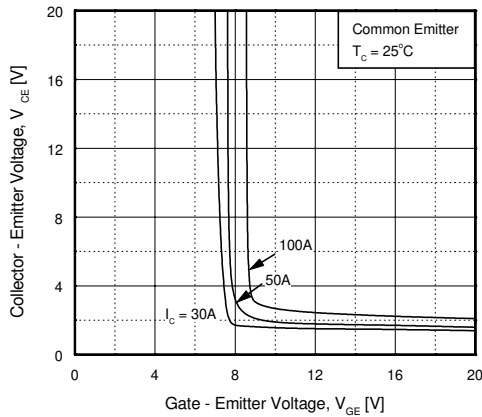


Fig 5. Saturation Voltage vs. V_{GE}

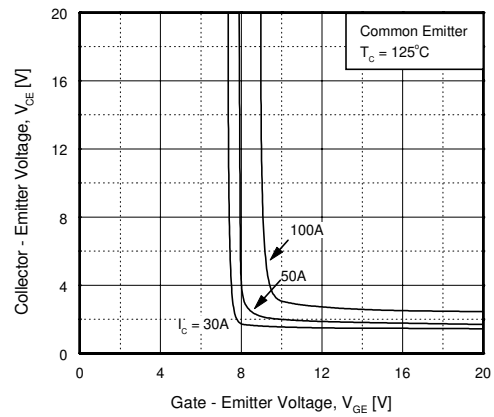


Fig 6. Saturation Voltage vs. V_{GE}

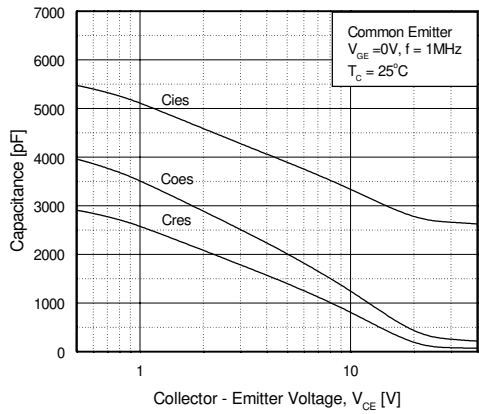


Fig 7. Capacitance Characteristics

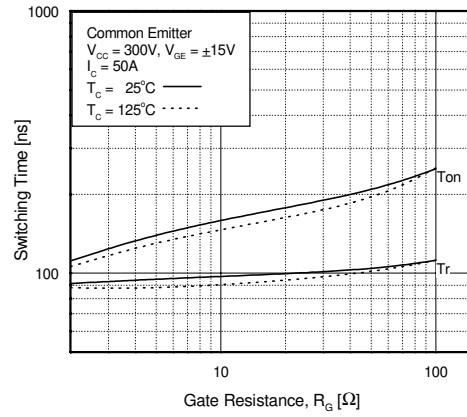


Fig 8. Turn-On Characteristics vs. Gate Resistance

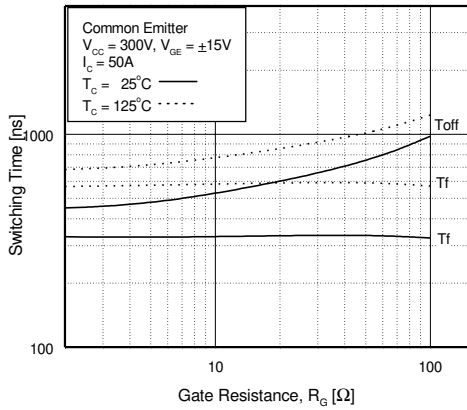


Fig 9. Turn-Off Characteristics vs. Gate Resistance

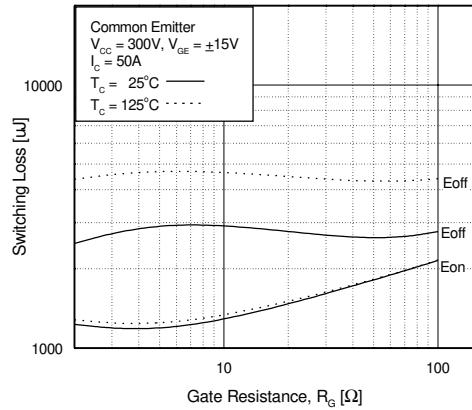


Fig 10. Switching Loss vs. Gate Resistance

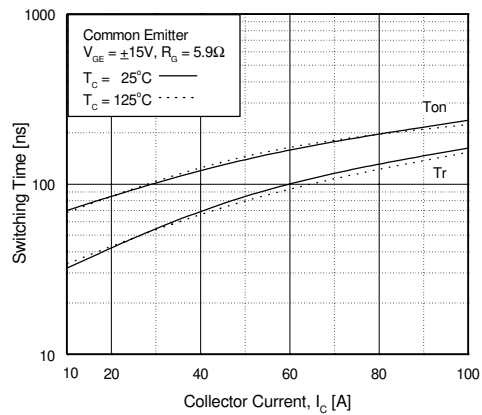


Fig 11. Turn-On Characteristics vs. Collector Current

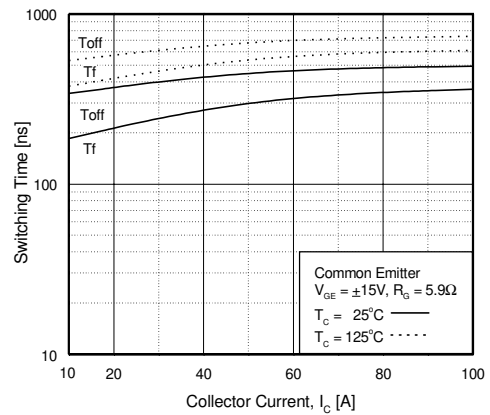


Fig 12. Turn-Off Characteristics vs. Collector Current

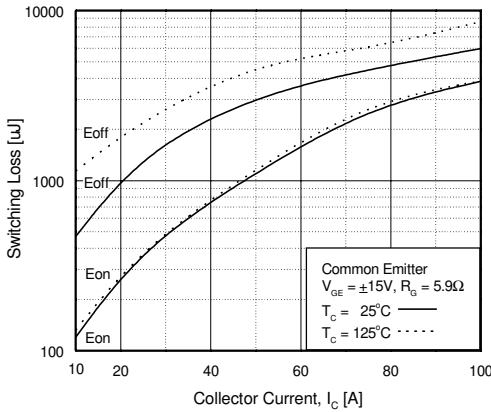


Fig 13. Switching Loss vs. Collector Current

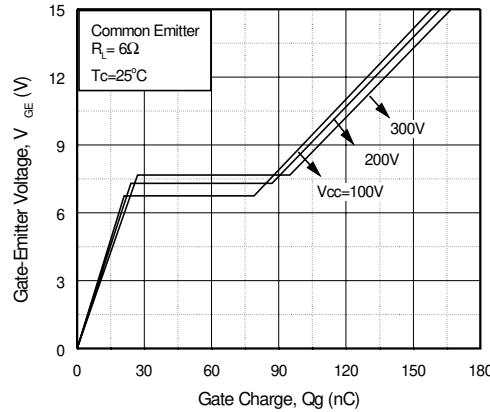


Fig 14. Gate Charge Characteristics

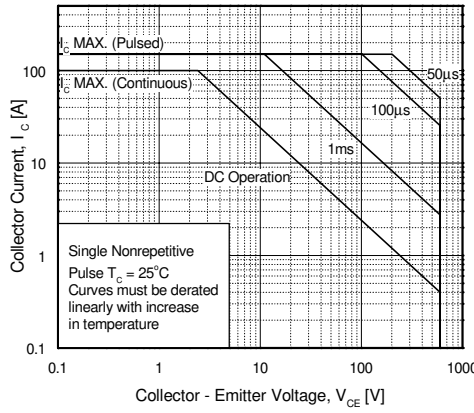


Fig 15. SOA Characteristics

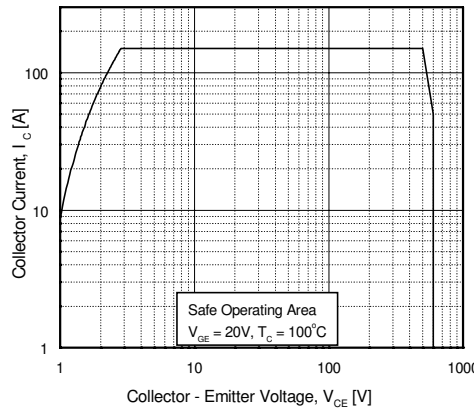


Fig 16. Turn-Off SOA

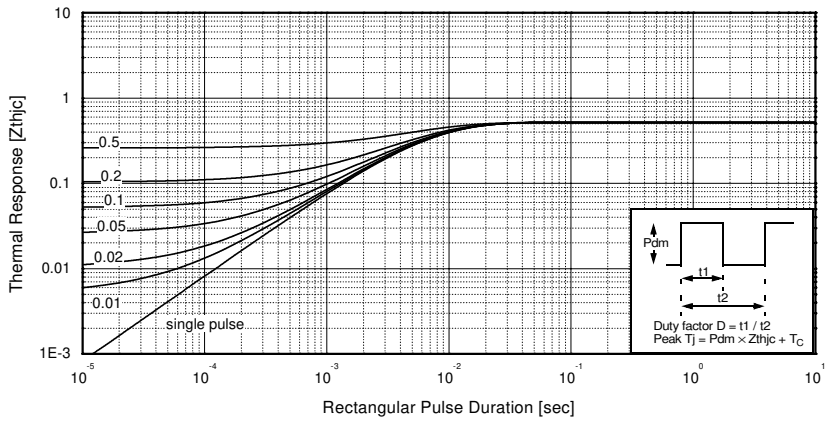
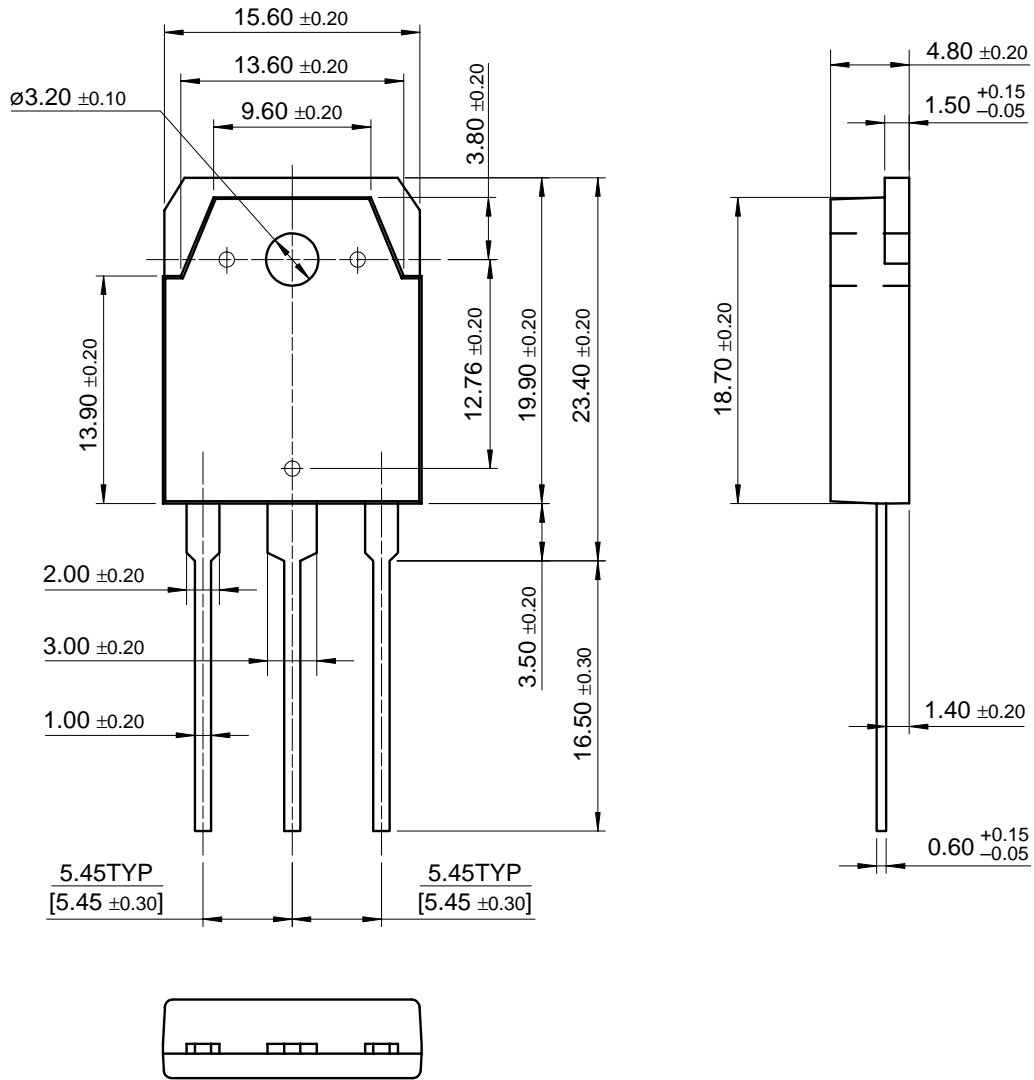


Fig 17. Transient Thermal Impedance of IGBT

Package Dimension

TO-3P



Dimensions in Millimeters

FGA50N60LS

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