Messrs. Digi-Key Corporation

APPROVAL SHEET (KYOCERA CORPORATION SAW FILTER SPECIFICATION)

Kindly send us back a copy of this specification sheet with your signature. The specification shall be regarded as "APPROVED" unless we receive your disagreement or counterproposal before your placement of initial order for the part number specified.

Part No.:SF16-0953M4UU01

Jan, 17, 2011

RoHS Compliant (Pb-Free)

KYOCERA CORPORATION

0.History

No	Date	Notes	Approved	Approved	Approved	Prepared
00	Jan.08 ,2011	First Edition	pulsi	r 7e. Hogon Mi	A.Kabumati	N. Chau
						·

Approved ate 0A

Approved

gastu Production

Approved

Engineering

Prepared.

644

Engineering

1.Scope

This specification shall cover the characteristics of the RF SAW filter.

2.Customer's Part No.

3.KYOCERA's Part No.

SF16-0953M4UU01

<u>SF 16 - 0953 M 4 UU 01</u>

12 3 4567

① Series ② Package Size ③ Frequency ④ Application ⑤ Terminals ⑥ In/Out Condition ⑦ Custom Specification

4.Electrical Characteristics

Terminating Source Impedance : 50 ohms , Single-ended Terminating Load Impedance : 50 ohms , Single-ended Table.1

	tomo	Fraguerov Barga			Unit	Spec.			
	Lenis	Frequency Kange				min.	typ.	max.	
4-1	Norminal Frequency					MHz	-	953	-
4-2	Maximum Insertion Loss	950	to	956	MHz	dB	-	2.5	3.0
4-3	Amplitude Ripple(P-P)	950	to	956	MHz	dB	-	0.1	2.0
	hput VSWR	950	to	956	MHz		-	1.4	2.5
4-4	Output VSWR	950	to	956	MHz		-	1.4	2.5
4-5	Absolute Attenuation	0.3	to	911	MHz	dB	30	43	-
		911	to	931	MHz	dB	20	38	-
		931	to	936	MHz	dB	15	45	-
		936	to	943	MHz	dB	3	12	-
		964	to	971	MHz	dB	3	10	-
		971	to	976	MHz	dB	10	28	-
		976	to	1025	MHz	dB	27	36	-
		1025	to	3000	MHz	dB	27	57	-
4-6 Maximum hput Power					dBm	+ 12			
4-7 Operating Temperature					deg.C	-30 to +85			
4-8 Storage Temperature						deg.C	-40 to +95		

Specifications can change owing to product and/or technical improvements.







9.Environmental Characteristics

ltem	Condition					
Humidity	Subject the filter to 60+/-2 deg.C and 90%RH to 95%RH					
	for 100 hours. Then, release the filter into the room					
	conditions for 2 hours minimum to the measurement.					
	It shall fulfill the specifications in Table 1.					
High Temperature	Subject the filter to 85+/-2 dea.C for 100 Hours.					
Storage	Then, release the filter into the room conditions					
g -	for 2 hours minimum to the measurement.					
	It shall fulfill the specifications in Table 1.					
Low Temperature	Subject the filter to -40+/-2 deg.C for 100Hours.					
Storage	Then, release the filter into the room conditions					
5	for 2 hours minimum to the measurement.					
	It shall fulfill the specifications in Table 1.					
Resistance to	Expose filter to increasing temperature with					
Reflow Solder Heat	a minimum total exposure above 230 deg.C of 30+/-5					
	seconds and must include 2-3 seconds at peak					
	temperature of 250 deg.C, twice.					
	Then, release the filter into the room conditions					
	for 2 hours minimum to the measurement.					
	It shall fulfill the specifications in Table 1.					
Temperature Cycle	10 Cycles (1 cycles:-40 deg.C for 30minutes then					
	25 deg.C for 15m inutes then 85 deg.C for 30 minutes.)					
	An examination is done under the evaluation circuit board					
	mounting condition.					
	Then, release the filter into the room conditions					
	for 2 hours minimum to the measurement.					
	It shall fulfill the specifications in Table 1.					
Vibration	Subject the filter to vibration for 2hour each					
	In the X,Y and Z axes with the amplitude of 1.5mm,					
	10 to 55 Hz/min.					
	It shall fulfill the specifications in Table 1.					
Mechanical Shock1	Subject the filter to 3 shocks in each direction					
	of six mutually perpendicular planes (a total of					
	18 shocks). Each shock shall be a sine wave shaped					
	with a magnitude of 100 G and a duration of 6 mseconds.					
Mashariad Chasko	It shall fulfill the specifications in Table 1.					
Mechanical Shock2	Drop the filter randomly onto a concrete floor					
	from the Height of Im, 3 times.					
EGD	A direct current voltage is increased to DEV/ICE mounted on the					
ESD	A unect current wildge is increased to be vide incurred by the direct					
	evaluation choun board. The failure rate which occurred by the difect					
	As for the voltage it increases with step of F12 series A failure voltage					
	is prescribed in the direct current voltage that an accumulate trouble rate					
	is 0.1% It is judged with the trouble when increase in the insertion loss					
	occurs herond 0.3dB before and after the examination A failure voltage					
	is more than 50V (Fig1)					
<u>Fig1</u>	io ^{SW} io					
· · · · · · · · · · · · · · · · · · ·	ñto l ownt-					
▲						
						DC Source 🐈 🛛 🗸
\perp						
···· ···						
ESD TEST Circuit(Machine Model)						









