### **CPH5871**

### Power MOSFET 30V, $52m\Omega$ , 3.5A, Single N-Channel with Schottky Diode

## ON Semiconductor®

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$V_{DSS}$	R <sub>DS</sub> (on) Max	ID Max
[MOSFET]	52mΩ@ 4.5V	
30V	74mΩ@ 2.5V	3.5A
	132mO@ 1.8V	

### **Features**

- Composite Type with a N-channel Sillicon MOSFET and a Schottky Barrier Diode Contained in One Package Facilitating High-density Mounting
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS Compliance

[MOSFET] • High Speed Switching

• 1.8V Drive

[SBD]

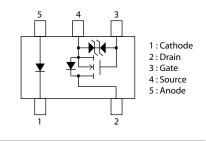
- Short Reverse Recovery Time
- Low Forward Voltage

### **Specifications**

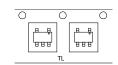
### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Value	Unit
[MOSFET]			
Drain to Source Voltage	V <sub>DSS</sub>	30	V
Gate to Source Voltage	VGSS	±12	V
Drain Current (DC)	ID	3.5	Α
Drain Current (Pulse) PW≤10μs, duty cycle≤1%	I <sub>DP</sub>	14	А
Power Dissipation When mounted on ceramic substrate (600mm² × 0.8mm) 1unit	PD	0.9	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +125	°C
[SBD]			
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	30	V
Nonrepetitive Peak Reverse Surge Voltage	V <sub>RSM</sub>	35	V
Average Output Current	IO	1	Α
Surge Forward Current 50Hz sine wave, 1cycle	IFSM	10	А
Junction Temperature	Tj	-55 to +125	°C
Storage Temperature	Tstg	-55 to +125	°C

### Electrical Connection N-Channel



Packing Type: TL Marking





### **Thermal Resistance Ratings**

Parameter	Symbol	Value	Unit
Junction to Ambient			
When mounted on ceramic substrate	$R_{\theta JA}$	138.8	°C/W
(600mm <sup>2</sup> × 0.8mm) 1µnit		l	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 6 of this data sheet.

### **CPH5871**

### **Electrical Characteristics** at Ta = 25°C

P	Completel	0 1111	Value			
Parameter	Symbol	Conditions	min	typ	max	Unit
[MOSFET]						
Drain to Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	30			٧
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μΑ
Gate to Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μΑ
Gate Threshold Voltage	V <sub>GS</sub> (th)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.4		1.3	V
Forward Transconductance	9FS	V <sub>DS</sub> =10V, I <sub>D</sub> =2A	2.0	3.4		S
	R <sub>DS</sub> (on)1	I <sub>D</sub> =2A, V <sub>GS</sub> =4.5V		40	52	mΩ
Static Drain to Source On-State Resistance	R <sub>DS</sub> (on)2	I <sub>D</sub> =1A, V <sub>GS</sub> =2.5V		53	74	mΩ
	R <sub>DS</sub> (on)3	I <sub>D</sub> =0.5A, V <sub>GS</sub> =1.8V		82	132	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		430		pF
Output Capacitance	Coss			59		pF
Reverse Transfer Capacitance	Crss			38		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			10		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		41		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)			36		ns
Fall Time	t <sub>f</sub>			37		ns
Total Gate Charge	Qg			4.7		nC
Gate to Source Charge	Qgs	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =3.5A		0.8		nC
Gate to Drain "Miller" Charge	Qgd			1.1		nC
Forward Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> =3.5A, V <sub>GS</sub> =0V		0.8	1.2	V
[SBD]						
Reverse Voltage	VR	I <sub>R</sub> =0.5mA	30			V
Forward Voltage	V <sub>F1</sub>	I <sub>F</sub> =0.7A		0.45	0.5	V
i orwaru voltage	V <sub>F2</sub>	I <sub>F</sub> =1A		0.48	0.53	V
Reverse Current	IR	V <sub>R</sub> =16V			15	μА
Interterminal Capacitance	С	V <sub>R</sub> =10V, f=1MHz, 1cycle		27		pF
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = I <sub>R</sub> =100mA, See specified Test Circuit			10	ns

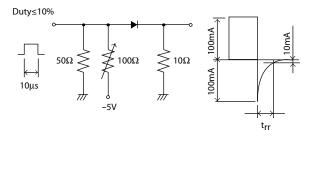
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

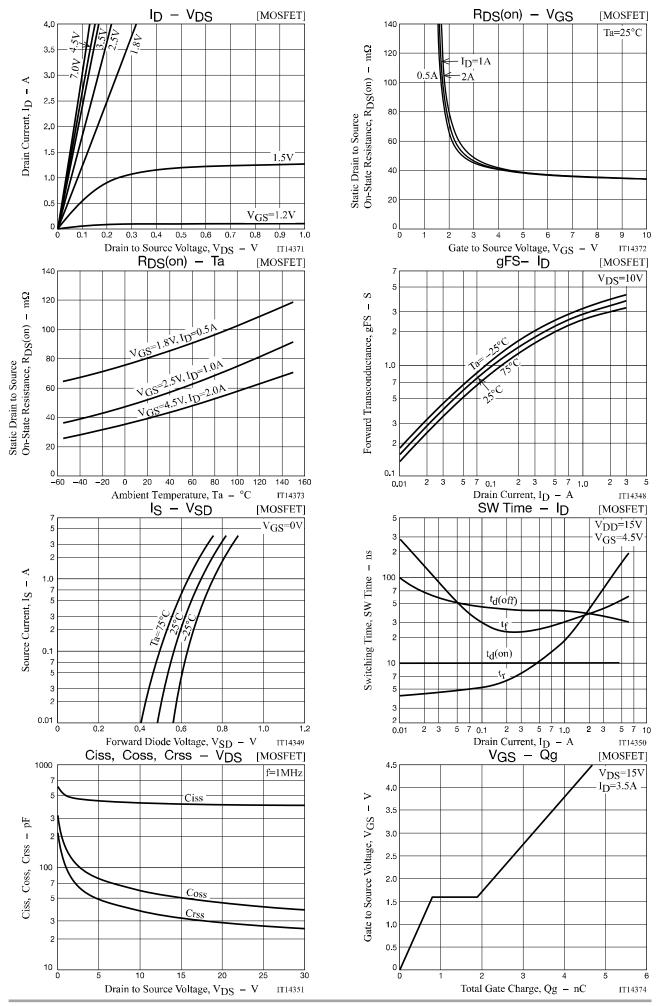
### **Switching Time Test Circuit** (MOSFET)

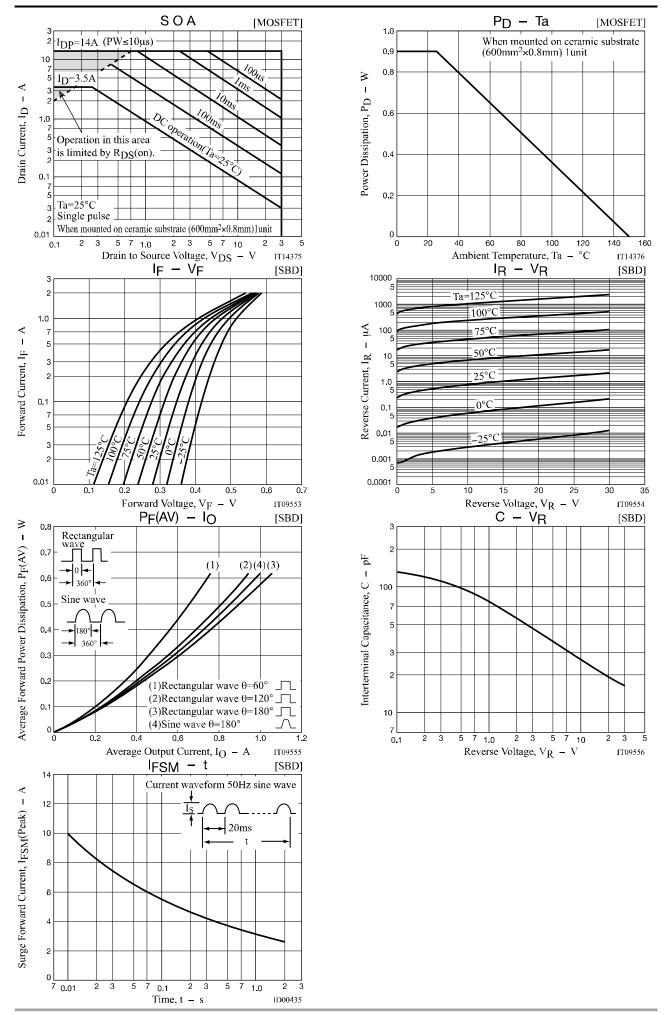
# V<sub>IN</sub> V<sub>DD</sub>=15V 4.5V V<sub>OV</sub> V<sub>IN</sub> V<sub>DD</sub>=2A R<sub>L</sub>=7.5Ω PW=10μs D.C.s1% PG S CPH5871

### trr Test Circuit

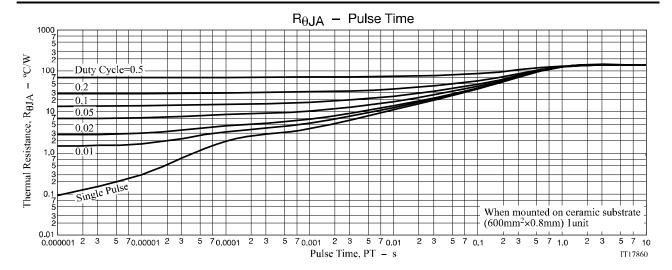
(SBD)







### **CPH5871**



### **Package Dimensions**

CPH5871-TL-H / CPH5871-TL-W

### CPH<sub>5</sub>

CASE 318BC ISSUE O

unit: mm

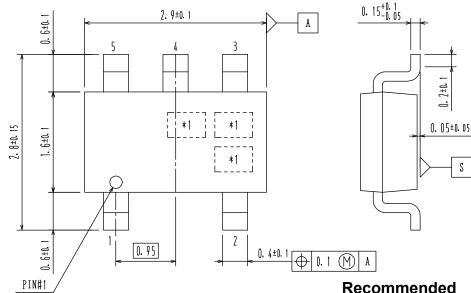
1: Cathode

2: Drain

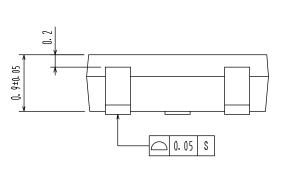
3: Gate

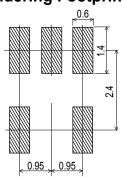
4: Source

5: Anode



### Soldering Footprint





\*1:Lot indication

### ORDERING INFORMATION

Device	Package	Shipping	Note	
CPH5871-TL-H	CPH5	3,000 pcs. / Tape & Reel	Pb-Free and Halogen Free	
CPH5871-TL-W	SC-74A, SOT-25	5,000 pcs. / Tape & Reel		

<sup>†</sup> For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

Note on usage: Since the CPH5871 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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