TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7W32FU, TC7W32FK

Dual 2-Input OR Gate

Features

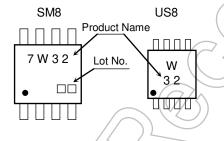
High Speed : t_{pd} = 6ns (typ.) at V_{CC} = 5V Low power dissipation : $I_{CC} = 1\mu A \text{ (max)}$ at $Ta = 25^{\circ}C$ High noise immunity : V_{NIH} = V_{NIL} = 28% V_{CC} (min)

Output drive capability : 10 LSTTL Loads

Symmetrical Output Impedance : $|I_{OH}| = I_{OL} = 4mA$ (min) Balanced propagation delays : t_{pLH} ≒ t_{pHL}

Wide operating voltage range : V_{CC} = 2 to 6V

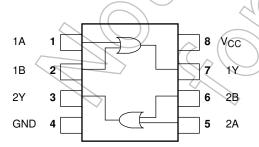
Marking



TC7W32FU SSOP8-P-0.65 (SM8) TC7W32FK SSOP8-P-0.50A (US8) Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Pin Assignment (top view)



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Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	–0.5 to 7.0	V
DC input voltage	V _{IN}	−0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	IIK	±20	mA
Output diode current	lok	±20	mA
DC output current	Гоит	±25	mA
DC V _{CC} /ground current	Icc	±25	∫mA
Power dissipation	Б	300 (SM8)	//
	P _D	200 (US8)	mW
Storage temperature	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C 📈

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

IEC Logic Symbol



Truth Table

	A	В	Y
	L	L	H
	L	<u>/</u> _(Æ)
/	H	\nearrow	Έ
/	¥	\mathbb{F}	Н

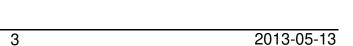
Operating Ranges

Characteristics	Symbol Rating		Unit
Supply voltage	Vcc	2.0 to 6.0	V
Input-voltage	VIN	0 to V _{CC}	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 $(V_{CC} = 6.0 \text{ V})$	

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40	1.124			
		rest	Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				2.0	1.5	_	4	1.5	_	
High-level input voltage	V _{IH}	_		4.5	3.15	_		3.15		
			6.0	4.2		#	4.2		V	
			2.0	_	-(0.5		0.5	V	
Low-level input voltage V	V _{IL}	V _{IL} —		4.5	_ <		1.35	_	1.35	
				6.0	_		1.8	_	1.8	
				2.0	1.9	2.0) —	1.9		
	V _{ОН}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	4.5	44	4.5	_	4.4		
High-level output voltage				6.0	5.9	6.0	_	5.9		
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	-(4.13	> —	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	> - (5.63) —	V
				2.0)	0.0	0.1		0.1	V
			I _{OL} = 20 μA	4.5		0.0	0.1	\ \ 	0.1	
Low-level output voltage	V_{OL}	$V_{IN}=V_{IL} \\$		6.0		0.0) 	_	0.1	
			$I_{OL} = 4 \text{ mA}$	4.5		0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0		0.18	0.26	_	0.33	
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$	or GND	6.0 <	<-	1	±.0.1	_	±1.0	μΑ
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$	or GND	6.0	+	H	1.0	_	10.0	μΑ



AC Characteristics (C_L= 15pF, V_{CC} = 5V, Ta = 25°C)

Chaus staviation	0	Took Condition		l lait		
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output Transition Time	t _{TLH}			4	8	20
	t _{THL}	_		4	0	ns
Propagation Delay Time	t _{pLH}	_	6		12	20
	t _{pHL}		0 12			ns

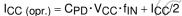
AC Characteristics (C_L = 50pF, Input: t_r = t_f = 6 ns)

Characteristics	Cumbal	Test Condition			Ta = 25°C			Ta = -40 to 85°C		
Characteristics Symbo	Symbol	rest Condition	V _{CC} (V)	Min	Typ.	Max	Min	Max	Unit	
			2.0	4	25	75	4	95		
Output Transition Time	t _{TLH}	_	4.5	\widehat{A}	> 7	15	2-/	19	ns	
t _{THL}	THL		6.0	/ <u>(</u> -))	6 <	> 13 (16		
Propagation delay time	•	_	2.0) $ $	27	75	14/	95		
	i i		4.5	\ \frac{1}{7}	8 /	15)	19	ns	
	tpHL		6.0		7	13)	_	16		
Input capacitance	C _{IN}	+(5	10	_	10	pF	
Power dissipation capacitance	C _{PD}		(Note 1)		21	2)_	_		pF	

Note 1: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:





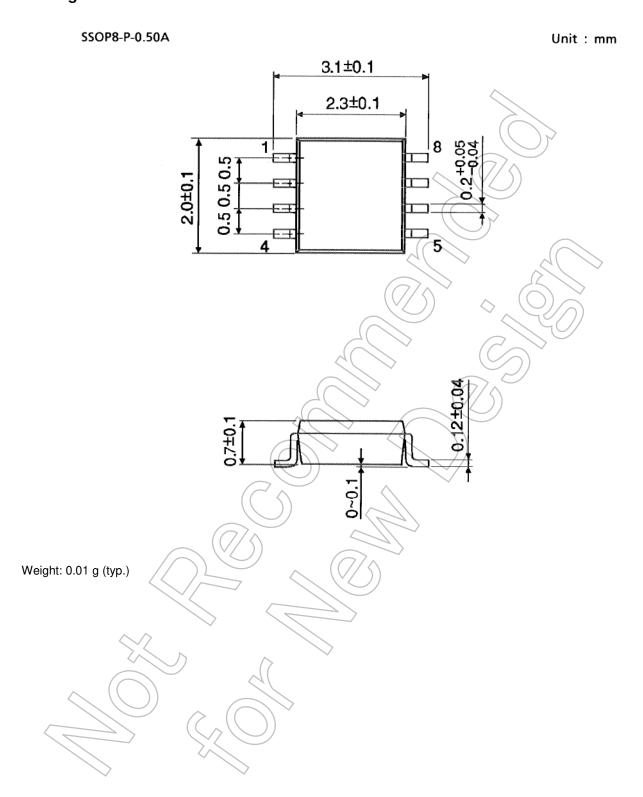


Package Dimensions

SSOP8-P-0.65 Unit: mm 4.0±0.1 2.8±0.1 1 0.650.650.65 2.9 ± 0.1 0.15±0.05 Weight: 0.02 g (typ.)

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Package Dimensions



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