

# Engineering/Process Change Notice

#### ECN/PCN No.: 4113

	For Man	ufacturer			
Product Description: PLASTIC SMD MEMS OSCILLATOR	Abracon Part Numbe	e <b>r / Part Series:</b> MKJ	□ Documentation only □ ECN ☑ EOL	⊠ Series □ Part Number	
Affected Revision:	New Revision: EOL		Application:	□ Safety ⊠ Non-Safety	
Prior to Change: Active https://abracon.com/Oscillators/ASTMKJ.p	bdf				
After Change: EOL					
Cause/Reason for Change: Discontinuation of manufacturing capabilit	ty.				
	Chang	ge Plan			
Effective Date: 2/7/2022	Additional Remarks: N/A				
Change Declaration: N/A					
Issued Date: 2/7/2022	Issued By: Brooke Cushman Product Engineer		Issued Department: Engineering		
Approval: Thomas Culhane Engineering Director	Approval: Reuben Quintanilla Quality Director		Approval: Ying Huang Purchasing Director		
	For Abraco	on EOL only			
Last Time Buy (if applicable): 5/7/2022	Alternate Part Number / Part Series:				
Additional Approval:	Additional Approval:		Additional Approval:		
	Customer Appro	val (If Applicable)			
<b>Qualification Status:</b> Note: It is considered approved if there is n	□ Approved □ The feedback from the cu	☐ Not accepted Istomer 1 month after	r ECN/PCN is released.		
Customer Part Number:		Customer Project:			
Company Name:	Company Representative:		Representative Signature:		
Customer Remarks:					

Form #7020 | Rev. G | Effective: 02/22/2021 |

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🚵 ESD Sensitive

## **ASTMKJ**

#### Moisture Sensitivity Level (MSL) - 1

#### **FEATURES:**

- Ultra-miniature size: 1.54 x 0.84 x 0.6mm
- Supply Voltage: 1.2V to 3.63V (-10 ~ +70°C); 1.5V to 3.63V (-40 ~ +85°C)
- Ultra-Low Current Consumption: 1.4µA max. (core current, no load)
- Frequency Stabilities include:
  - $\pm 75$  ppm over -10 to  $+70^{\circ}$ C
  - $\pm 100$  ppm over -40 to +85°C
- Internal power supply filtering eliminates external bypass capacitor for Vdd port.
- High Performance MEMS Technology by SiTime
- Proprietary NanoDrive<sup>TM</sup> Technology enables programmable output swing for lower power

#### $\triangleright$





#### **APPLICATIONS:**

- Timekeeping
- Battery Management
- Mobile devices
- RTC reference clock
- · Wireless accessories
- · Fitness/Medical monitoring sensors
- · Sport video cams

STANDARD SPECIFICATIONS;					
Parameters	Min	Тур	Max	Unit	Notes
Output Frequency (F <sub>out</sub> )		32.768		kHz	
Initial Frequency Tolerance $(F_{tol})^{(1)(5)}$	-10		+10	ppm	$T_A = +25$ °C, post reflow, $V_{dd}$ .1.5-3.63V
Fraguency Stability over Temperature	-75		+75		$T_A = -10^{\circ}C \text{ to } +70^{\circ}C, V_{dd}: 1.5-3.63V$
( $F_{\rm ex}$ ) <sup>(2)</sup>	cy Stability over Temperature -100 +100 pp	ppm	$T_A = -40^{\circ}C$ to +85°C, $V_{dd}$ :1.5-3.63V		
(1 stab)	-250		+250		$T_{A}$ = -10°C to +70°C, $V_{dd}$ :1.2-1.5V
Aging (@+25°C)	-1		+1	ppm	First year
Supply Voltage (V1)	1.2		3.63	v	$T_{A}$ = -10°C to +70°C
Suppry Voltage (Vad)	1.5		3.63	v	$T_A = -40^{\circ}C$ to $+85^{\circ}C$
		0.90			$T_A = +25$ °C, Vdd: 1.8V. No load.
Core Operating Current ( $I_{dd}$ ) <sup>(3)</sup>			1.3	μA	$T_A = -10^{\circ}$ C to +70°C, $V_{dd}$ max: 3.63V. No load
			1.4		$T_{A} = -40^{\circ}$ C to +85°C, $V_{dd}$ max: 3.63V. No load.
Output Stage Operating Current $(I_{dd out})^{(3)}$		0.065	0.125	$\mu A/V_{pp}$	$T_A = -40^{\circ}$ C to +85°C, V <sub>dd</sub> max: 1.5-3.63V. No load.
Power Supply Ramp (t <sub>vdd Ramp</sub> )			100	ms	$T_{A}$ = -40°C to +85°C, 0 to 90%*V <sub>dd</sub>
$\mathbf{T} = \mathbf{T} = $		180	300		$T_A = -40^{\circ}C \le T_A \le +50^{\circ}C$ , valid output
Start-up Time at Power-up (T <sub>start</sub> ) ("			450	ms	$T_A = +50^{\circ}C \le T_A \le +85^{\circ}C$ , valid output
On eaching Tanga exchange Banger (T. )	-10		+70	°C	Option "M"
Operating reuperature Kange (T <sub>use</sub> )	-40		+85		Option "L"
Period Jitter		35		ns <sub>RMS</sub>	Cycles=10000, T <sub>A</sub> =+25°C, V <sub>dd</sub> :1.5-3.63V
<b>LVCMOS Output Option</b> (T <sub>A</sub> = -40°C	to +85°C. Typ	oical values ar	at $T_A = +25^{\circ}C$	C)	
Output Rise/Fall Time (t./t.)		100	200	ns	10-90%(V <sub>dd</sub> ), 15pF load, V <sub>dd</sub> :1.5- 3.63V
1			50		10-90%( $V_{dd}$ ), 5pF load, $V_{dd} \ge 1.62V$
Output Clock Duty Cycle	48		52	%	
Output Voltage V <sub>OH</sub>	90%*V <sub>dd</sub>			V	V <sub>dd</sub> :1.5-3.63V. I <sub>OH</sub> = -10µA, 15pF
Volume Vo			10%*V <sub>dd</sub>	v	$V_{dd}$ :1.5-3.63V. $I_{OL}$ = 10 $\mu$ A, 15pF
NanoDrive <sup>TM (6)</sup> Programmable, Redu	ced Swing C	Output Option	on		
Output Rise/Fall Time (t <sub>r</sub> /t <sub>f</sub> )			200	ns	30-70%(V <sub>OL</sub> / V <sub>OH</sub> ), 10pF load
Output Clock Duty Cycle	48		52	%	
AC-coupled Programmable Output Swing ( $V_{SW}$ )		0.20 to 0.80		V	ASTMKJ does not internally AC- couple. This output description is intended for a receiver that is AC- coupled. See Part Identification section for available AC-coupled signal swing options. V <sub>dd</sub> :1.5-3.63V. 10pF load,



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## ASTMKJ

**RoHS/RoHS II compliant** 

#### (Continued)

Parameters	Min	Тур	Max	Unit	Notes
DC-biased Programmable Output Voltage High Range ( $V_{OH}$ )		0.60 to 1.225		V	$V_{dd}$ :1.5-3.63V. $I_{OH}$ =-0.2µA.10pF load. See Part Identification section for available $V_{OH}/V_{OL}$ levels.
DC-biased Programmable Output Voltage Low Range (V <sub>OL</sub> )		0.35 to 0.80		V	$V_{dd}$ :1.5-3.63V. $I_{OL}$ =0.2 $\mu$ A.10pF load. See Part Identification section for available $V_{OH}/V_{OL}$ levels.
Programmable Output Voltage Swing Tolerance	-0.055		+0.055	V	$T_A = -40^{\circ}C$ to +85°C, $V_{dd}$ :1.5-3.63V

Note:

- 1. Measured peak-to-peak. Tested with Agilent 53132A frequency counter. Due to the low operating frequency, the gate time must be  $\geq 100$ ms to ensure an accurate frequency measurement.
- Stability is specified for two operating voltage ranges. Stability progressively degrades with supply voltage below 1.5V. 2. Measured peak-to-peak. Inclusive of initial tolerance at +25°C, and variations over operating temperature, rated power supply voltage and load.
- Core operating current does not include output driver operating current or load current. To derive total operating current (no 3. load), add core operating current + output driver operating current, where output driver operating current =  $C_{driver} * V_{out} * F_{out}$ .
- Measured from the time  $V_{dd}$  reaches 1.5V 4.
- Board-level underfill (BLUF) is not recommended as it will cause a shift in the frequency tolerance. NanoDrive<sup>TM</sup> is a SiTime trademark. 5.
- 6.

#### **Absolute Maximum Ratings**

Attempted operation outside the absolute maximum ratings may cause permanent damage to the part. Actual performance of the IC is only guaranteed within the operational specifications, not at absolute maximum ratings.

Parameters	Test Condition	Value	Unit		
Continuous Power Supply Voltage Range (V <sub>dd</sub> )		-0.5 to 3.63	V		
Short Duration Max. Power Supply Voltage (V <sub>dd</sub> )	≤30 minutes	4.0	V		
Continuous Maximum Operating Temperature Range	Vdd:1.5-3.63V	105	°C		
Short Duration Max. Operating Temperature Range	Vdd:1.5-3.63V, ≤30 minutes	125	°C		
Human Body Model (HBM) ESD Protection	JESD22-A114	3000	V		
Charge-Device Model (CDM) ESD Protection	JESD22-C101	750	V		
Machine Model (MM) ESD Protection	JESD22-A115	300	V		
Latch-up Tolerance	JESD78 Compliant				
Mechanical Shock Resistance	Mil 883, Method 2002	10000	g		
Mechanical Vibration Resistance	Mil 883, Method 2007	70	g		
1508 CSP Junction Temperature		150	°C		
Storage Temperature		-65 to +150	°C		
		-	-		



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# 1.54 x 0.84 x 0.60mm

## ASTMKJ

Pb RoHS/RoHS II compliant

### **Block Diagram**





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### **Typical Performance Data (TA=25°C, Vdd=1.8V, unless otherwise stated)**



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## ASTMKJ

#### **OUTLINE DIMENSION:**



**Dimensions: mm** 

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Item	Conditions	
$T_{S}$ MAX to $T_{L}$ (Ramp-up Rate)	3°C/second max	
Preheat		
Temperature Minimum (T <sub>s</sub> MIN)	150°C	
Temperature Typical (T <sub>S</sub> TYP)	175°C	
Temperature Maximum (T <sub>S</sub> MAX)	200°C	
Time (t <sub>s</sub> )	60 - 180 seconds	
Ramp-up Rate $(T_L \text{ to } T_P)$	3°C/second max	
Time Maintained Above		
Temperature (T <sub>L</sub> )	217°C	
Time $(t_L)$	60-150 seconds	
Peak Temperature (T <sub>P</sub> )	260°C max	
Target Peak Temperature (T <sub>P</sub> Target)	255°C	
Time within 5°C of actual peak $(t_P)$	20 - 40 seconds	
Max. Number of Reflow Cycles	3	
Ramp-down Rate	6°C/second max	
Time 25°C to Peak Temperature (t)	8 minutes max	



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