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953401CGT

Category: Desktop Chipsets

Generic Part: 953401

Market Group: PC CLOCK

Description: PC MAIN CLOCK

The ICS953401 is a two chip clock solution for desktop designs using SIS 656/662 style chipsets. When used with a zero delay buffer such as the ICS9P931 for DDR applications it provides all the necessary clocks signals for such a system.

Output Features:

- 2 - Pairs of differential CPUCLKs (differential current mode)
- 1 - Pair of differential SRCCLK (differential current mode)
- 5 - Pairs of differential PCI-Express clocks
- 8 - PCI @ 3.3V, 2 free-running
- 2 - ZCLKs @ 3.3V
- 1- 12/48MHZ @ 3.3V selectable by I2C
- 1- 24/48MHZ, @ 3.3V selectable by I2C
- 3 - REF @ 3.3V, 14.318MHz



Parameters

Package	TSSOP 56 (PA56)
Voltage	3.3 V
Package	TSSOP 56
Speed	NA
Temperature	C
Status	Active
Sample	No
Minimum Order Quantity	2000
Factory Order Increment	2000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	TSSOP 6.10 MM
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Standard
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temperature	240°C
Rebake Conditions	NA
Length	14.0
Mark	G
Width	6.1
Pitch	0.5
Thickness	1.0
Status	Active