

TS Type < for Stratum 3 >

7.0 x 5.0 mm SMD Stratum 3 Voltage Controlled Temperature Compensated Crystal Oscillator

FEATURE

- Typical 7.0 x 5.0 x 1.9 mm ceramic SMD package.
- Stratum 3 (Overall ±4.6ppm including 20 years aging.)
- CMOS and Clipped Sine wave (without DC-cut capacitor) output optional.

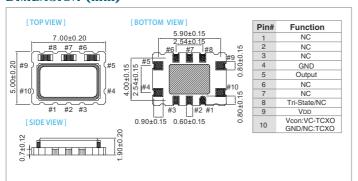
TYPICAL APPLICATION

- Base Stations
- Stratum 3

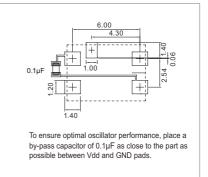
Actual Size

RoHS Compliant

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

| Parameter | 2.5V | | 3.3V | | Unit |
|--|--|-----------|---------------|--------|----------|
| | Min. | Max. | Min. | Max. | Ullit |
| Supply Voltage Variation (VDD) | VDD-5% | VDD+5% | VDD-5% | VDD+5% | V |
| Frequency Range | 5 | 52 | 5 | 52 | |
| Standard Frequency (for CMOS) | 8.192, 10, 12.8, 20 | | | | MHz |
| Standard Frequency (for Clipped sine Wave) | 8.192, 10, 12.8, 16.384, 19.2, 19.44, 20, 25, 26 | | | | |
| Operating Temp. Range | -20 ~ 70 -40 ~ 85 | | | | °C |
| 5 01 1 1111 /O 11 00 W | | | | | |
| Frequency Stability (Overall, 20 Years)* | _ | ±4.6 | | ±4.6 | ppm |
| Frequency Stability Vs Temp. Range | _ | ±0.28 | _ | ±0.28 | ppm |
| Holdover Stability + | _ | ±0.37 | _ | ±0.37 | ppm |
| Supply Current (CMOS output) | _ | 6.0 | _ | 6.0 | mA |
| Supply Current (Clipped Sine Wave) | _ | 3.5 | _ | 3.5 | |
| Output Level (CMOS) | 222/17 | I | | I | 1 |
| Output High (Logic"1") | 90%VDD | _ | 90%VDD | _ | V |
| Output Low (Logic"0") | _ | 10%VDD | _ | 10%VDD | |
| Duty | 45 | 55 | 45 | 55 | % |
| Output Level (Clipped Sine Wave) | 0.8 | _ | 0.8 | _ | Vp-p |
| Load (CMOS) | 15pF | | 15pF | | |
| Load (Clipped Sine Wave) | 10 KΩ // 10pF | | 10 KΩ // 10pF | | |
| Control Voltage Range (VCTCXO) | 0.5 | 2.5 | 0.5 | 2.5 | V |
| Pulling Range (VCTCXO) | ±5.0 | _ | ±5.0 | - | ppm |
| Vc Input Impedance (VCTCXO) | 100 | _ | 100 | _ | kΩ |
| Phase Noise @ 10 MHz 100 Hz | -120 | | -120 | | |
| 1 kHz | -1- | -140 -140 | | 40 | dBc / Hz |
| 10 kHz | -148 | | -148 | | |
| Start Time | _ | 5 | _ | 5 | mSec |
| Tri-State | | | | | |
| Disable | _ | 1.5 | - | 0.99 | V |
| Enable | 3.5 | _ | 2.31 | - | |
| Storage Temp. Range | -55 | 125 | -55 | 125 | °C |

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

Note: not all combination of options are available. Other specifications may be available upon request.

^{*} Including calibration @ 25°C, supply voltage VDD±5%, load 15pF±5%, reflow soldering, 20 years aging and frequency stability over temperature.

⁺ Including 24hours aging , supply voltage VDD±5% and frequency stability over temperature.