# PESD5V0S1BA; PESD5V0S1BB; PESD5V0S1BL

Low capacitance bidirectional ESD protection diodes

Rev. 04 — 20 August 2009

**Product data sheet** 

#### **Product profile** 1.

#### 1.1 General description

Low capacitance ElectroStatic Discharge (ESD) protection diodes in ultra small SMD plastic packages designed to protect one signal line from the damage caused by ESD and other transients.

Table 1.	Product	overview
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Type number	Package		
	Nexperia	JEITA	
PESD5V0S1BA	SOD323	SC-76	
PESD5V0S1BB	SOD523	SC-79	
PESD5V0S1BL	SOD882	-	

#### 1.2 Features

- Bidirectional ESD protection of one line ESD protection > 30 kV
- Max. peak pulse power: P<sub>PP</sub> = 130 W
- Low clamping voltage: V<sub>(CL)R</sub> = 14 V
- Ultra low leakage current: I<sub>RM</sub> = 5 nA

#### 1.3 Applications

- Cellular handsets and accessories
- Portable electronics
- Computers and peripherals

#### 1.4 Quick reference data

#### Table 2. **Quick reference data**

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>RWM</sub>	reverse stand-off voltage		-	-	5	V
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 0 V; f = 1 MHz	-	35	45	pF

IEC 61000-4-2, level 4 (ESD)

- IEC 61000-4-5 (surge); I<sub>PP</sub> = 12 A
- Ultra small SMD plastic packages
- Communication systems
- Audio and video equipment

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### 2. Pinning information

Pin	Description	Simplified outline	Symbol
SOD323, S	SOD523		
1	cathode 1		
2	cathode 2	1 2 001aab540	1 2 sym045
SOD882			
1	cathode 1		
2	cathode 2	1 2	1 2 sym045
		Transparent top view	

# 3. Ordering information

#### Table 4.Ordering information

<b>T</b>							
Type number	Раскаде	Package					
	Name	Description	Version				
PESD5V0S1BA	SC-76	plastic surface mounted package; 2 leads	SOD323				
PESD5V0S1BB	SC-79	plastic surface mounted package; 2 leads	SOD523				
PESD5V0S1BL	-	leadless ultra small plastic package; 2 terminals; body $1.0\times0.6\times0.5$ mm	SOD882				

### 4. Marking

Table 5. Marking codes	
Type number	Marking code
PESD5V0S1BA	E6
PESD5V0S1BB	L7
PESD5V0S1BL	F1

#### Low capacitance bidirectional ESD protection diodes

### 5. Limiting values

#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode						
P <sub>PP</sub>	peak pulse power	8/20 μs	[1][2]	-	130	W
I <sub>PP</sub>	peak pulse current	8/20 μs	[1][2]	-	12	A
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	+150	°C
T <sub>stg</sub>	storage temperature			-65	+150	°C

 Non-repetitive current pulse 8/20 μs exponentially decaying waveform according to IEC61000-4-5; see Figure 1.

[2] Measured from pin 1 to pin 2.

#### Table 7. ESD maximum ratings

Symbol	Parameter	Conditions	Min	Мах	Unit
ESD	electrostatic discharge capability	IEC 61000-4-2 (contact discharge)	[1][2] _	30	kV
		HBM MIL-Std 883	-	10	kV

[1] Measured from pin 1 to pin 2.

[2] Device stressed with ten non-repetitive ElectroStatic Discharge (ESD) pulses; see Figure 2.

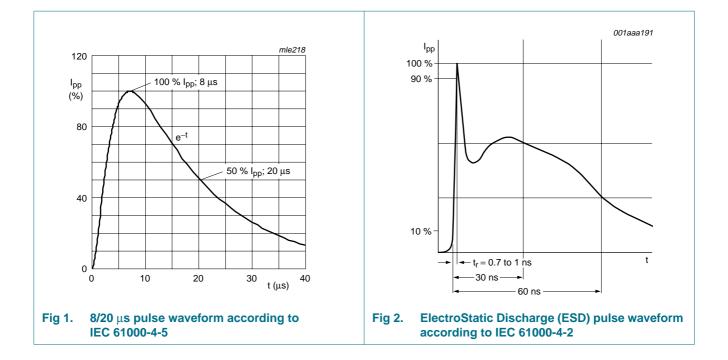
#### Table 8. ESD standards compliance

Standard	Conditions
IEC 61000-4-2, level 4 (ESD); Figure 2	> 15 kV (air); > 8 kV (contact)
HBM MIL-STD 883; class 3	> 4 kV

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# PESD5V0S1BA/BB/BL

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## 6. Characteristics

#### Table 9. Characteristics

 $T_{amb} = 25 \circ C$  unless otherwise specified

	-						
Symbol	Parameter	Conditions	Ν	Min	Тур	Max	Unit
Per diode							
V <sub>RWM</sub>	reverse stand-off voltage		-		-	5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5 V; see <u>Figure 6</u>	-		5	100	nA
V <sub>(CL)R</sub>	clamping voltage	I <sub>PP</sub> = 1 A	[1][2] _		-	10	V
		I <sub>PP</sub> = 12 A	[1][2] _		-	14	V
V <sub>(BR)</sub>	breakdown voltage	I <sub>R</sub> = 1 mA	5	5.5	-	9.5	V
r <sub>dif</sub>	differential resistance	I <sub>R</sub> = 1 mA	-		-	50	Ω
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 0 V; f = 1 MHz; see <u>Figure 5</u>	-		35	45	pF

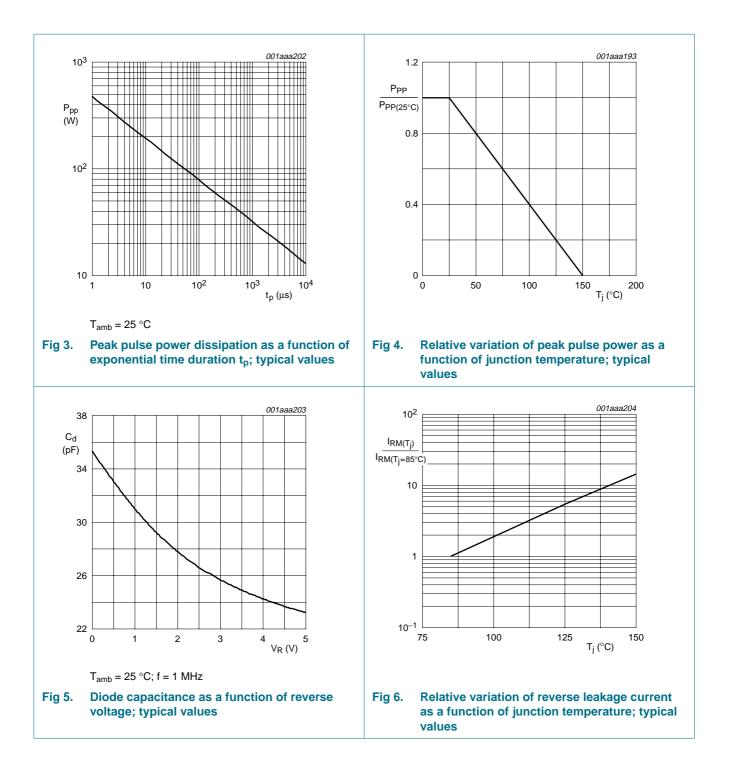
[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC61000-4-5; see Figure 1.

[2] Measures from pin 1 to pin 2.

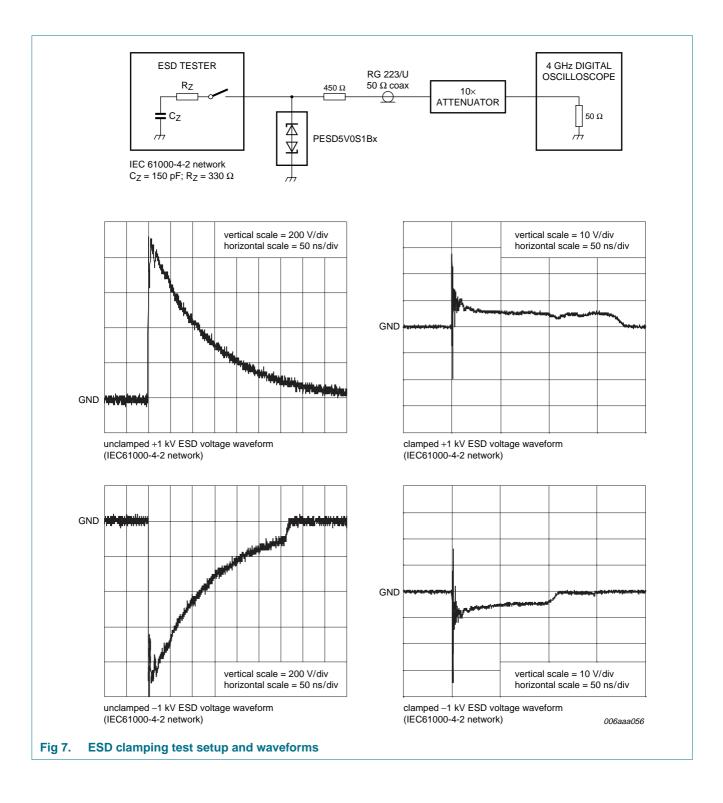
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# PESD5V0S1BA/BB/BL

#### Low capacitance bidirectional ESD protection diodes



#### Low capacitance bidirectional ESD protection diodes

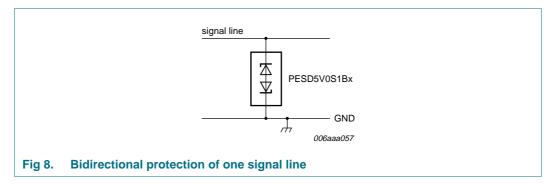


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#### Low capacitance bidirectional ESD protection diodes

### 7. Application information

PESD5V0S1Bx series is designed for the protection of one bidirectional signal line from the damage caused by ElectroStatic Discharge (ESD) and surge pulses. The devices may be used on lines where the signal polarities are above and below ground. They provide a surge capability of up to 130 W per line for a 8/20  $\mu$ s waveform.



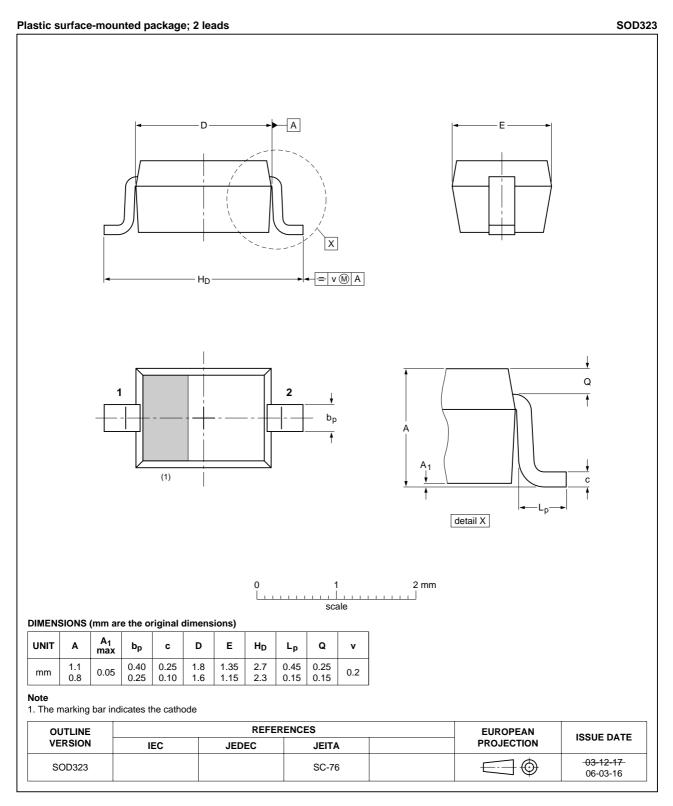
#### Circuit board layout and protection device placement:

Circuit board layout is critical for the suppression of ESD, EFT and surge transients. The following guidelines are recommended:

- 1. Place the protection device as close to the input terminal or connector as possible.
- 2. The path length between the protection device and the protected line should be minimized.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protection conductors in parallel with unprotected conductor.
- 5. Minimize all printed-circuit board conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Ground planes should be used whenever possible. For multilayer printed-circuit boards, use ground vias.

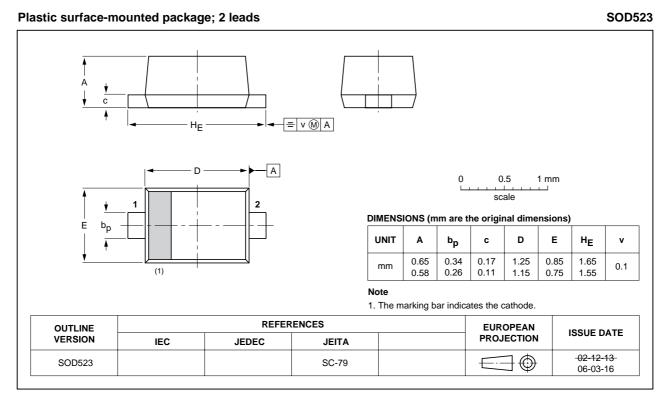
#### Low capacitance bidirectional ESD protection diodes

### 8. Package outline



#### Fig 9. Package outline SOD323 (SC-76)

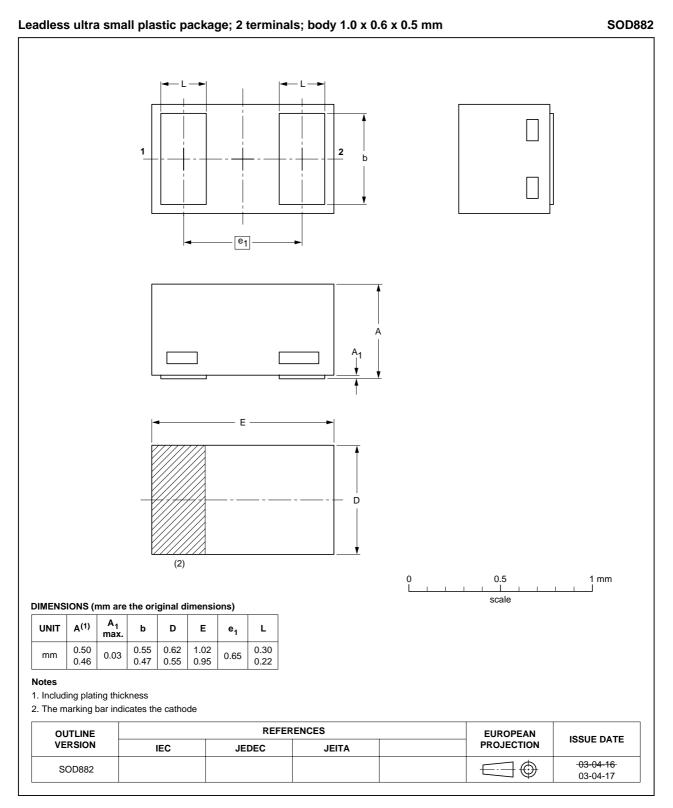
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#### Fig 10. Package outline SOD523 (SC-79)

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#### Fig 11. Package outline SOD882

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### 9. Packing information

#### Table 10. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing qua	antity
			3000	10000
PESD5V0S1BA	SOD323	4 mm pitch, 8 mm tape and reel	-115	-135
PESD5V0S1BB	SOD523	4 mm pitch, 8 mm tape and reel	-115	-135
PESD5V0S1BL	SOD882	4 mm pitch, 8 mm tape and reel	-	-315

[1] For further information and the availability of packing methods, see Section 12.

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### Low capacitance bidirectional ESD protection diodes

# **10. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0S1BA_BB_BL_4	20090820	Product data sheet	-	PESD5V0S1BA_BB_BL_3
Modifications:	including new content. • <u>Table 3 "Pinnir</u> • <u>Figure 9 "Pack</u>	0	imers. No changes 76)": updated	me Nexperia, were made to the technical
PESD5V0S1BA_BB_BL_3	20041217	Product data sheet	-	PESD5V0S1BA_BB_BL_2
PESD5V0S1BA_BB_BL_2	20040802	Product data sheet	-	PESD5V0S1BA_1 PESD5V0S1BB_1
PESD5V0S1BA_1	20040322	Product specification	-	-

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#### **11.1 Data sheet status**

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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For sales office addresses, please send an email to: salesaddresses@nexperia.com

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