

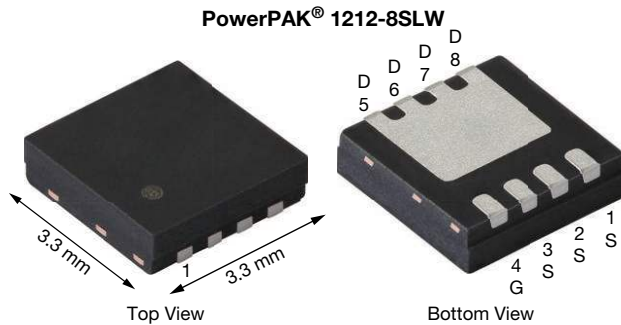
## Automotive N-Channel 60 V (D-S) 175 °C MOSFET



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

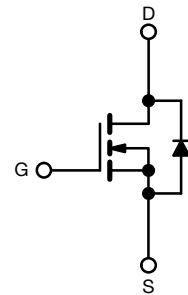
### FEATURES

- TrenchFET® Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % R<sub>g</sub> and UIS tested
- Wettable flank terminals
- Low thermal resistance with 0.75 mm profile
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



Marking code: Q045

PRODUCT SUMMARY	
V <sub>DS</sub> (V)	60
R <sub>DS(on)</sub> (Ω) at V <sub>GS</sub> = 4.5 V	0.0059
R <sub>DS(on)</sub> (Ω) at V <sub>GS</sub> = 10 V	0.0038
I <sub>D</sub> (A)	141
Configuration	Single
Package	PowerPAK 1212-8SLW



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	60	V
Gate-source voltage		V <sub>GS</sub>	± 20	
Continuous drain current	T <sub>C</sub> = 25 °C	I <sub>D</sub>	141	A
	T <sub>C</sub> = 125 °C		81	
Continuous source current (diode conduction)		I <sub>S</sub>	179	
Pulsed drain current <sup>a</sup>		I <sub>DM</sub>	192	
Single pulse avalanche current	L = 0.1 mH	I <sub>AS</sub>	30	
Single pulse avalanche energy		E <sub>AS</sub>	45	mJ
Maximum power dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	P <sub>D</sub>	197	W
	T <sub>C</sub> = 125 °C		66	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Soldering recommendations (peak temperature) <sup>c</sup>			260	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount <sup>b</sup>	R <sub>thJA</sub>	81	°C/W
Junction-to-case (drain)		R <sub>thJC</sub>	0.76	

### Notes

- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- When mounted on 1" square PCB (FR4 material)
- See solder profile ([www.vishay.com/doc?73257](http://www.vishay.com/doc?73257)). A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection



SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA	60	-	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.5	2.0	2.5	
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 60 V	-	-	1	μA
		V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> ≥ 5 V	20	-	-	A
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A	-	0.0038	0.0059	Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	-	0.0025	0.0038	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 125 °C	-	-	0.0095	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C	-	-	0.0113	
Forward transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A	-	90	-	S
<b>Dynamic <sup>b</sup></b>						
Input capacitance	C <sub>iSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz	-	2761	3866	pF
Output capacitance	C <sub>oss</sub>		-	851	1192	
Reverse transfer capacitance	C <sub>rSS</sub>		-	67	94	
Total gate charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 30 V, I <sub>D</sub> = 4 A	-	47	71	nC
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>		-	10	-	
Gate-drain charge <sup>c</sup>	Q <sub>gd</sub>		-	8	-	
Gate resistance	R <sub>g</sub>	f = 1 MHz	0.4	0.9	1.4	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, R <sub>L</sub> = 12 Ω, I <sub>D</sub> ≅ 2.5 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω	-	12	18	ns
Rise time <sup>c</sup>	t <sub>r</sub>		-	4	8	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>		-	32	48	
Fall time <sup>c</sup>	t <sub>f</sub>		-	14	21	
<b>Source-Drain Diode Ratings and Characteristic <sup>b</sup></b>						
Pulsed current <sup>a</sup>	I <sub>SM</sub>		-	-	192	A
Forward voltage	V <sub>SD</sub>	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 V	-	0.82	1.1	V
Body diode reverse recovery time	t <sub>rr</sub>	V <sub>DD</sub> = 48 V, I <sub>FM</sub> = 3 A, di/dt = 100 A/μs, R = 10 Ω, L = 0.3 mH, pulse width = 2 μs	-	38	76	ns
Body diode reverse recovery charge	Q <sub>rr</sub>		-	35	70	nC
Reverse recovery fall time	t <sub>a</sub>		-	19	-	
Reverse recovery rise time	t <sub>b</sub>		-	20	-	
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-1.6	-

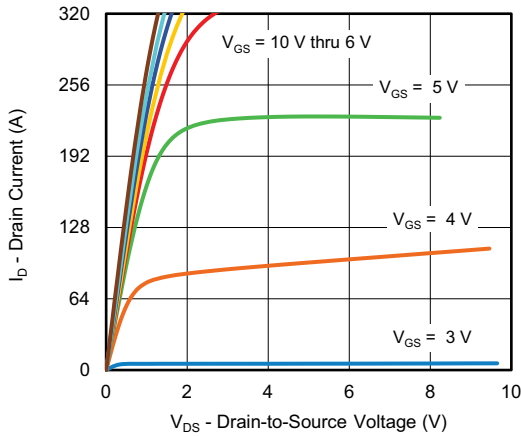
**Notes**

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

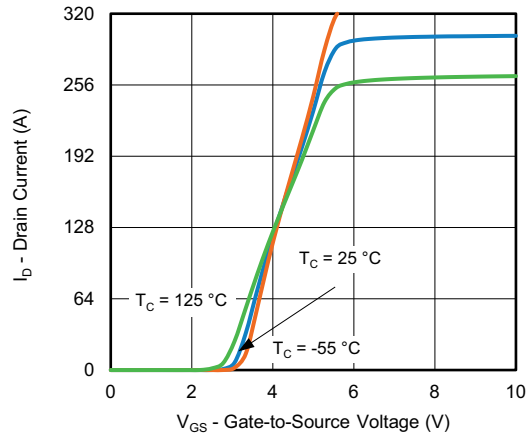
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



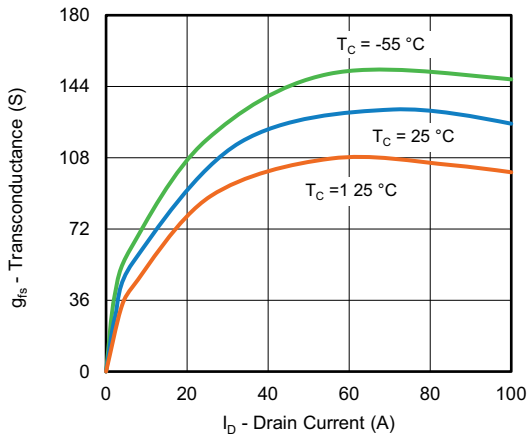
TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



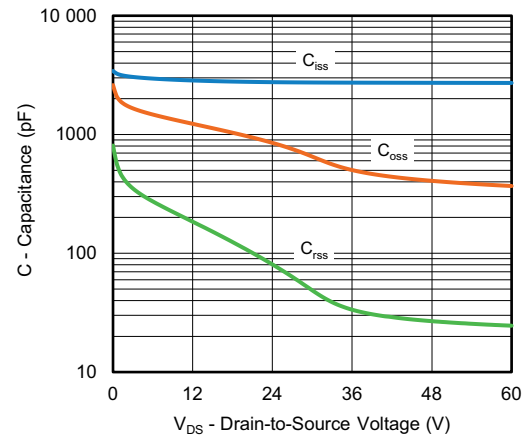
Output Characteristics



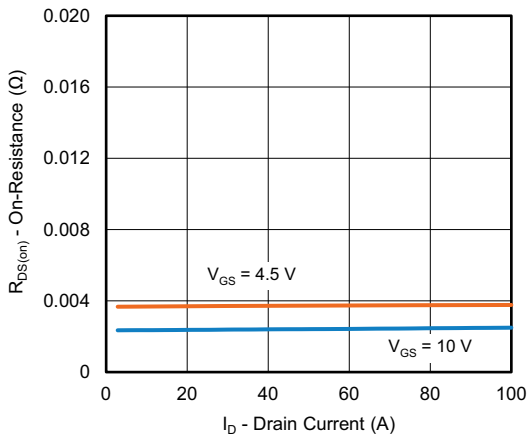
Transfer Characteristics



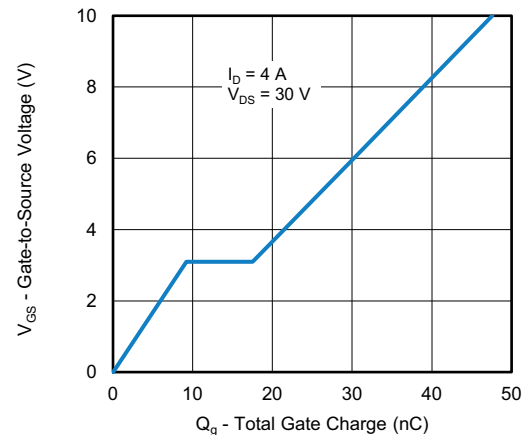
Transconductance



Capacitance

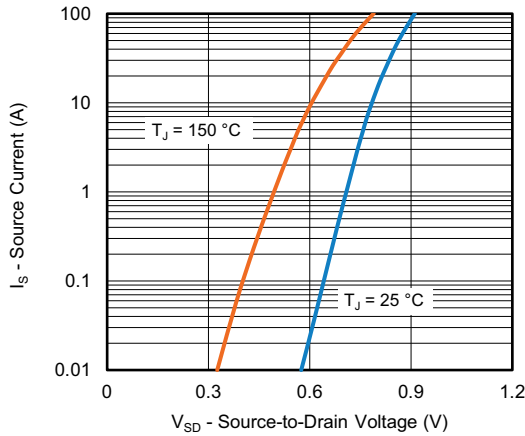


On-Resistance vs. Drain Current

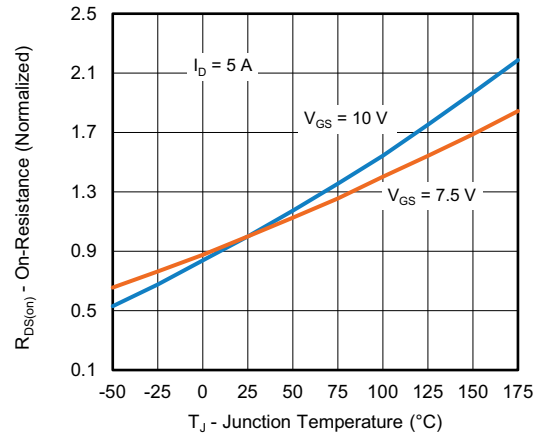


Gate Charge

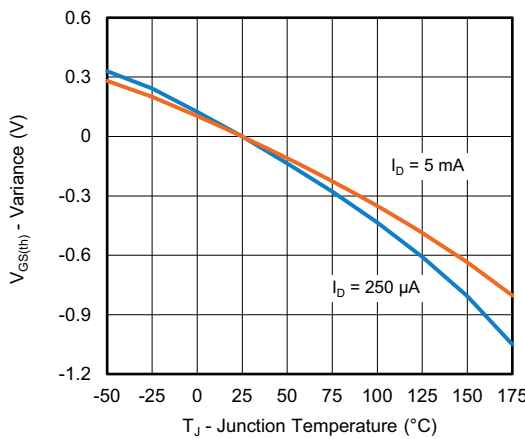
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



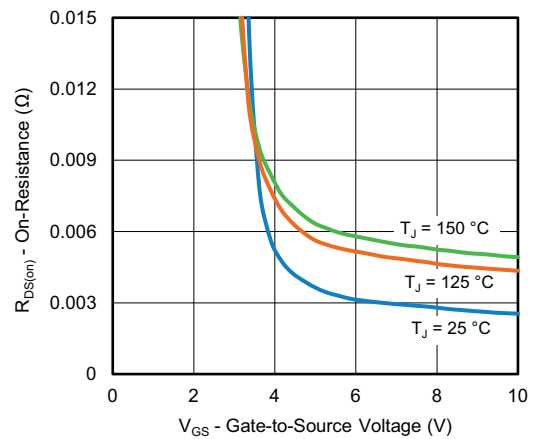
**Source Drain Diode Forward Voltage**



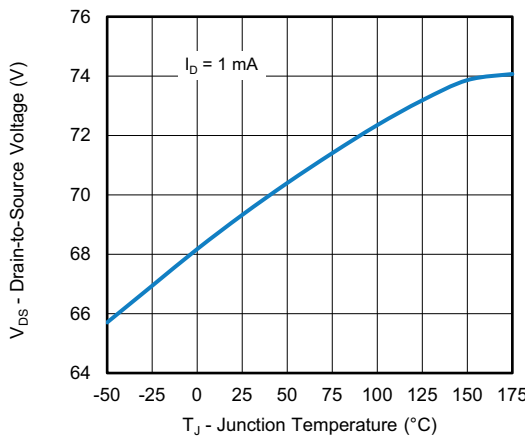
**On-Resistance vs. Junction Temperature**



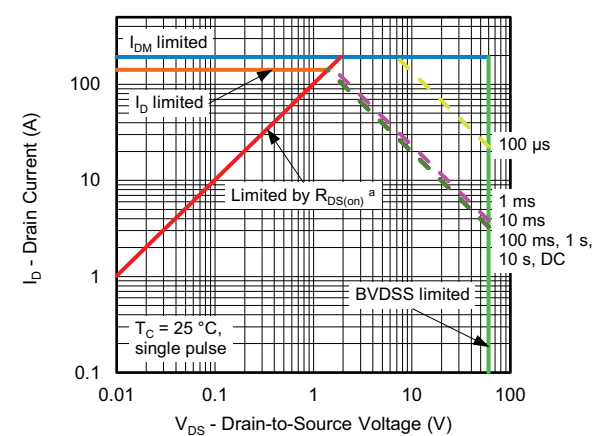
**Threshold Voltage**



**On-Resistance vs. Gate-to-Source Voltage**



**Drain Source Breakdown vs. Junction Temperature**



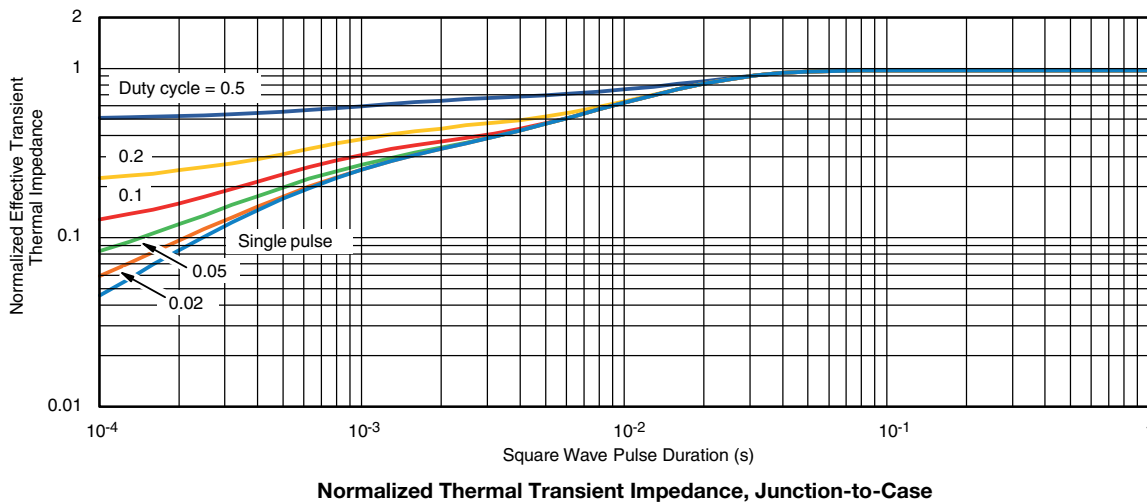
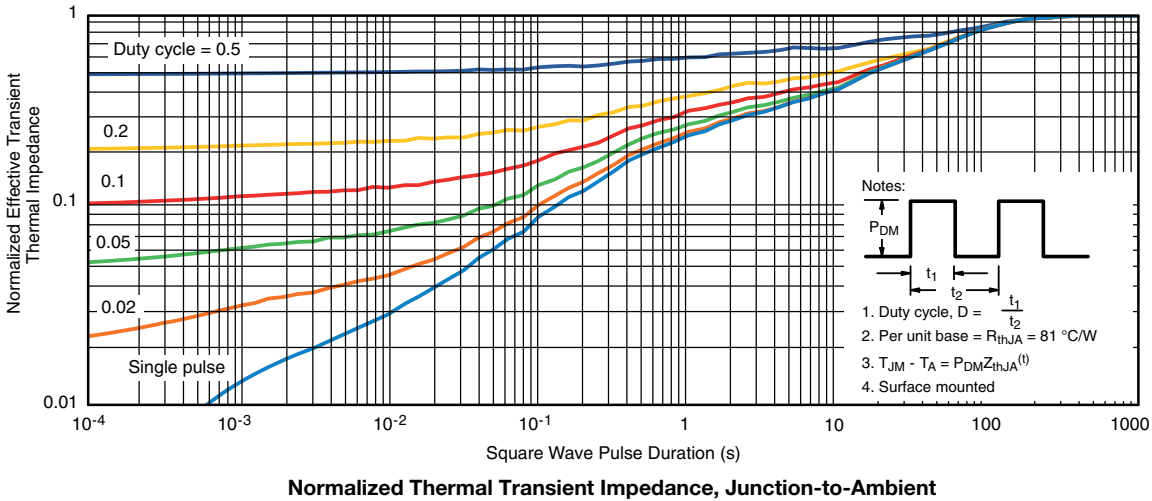
**Safe Operating Area**

**Note**

a.  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

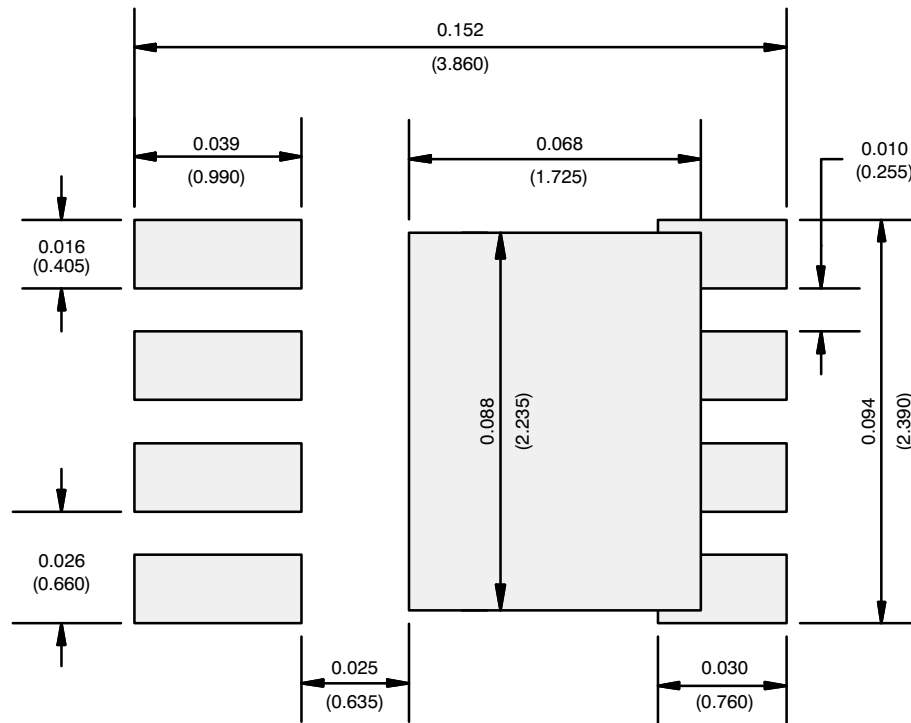


**Note**

- The characteristics shown in the two graphs
    - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25\text{ }^\circ\text{C}$ )
    - Normalized Transient Thermal Impedance Junction-to-Case ( $25\text{ }^\circ\text{C}$ )
- are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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## RECOMMENDED MINIMUM PADS FOR PowerPAK® 1212-8 Single



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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