



Doc.No.: AFY320240B0-3.5N12NTM-C

REV : B

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EFFECTIVE DATE : 2016-5-14

# SPECIFICATION OF LCD MODULE

MODULE NO: AFY320240B0-3.5N12NTM-C

Customer Approval:

Accept

Reject

| FUTURE FOCUS | SIGNATURE | DATE |
|--------------|-----------|------|
| PREPARED BY  |           |      |
| CHECKED BY   |           |      |
| APPROVED BY  |           |      |



Doc.No.: AFY320240B0-3.5N12NTM-C

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### REVISION RECORD

| REV NO. | REV DATE   | CONTENTS  | REMARKS     |
|---------|------------|---|-------------|
| O       | 2015-12-24 | First release   | Preliminary |
| A       | 2016-02-16 | Update the TBD data   | Page4,7     |
| B       | 2016-05-14 | Update the Surface luminance Typ. according to the principle of TFT surface brightness multiplied by 90%. | Page7       |
|         |            |   |             |
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## 1. GENERAL INFORMATION

| No. | Item                           | Contents                       | Unit |
|-----|--------------------------------|--------------------------------|------|
| 1   | LCD size                       | 3.5 inch (Diagonal)            | /    |
| 2   | LCD type                       | TN/Normally white/Transmissive | /    |
| 3   | Viewing direction(eye)         | 12 O'clock                     | /    |
| 4   | Gray scale inversion direction | 6 O'clock                      | /    |
| 5   | Resolution(H*V)                | 320 *240 Pixels                | /    |
| 6   | Module size (L*W*H)            | 76.9*63.9*5.1                  | mm   |
| 7   | Active area (L*W)              | 70.08*52.56                    | mm   |
| 8   | Pixel pitch (L*W)              | 0.219*0.219                    | mm   |
| 9   | Interface type                 | RGB interface                  | /    |
| 10  | Module power consumption       | 0.066(without backlight)       | W    |
| 11  | Back light type                | LED                            | /    |
| 12  | Driver IC                      | HX8238D OR COMPATIBLE          | /    |
| 13  | Weight                         | 49                             | g    |

## 2. ABSOLUTE MAXIMUM RATINGS

| Item                             | Symbol | Min  | Max           | Unit |
|----------------------------------|--------|------|---------------|------|
| Power supply input voltage (LCM) | VDD    | -0.3 | 3.6           | V    |
| Backlight current (normal temp.) | ILED   | -    | 50            | mA   |
| Operation temperature            | Top    | -20  | 70            | °C   |
| Storage temperature              | Tst    | -30  | 80            | °C   |
| Humidity                         | RH     | -    | 90%(Max60 °C) | RH   |

## 3. ELECTRICAL CHARACTERISTICS

### DC CHARACTERISTICS (at Ta=25°C)

| Item                                   | Symbol | Min    | Typ | Max    | Unit | Note |
|--|--------|--------|-----|--------|------|------|
| Power supply input voltage (LCM)       | VDD    | 2.5    | 3.3 | -      | V    |      |
| I/O logic voltage                      | VDDIO  | N/A    | N/A | N/A    | V    |      |
| Input voltage 'H' level                | VIH    | 0.7VDD | -   | VDD    | V    |      |
| Input voltage 'L' level                | VIL    | VSS    | -   | 0.3VDD | V    |      |
| Power supply current                   | IVDD   | -      | 20  | -      | mA   |      |
| TFT gate on voltage                    | VGH    | -      | N/A | -      | V    |      |
| TFT gate off voltage                   | VGL    | -      | N/A | -      | V    |      |
| Analog power supply voltage            | AVDD   | -      | N/A | -      | V    |      |
| Differential input common mode voltage | Vcom   | -      | N/A | -      | V    |      |

## 4. BACKLIGHT CHARACTERISTICS

(at Ta=25°C,RH=60%)

| Item                  | Symbol | Min.                      | Typ.  | Max. | Unit | Note      |
|-----------------------|--------|---------------------------|-------|------|------|-----------|
| LED forward voltage   | VF     | -                         | 9.6   | 10.2 | V    | IF=20*2mA |
| LED forward current   | IF     | -                         | 40    | -    | mA   |           |
| LED power consumption | PLED   | -                         | 0.384 | -    | W    | Note1     |
| Number of LED         | -      | -                         | 6     | -    | PCS  |           |
| Connection mode       | -      | 3 in series 2 in parallel |       | -    | /    |           |
| LED life-time         | -      | 20000                     | -     | -    | Hrs  | Note2     |

Note1.Calculator Value for reference ILED×VLED×LED Quantity = PLED

Note2.The LED Life-time define as the estimated time to 50% degradation of initial brightness at Ta=25°C and IF =40mA. The LED lifetime could be decreased if operating IF is larger than40mA.

## 5. TOUCH PANEL CHARACTERISTICS

(at Ta=25°C)

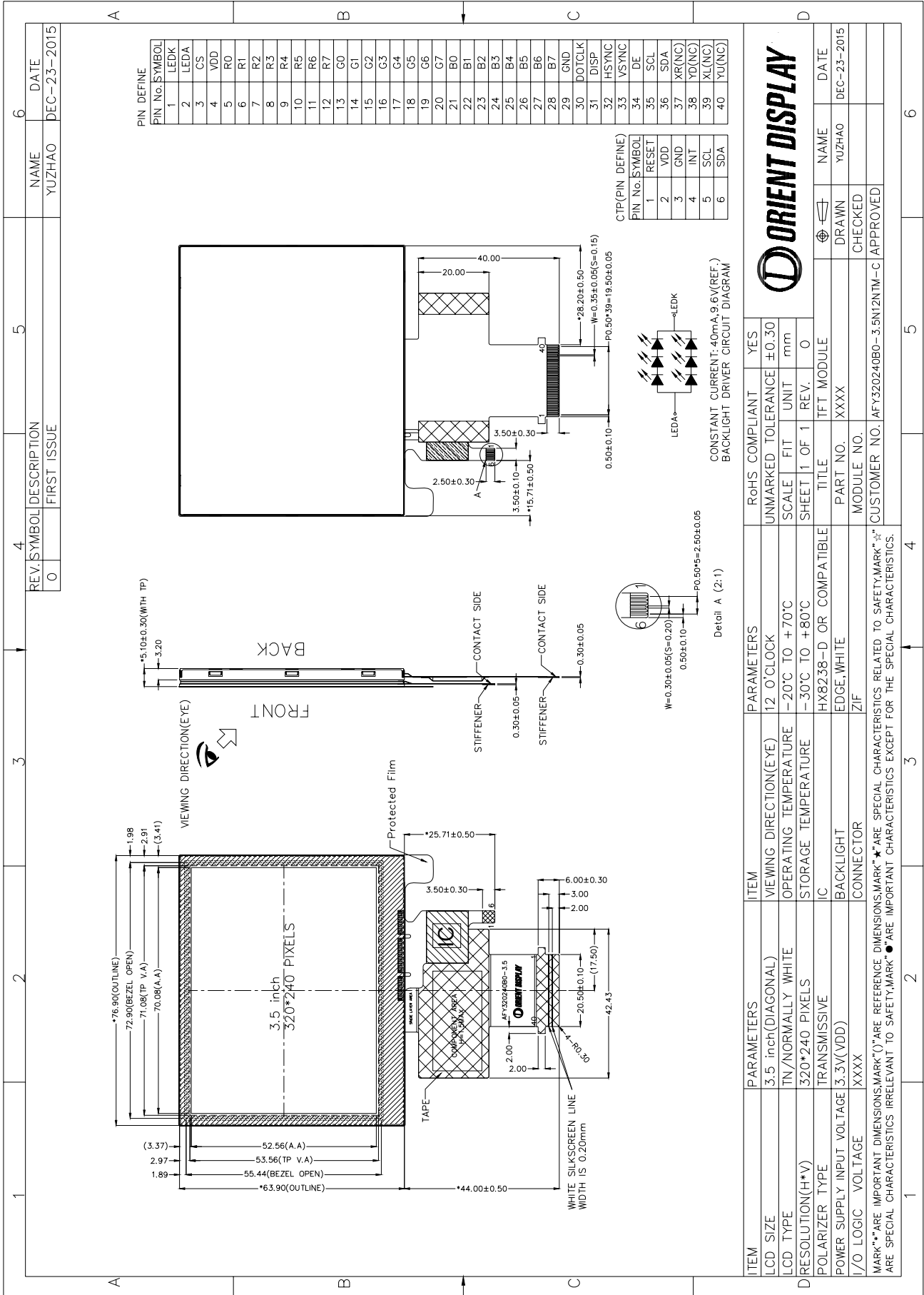
| FPC Design | Item                    | Description     | Note |
|------------|-------------------------|-----------------|------|
| [√] COF    | IC solution on TP Model | FT5416          |      |
|            | Touch Count Max         | 5 point         |      |
|            | Display Resolution*     | 320*240         |      |
|            | Interface Type *        | I2C             |      |
|            | I2C Slave Address*      | 0x70            |      |
|            | Origin of Coordinate*   | top left corner |      |
| [ ] COB    | IC solution on Broad*   |                 |      |
|            | Driving Channels        |                 |      |
|            | Sensing Channels        |                 |      |

| Parameter                 | Min | Typ | Max | Unit |
|---------------------------|-----|-----|-----|------|
| Interface Signal Voltage* | -   | 3.0 | -   | V    |
| Power Voltage*            | 2.8 | 3.0 | 3.3 | V    |
| Power ripple*             | -   | -   | 50  | MV   |

Note1: The detail refer to the Specification For IC

Note2: '\*' means that the item is optional according to the product requirement

### 6. EXTERNAL DIMENSIONS



## 7. ELECTRO-OPTICAL CHARACTERISTICS

| Item                    | Symbol   | Condition  | Min    | Typ    | Max    | Unit              | Remark            | Note  |
|-------------------------|----------|--|--------|--------|--------|-------------------|-------------------|-------|
| Response time           | Tr+ Tf   | -  | -      | 50     | 80     | ms                | FIG 1.            | Note4 |
| Contrast ratio          | Cr       |  | 200    | 300    | -      | ---               | FIG 2.            | Note1 |
| Surface luminance       | Lv       | $\theta=0^\circ$                                     | 350    | 495    | -      | cd/m <sup>2</sup> | FIG 2.            | Note2 |
| Luminance uniformity    | Yu       | $\theta=0^\circ$                                     | 75     | 80     | -      | %                 | FIG 2.            | Note3 |
| NTSC                    | -        | $\theta=0^\circ$                                     | -      | 60     | -      | %                 | FIG 2.            | Note5 |
| Viewing angle           | $\theta$ | $\varnothing = 90^\circ$                             | 50     | 60     | -      | deg               | FIG 3.            | Note6 |
|                         |          | $\varnothing = 270^\circ$                            | 40     | 50     | -      | deg               | FIG 3.            |       |
|                         |          | $\varnothing = 0^\circ$                              | 50     | 60     | -      | deg               | FIG 3.            |       |
|                         |          | $\varnothing = 180^\circ$                            | 50     | 60     | -      | deg               | FIG 3.            |       |
| CIE (x, y) chromaticity | Red x    | $\theta=0^\circ$<br>$\varnothing=0^\circ$<br>Ta=25°C | 0.5706 | 0.6106 | 0.6506 | -                 | FIG 2.<br>CIE1931 | Note5 |
|                         | Red y    |  | 0.3254 | 0.3654 | 0.4054 | -                 |                   |       |
|                         | Green x  |  | 0.3000 | 0.3400 | 0.3800 | -                 |                   |       |
|                         | Green y  |  | 0.5649 | 0.6049 | 0.6449 | -                 |                   |       |
|                         | Blue x   |  | 0.1074 | 0.1474 | 0.1874 | -                 |                   |       |
|                         | Blue y   |  | 0.0387 | 0.0787 | 0.1187 | -                 |                   |       |
|                         | White x  |  | 0.2632 | 0.3032 | 0.3432 | -                 |                   |       |
|                         | White y  |  | 0.2829 | 0.3229 | 0.3629 | -                 |                   |       |

### Note1. Definition of contrast ratio

Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.

$$\text{Contrast Ratio} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

### Note2. Definition of surface luminance

Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

$$L_v = \text{Average Surface Luminance with all white pixels (P1, P2, P3 .....Pn)}$$

### Note3. Definition of luminance uniformity

The luminance uniformity in surface luminance ( $Y_u$ ) is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG 2.

$$Y_u = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3 .....Pn)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3 .....Pn)}}$$

### Note4. Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time ( $T_{ON}$ ) is the time between photo detector output intensity changed from 90% to 10%. And fall time ( $T_{OFF}$ ) is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

### Note5. Definition of color chromaticity (CIE1931)

CIE (x, y) chromaticity, The x, y value is determined by screen active area center position P5. For more information see FIG 2.

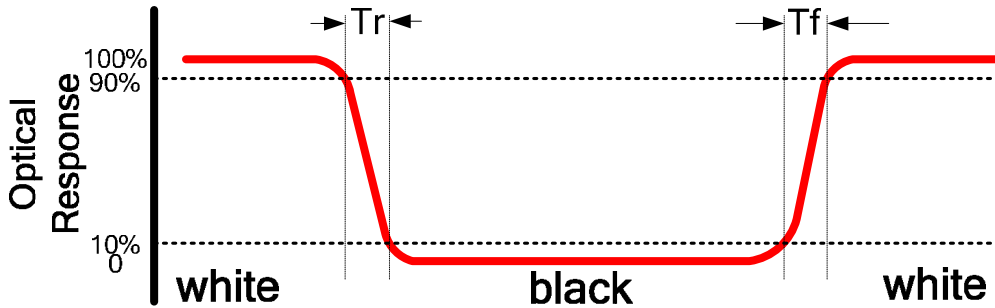
### Note6. Definition of Viewing angle.

Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-7 or BM-5 photo detector or compatible.

**Note:** For TFT module, Gray scale reverse occurs in the direction of panel viewing angle.

**FIG.1. The definition of Response Time**



**FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity**

Size:  $S \leq 5"$  (see Figure a)

A : 5 mm B : 5 mm

H, V : Active Area

Light spot size  $\varnothing = 5\text{mm}$  (BM-5),  $\varnothing = 7.7\text{mm}$  (BM-7) 500mm distance or compatible distance from the LCD surface to detector lens.

test spot position: see Figure a.

measurement instrument : TOPCON's luminance meter

BM-5 or BM-7 or compatible (see Figure c)

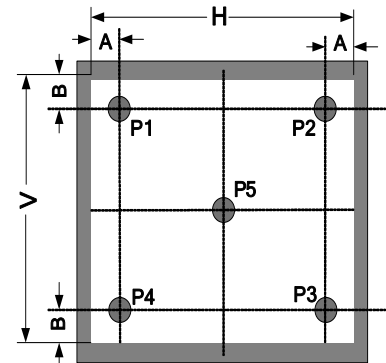


Figure a

Size:  $5" < S \leq 12.3"$  (see figure b)

H, V : Active Area

Light spot size  $\varnothing = 5\text{mm}$  (BM-5),  $\varnothing = 7.7\text{mm}$  (BM-7) 500mm distance or compatible distance from the LCD surface to detector lens

test spot position: see Figure b

measurement instrument : TOPCON's luminance meter

BM-5 or BM-7 or compatible (see Figure c)

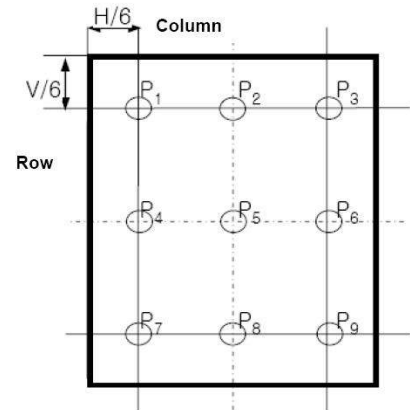


Figure b

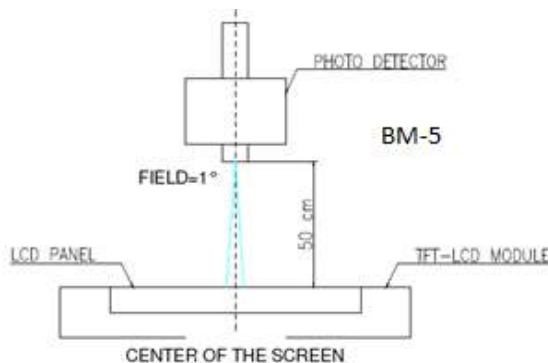
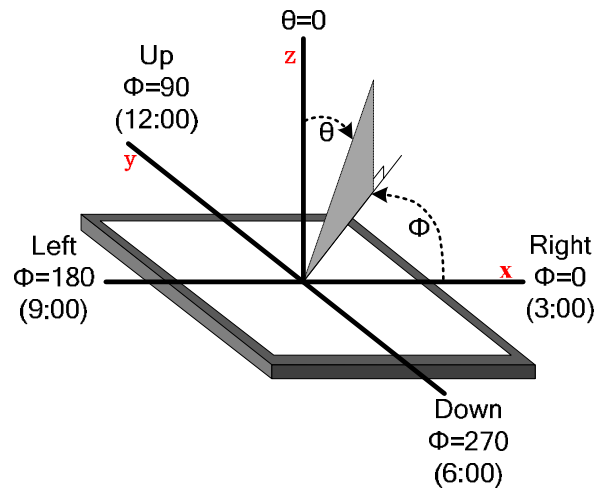


Figure c



**FIG.3. The definition of viewing angle**


## 8. INTERFACE DESCRIPTION

### LCM interface description

| Interface No. | Name       | I/O or connect to | Description                      |
|---------------|------------|-------------------|----------------------------------|
| 1             | LEDK       | P                 | Power for LED backlight(Cathode) |
| 2             | LEDA       | P                 | Power for LED backlight(Anode)   |
| 3             | CS         | I                 | Chip select pin                  |
| 4             | VDD        | P                 | Power for LCD                    |
| 5-12          | Red(0-7)   | I                 | Red data                         |
| 13-20         | Green(0-7) | I                 | Green data                       |
| 21-28         | Blue(0-7)  | I                 | Blue data                        |
| 29            | GND        | I                 | Ground                           |
| 30            | DCLK       | I                 | Dot-clock signal                 |
| 31            | DISP       | I                 | Display on/off                   |
| 32            | HSYNC      | I                 | Horizontal sync input.           |
| 33            | VSYNC      | I                 | Vertical sync input              |
| 34            | DE         | I                 | Data enable                      |
| 35            | SCL        | I                 | Clock pin of serial interface    |
| 36            | SDA        | I/O               | Data input pin in serial mode    |
| 37            | XR(NC)     | -                 | No connection                    |
| 38            | YD(NC)     | -                 | No connection                    |
| 39            | XL(NC)     | -                 | No connection                    |
| 40            | YU(NC)     | -                 | No connection                    |

### CTP interface description

| Interface No. | Name  | I/O or connect to | Description            |
|---------------|-------|-------------------|------------------------|
| 1             | RESET | I                 | Reset low              |
| 2             | VDD   | P                 | Power Supply of CTP    |
| 3             | GND   | P                 | Ground                 |
| 4             | INT   | O                 | State change interrupt |
| 5             | SCL   | I                 | Serial interface clock |
| 6             | SDA   | I/O               | Serial interface date  |

## 9. AC CHARACTERISTICS

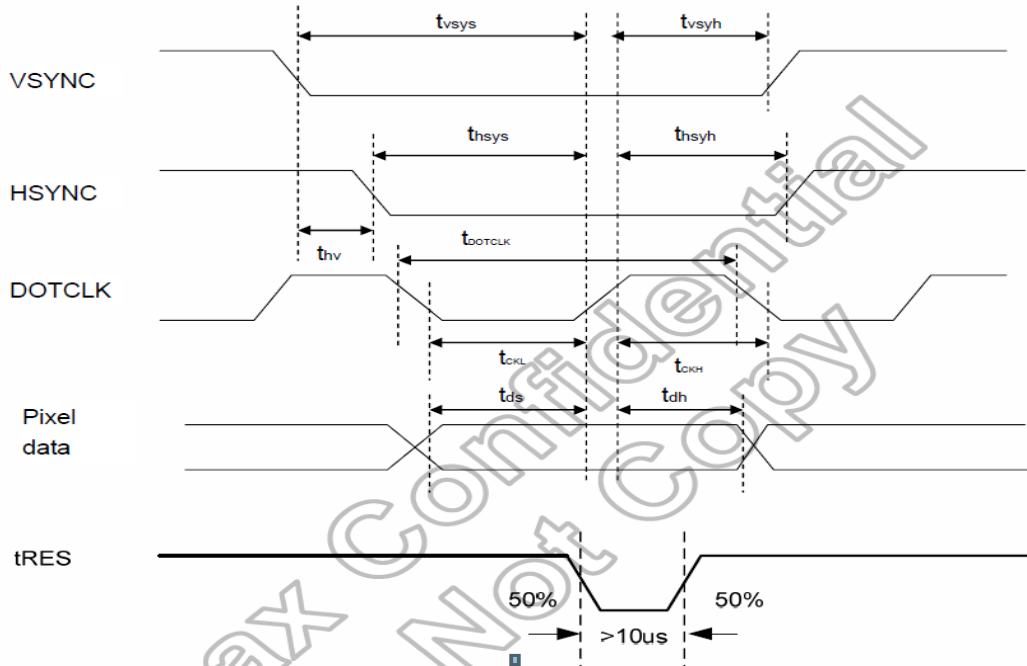
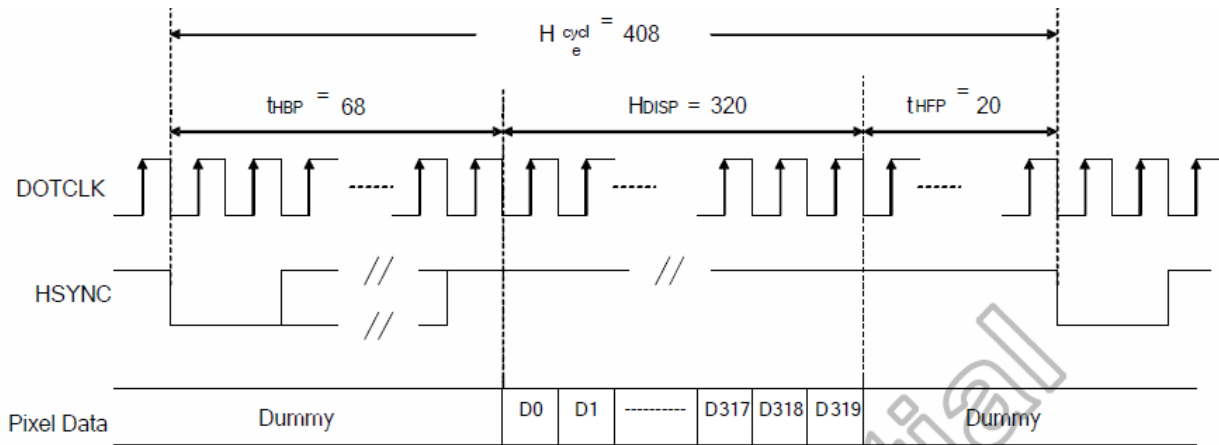


Figure 12. 1 Pixel Timing

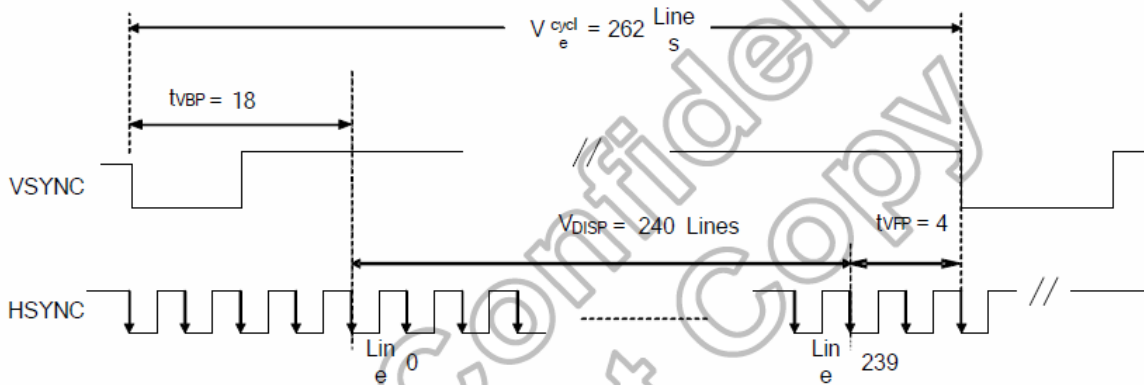
| Characteristics                              | Symbol  | Min.   |       | Typ.   |       | Max.   |       | Unit    |
|--|---------|--------|-------|--------|-------|--------|-------|---------|
|  |         | 24 bit | 8 bit | 24 bit | 8 bit | 24 bit | 8 bit |         |
| DOTCLK Frequency                             | fDOTCLK | -      | -     | 6.5    | 19.5  | 10     | 30    | MHz     |
| DOTCLK Period                                | tDOTCLK | 100    | 33.3  | 154    | 51.3  | -      | -     | ns      |
| Vertical Sync Setup Time                     | tvsys   | 20     | 10    | -      | -     | -      | -     | ns      |
| Vertical Sync Hold Time                      | tvsyh   | 20     | 10    | -      | -     | -      | -     | ns      |
| Horizontal Sync Setup Time                   | thsys   | 20     | 10    | -      | -     | -      | -     | ns      |
| Horizontal Sync Hold Time                    | thsyh   | 20     | 10    | -      | -     | -      | -     | ns      |
| Phase difference of Sync Signal Falling Edge | thv     | 1      |       | -      |       | 240    |       | tDOTCLK |
| DOTCLK Low Period                            | tCKL    | 50     | 15    | -      | -     | -      | -     | ns      |
| DOTCLK High Period                           | tCKH    | 50     | 15    | -      | -     | -      | -     | ns      |
| Data Setup Time                              | tds     | 12     | 10    | -      | -     | -      | -     | ns      |
| Data hold Time                               | tdh     | 12     | 10    | -      | -     | -      | -     | ns      |
| Reset pulse width                            | tRES    | 10     |       | -      |       | -      |       | $\mu s$ |

**Note:** External clock source must be provided to DOTCLK pin of HX8238-D. The driver will not operate if absent of the clocking signal.

Table 12. 1 Pixel Timing



(a) Horizontal Data Transaction Timing



(b) Vertical Data Transaction Timing

Figure 12. 2 Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)

| Characteristics              | Symbol                              | Min.   |       | Typ.        |       | Max.   |       | Unit                |
|------------------------------|-------------------------------------|--------|-------|-------------|-------|--------|-------|---------------------|
|                              |                                     | 24 bit | 8 bit | 24 bit      | 8 bit | 24 bit | 8 bit |                     |
| DOTCLK Frequency             | f <sub>DOTCLK</sub>                 | -      | -     | 6.5         | 19.5  | 10     | 30    | MHz                 |
| DOTCLK Period                | t <sub>DOTCLK</sub>                 | 100    | 33.3  | 154         | 51.3  | -      | -     | ns                  |
| Horizontal Frequency (Line)  | f <sub>H</sub>                      | -      | -     | 14.9        |       | 22.35  |       | KHz                 |
| Vertical Frequency (Refresh) | f <sub>V</sub>                      | -      | -     | 60          |       | 90     |       | Hz                  |
| Horizontal Back Porch        | t <sub>HBP</sub>                    | -      | -     | 68          | 204   | -      | -     | t <sub>DOTCLK</sub> |
| Horizontal Front Porch       | t <sub>HFP</sub>                    | -      | -     | 20          | 60    | -      | -     | t <sub>DOTCLK</sub> |
| Horizontal Data Start Point  | t <sub>HBP</sub>                    | -      | -     | 68          | 204   | -      | -     | t <sub>DOTCLK</sub> |
| Horizontal Blanking Period   | t <sub>HBP</sub> + t <sub>HFP</sub> | -      | -     | 88          | 264   | -      | -     | t <sub>DOTCLK</sub> |
| Horizontal Display Area      | H <sub>DISP</sub>                   | -      | -     | 320         | 960   | -      | -     | t <sub>DOTCLK</sub> |
| Horizontal Cycle             | H <sub>cycle</sub>                  | -      | -     | 408         | 1224  | 450    | 1350  | t <sub>DOTCLK</sub> |
| Vertical Back Porch          | t <sub>VBP</sub>                    | -      | -     | 18          |       | -      |       | Lines               |
| Vertical Front Porch         | t <sub>VFP</sub>                    | -      | -     | 4           |       | -      |       | Lines               |
| Vertical Data Start Point    | t <sub>VBP</sub>                    | -      | -     | 18          |       | -      |       | Lines               |
| Vertical Blanking Period     | t <sub>VBP</sub> + t <sub>VFP</sub> | -      | -     | 22          |       | -      |       | Lines               |
| Vertical Display Area        | NTSC                                | -      | -     | 240         |       | -      |       | Lines               |
|                              | PAL                                 |        |       | 280(PALM=0) |       |        |       |                     |
|                              | PAL                                 |        |       | 288(PALM=1) |       |        |       |                     |
| Vertical Cycle               | NTSC                                | -      | -     | 262         |       | 350    |       | Lines               |
|                              | PAL                                 |        |       | 313         |       |        |       |                     |

Table 12. 2 Data Transaction Timing in Normal Operating Mode

## 10. POWER SEQUENCE

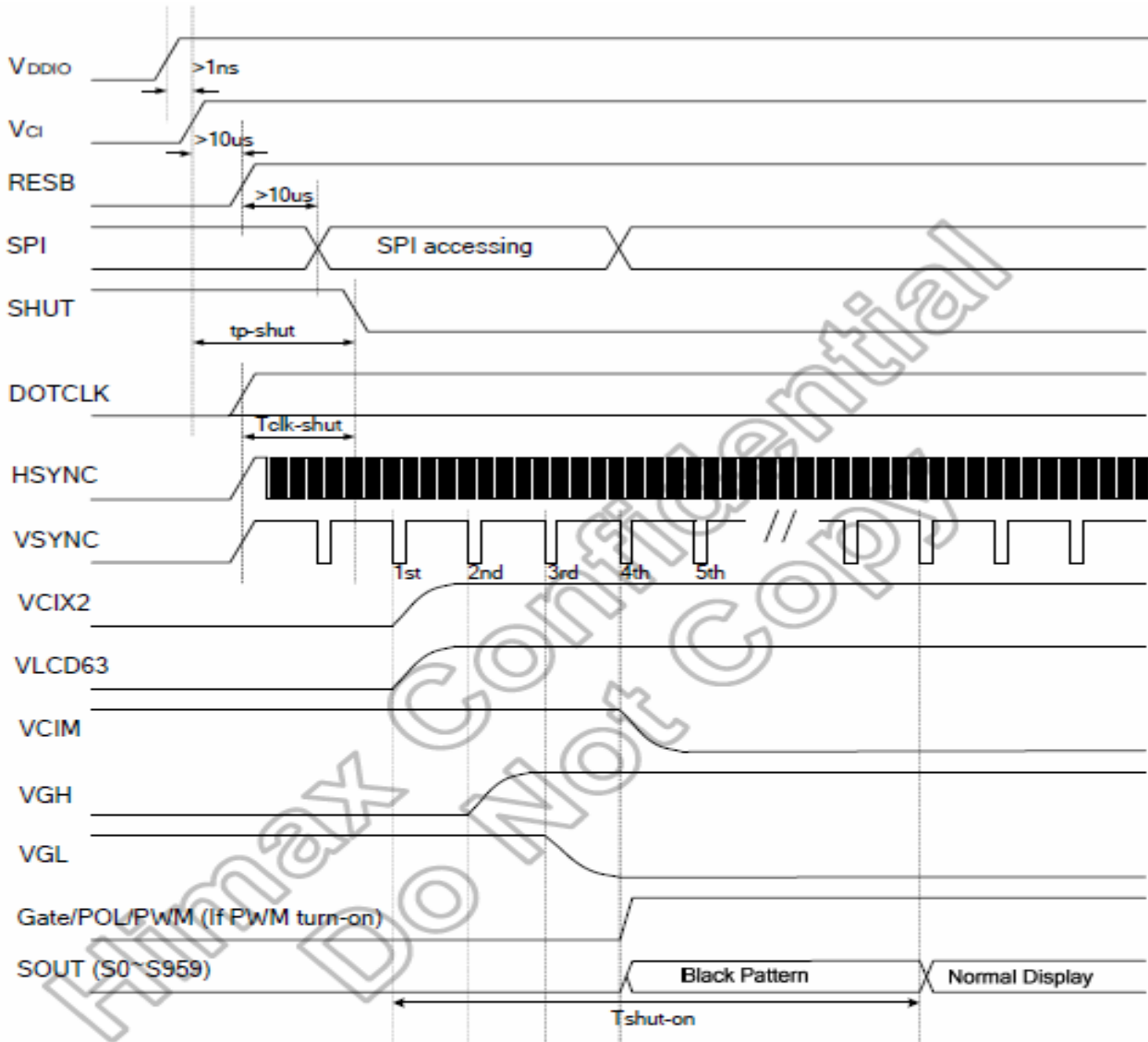


Figure 12. 10 Power Up Sequence

| Characteristics  | Symbol    | Spec. |      |       | Unit    |
|--|-----------|-------|------|-------|---------|
|  |           | Min.  | Typ. | Max.  |         |
| VDDD / VDDIO on to falling edge of SHUT  | tp-shut   | 1     | -    | -     | $\mu s$ |
| DOTCLK   | tclk-shut | 1     | -    | -     | clk     |
| Falling edge of SHUT to display start<br>- 1 line: 408 clk<br>- 1 frame: 262 line<br>- DOTCLK = 6.5MHz | tshut-on  | -     | -    | 14    | frame   |
|  |           | -     | 166  | 232.4 | ms      |

**Note:** It is necessary to input DOTCLK before the falling edge of SHUT.  
Display starts at 10th falling edge of VSTNC after the falling edge of SHUT.

Table 12. 3 Power Up Sequence

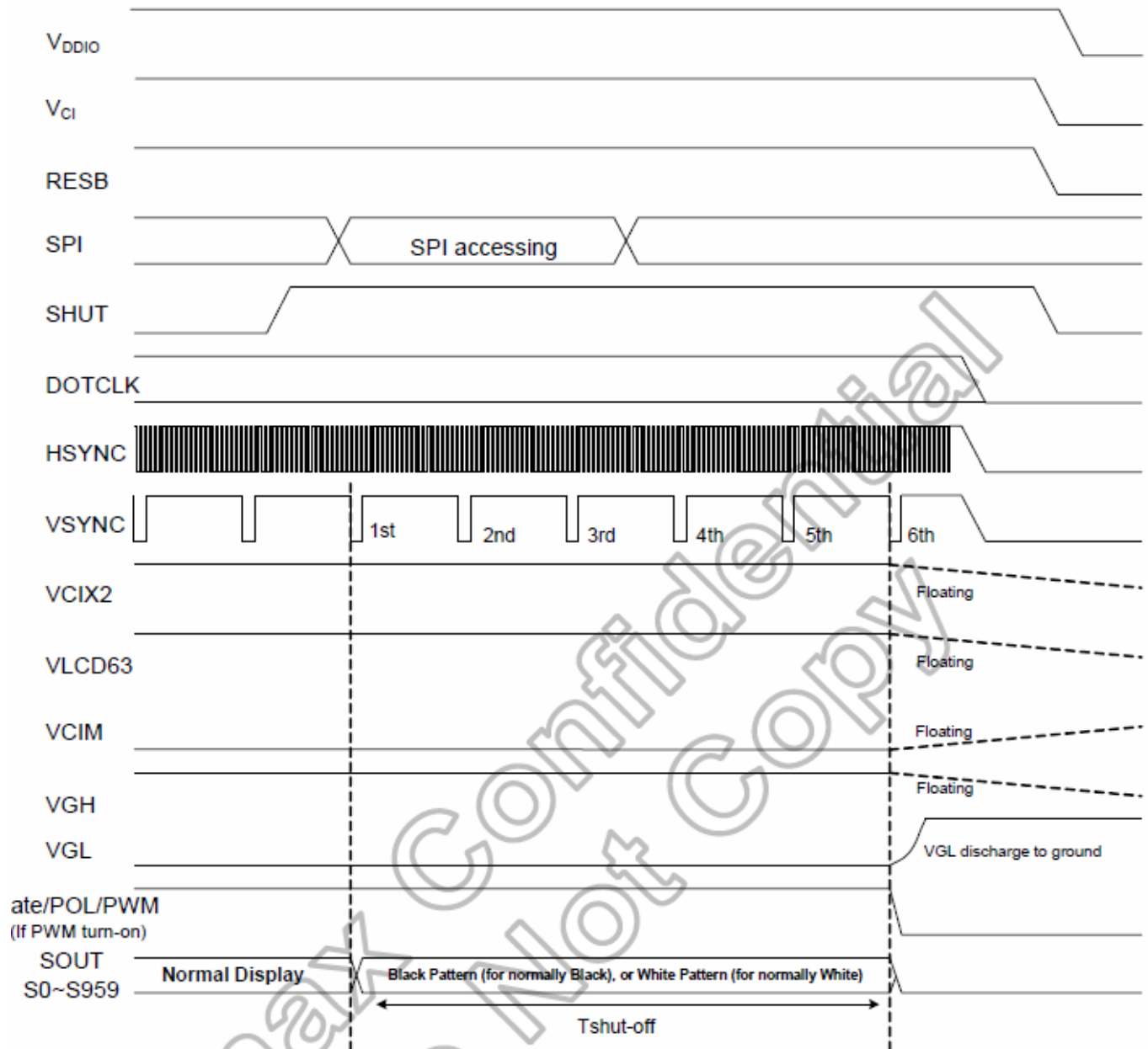


Figure 12. 11 Power Down Sequence

| Characteristics   | Symbol    | Spec. |      |      | Unit  |
|---|-----------|-------|------|------|-------|
|   |           | Min.  | Typ. | Max. |       |
| Rising edge of SHUT to display off                            | tshut-off | 2     | -    | -    | frame |
| - 1 line: 408 clk<br>- 1 frame: 262 line<br>- DOTCLK = 6.5MHz |           | 33.4  | -    | -    | ms    |

**Note:** DOTCLK must be maintained at least 2 frames after the rising edge of SHUT.

Display become off at the 2nd falling edge of VSTNC after the falling edge of SHUT.

If RESET signal is necessary for power down, provide it after the 2-frames-cycle of the SHUT period.

Table 12. 4 Power Down Sequence

### 11. RELIABILITY TEST CONDITIONS

| No. | Test item                  | Test condition   | Inspection after test  |
|-----|----------------------------|--|--|
| 1   | High Temperature Storage   | 80±2°C/240 hours   | Inspection after 2~4hours storage at room temperature, the sample shall be free from defects:<br>1.Current changing value before test and after test is 50% larger;<br>2. function defect: Non-display ,abnormal-d isplay, missing lines, Short lines, ITO corrosion;<br>3.visual defect: Air bubble in the LCD, Seal leak, Glass crack。 |
| 2   | Low Temperature Storage    | -30±2°C/240 hours  |  |
| 3   | High Temperature Operating | 70±2°C/120 hours   |  |
| 4   | Low Temperature Operating  | -20±2°C/120 hours  |  |
| 5   | Temperature Cycle          | -20±2°C~25~70±2°C×10cycles<br>(30min.) (5min.) (30min.)  |  |
| 6   | Damp Proof Test            | 50°C×90% RH/120 hours  |  |
| 7   | Vibration Test             | Frequency: 10Hz~55Hz~10Hz<br>Amplitude: 1.5mm, X, Y, Z direction for total 3hours<br>(Packing condition) |  |
| 8   | Dropping test              | Drop to the ground from 1m height, one time, every side of carton. (Packing condition)                   |  |
| 9   | ESD test                   | Voltage:±8KV R: 330Ω C: 150pF<br>Air discharge, 10time   |  |

**Remark:**

- The test samples should be applied to only one test item.
- Sample size for each test item is 3~5pcs.
- For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

## 12. INSPECTION CRITERION

### 12.1 description

This specification is made to be used as the standard acceptance/rejection criteria for TFT LCM Product.

#### 1. Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1 : 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

Minor defect: AQL 1.5

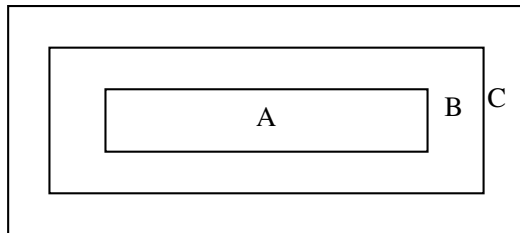
#### 2. Inspection condition

- Viewing distance for cosmetic inspection is about  $30\pm 5$ cm with bare eyes, and under an environment 600~1000lux for visual inspection and 0~200lux for function test., all directions for inspecting the sample should be within  $45^\circ$  against perpendicular line. (Normal temperature  $18\sim 28^\circ\text{C}$  and normal humidity  $60\pm 15\%$  RH).

- Driving voltage

The Vop value from which the most optical contrast can be obtained near the specified Vop in the specification (Within  $\pm 0.5\text{V}$  of the typical value at  $25^\circ\text{C}$ .).

#### 3. Definition of inspection zone in LCD



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

## 12.2 Inspection criterion

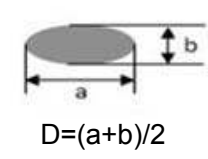
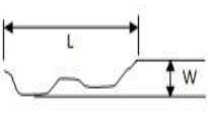
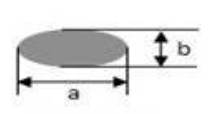
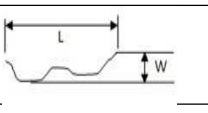
### 12.2.1 Function defect

| Items to be inspected  | Inspection criterion   | Classification of defects |
|------------------------|--|---------------------------|
| All functional defects | 1) No display<br>2) Display abnormally<br>3) Missing vertical, horizontal segment<br>4) Short circuit<br>5) Back-light no lighting, flickering and abnormal lighting.<br>6) obvious striation<br>7) Current beyond specification value | MA                        |
| Missing                | Missing component  |                           |
| Outline dimension      | Overall outline dimension exceed the drawing is not allowed.   |                           |

**12.2.2 LCD pixel defect ( bad dot) (defect type: MI)**

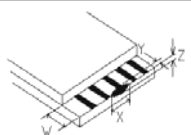
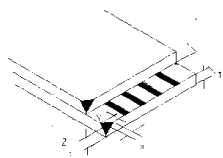
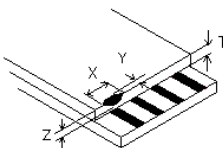
| Checking item                       | Judgment criterion            |             |                |               |
|-------------------------------------|-------------------------------|-------------|----------------|---------------|
|                                     | Item\LCD size                 | S ≤5.0 Inch | 5.0<S≤7.0 Inch | 7<S≤12.3 Inch |
| Color bad dot-bright dot(R、G、B)     |                               | 1           | 2              | 3             |
| two adjacent bright point           |                               | 0           | 1              | 2             |
| three or more adjacent point        |                               | 0           | 0              | 0             |
| total points for bad dot-bright dot |                               | 1           | 2              | 5             |
| Bad dot-dark dot                    |                               | 2           | 4              | 5             |
| two adjacent dark point             |                               | 1           | 2              | 3             |
| three or more adjacent point        |                               | 0           | 1              | 1             |
| total points for bad dot -dark dot  |                               | 3           | 6              | 7             |
| patch bright dot                    | Invisible with ND5%,it is OK. |             |                |               |

**12.2.3 Dot and line defect (defect type: MI)**

| Checking item   | Judgment criterion                             |                  |                           |               | Figure   |         |
|---|--|------------------|---------------------------|---------------|--|---------|
|   | Diameter(mm)\LCD Size                          | S ≤5.0 Inch      | 5<S≤7 Inch                | 7<S≤12.3 Inch |  |         |
| Dot defect  | D≤0.1  | allowed          | allowed                   | allowed       |  <p>D=(a+b)/2</p> |         |
|   | 0.1<D≤0.2                                      | 4                | allowed                   | allowed       |  |         |
|   | 0.2<D≤0.3                                      | 0                | 5                         | 6             |  |         |
|   | 0.3<D≤0.5                                      | 0                | 0                         |               |  |         |
|   | D>0.5  | 0                | 0                         | 0             |  |         |
|   | the distance between the two defect dot:DS≥5mm |                  |                           |               |  |         |
| line defect   | <b>Length(mm)</b>                              | <b>Width(mm)</b> | <b>Judgment criterion</b> |               |                   |         |
|   | disregard                                      | W≤0.05           | allowed                   | allowed       |  | allowed |
|   | L≤5  | 0.05<W≤0.1       | 4                         | 5             |  | 7       |
|   | L>5  | W>0.1            | 0                         | 0             |  | 0       |
| Concave point and air bubble for polarizer  | <b>LCD Size(mm)</b>                            |                  | <b>Judgment criterion</b> |               |  <p>D=(a+b)/2</p> |         |
|   | D≤0.3  |                  | allowed                   | allowed       |  | allowed |
|   | 0.3<D≤1.0                                      |                  | 3                         | 4             |  | 5       |
|   | 1.0<D≤1.5                                      |                  | 1                         | 2             |  | 3       |
|   | D>1.5  |                  | 0                         | 0             |  | 0       |
| Fold mark, linear scar for polarizer  | <b>Length(mm)</b>                              | <b>Width(mm)</b> | <b>Judgment criterion</b> |               |                   |         |
|   | disregard                                      | W≤0.05           | allowed                   | allowed       |  | allowed |
|   | 1<L≤5  | 0.05<W≤0.2       | 3                         | 4             |  | 5       |
|   | L>5  | W>0.2            | 0                         | 0             |  | 0       |
| Notes:1.If the fold mark and linear scar for polarizer is visible with operating condition,the defect is judged with line judge ; 2.If the fold mark and linear scar for polarizer is visible with non-operating condition,the defect is judged with the above judgment standard. |  |                  |                           |               |  |         |



### 12.2.4 Corner and others crack for LCD (defect type: MI)

| Checking item             | Judgment criterion   | Figure  |
|---------------------------|--|---|
| electric conduction crack | $X \leq 3.0\text{mm}, Y \leq 1/4w, Z \leq t, N \leq 2$   |  |
| corner crack              | $X \leq 3.0\text{mm}, Y \leq 3.0\text{mm}, Z \leq t, N \leq 3$<br>Corner crack extended to ITO PIN, none allowed |  |
| surface crack             | $X \leq 1.5\text{mm}, Y \leq 1.0\text{mm}, Z \leq t, N \leq 4$   |  |

### 12.2.5 Module cosmetic criterion (defect type: MI)

| Item                                  | Judgment criterion   |
|---------------------------------------|--|
| Difference in Spec.                   | None allowed   |
| Pattern peeling                       | No substrate pattern peeling and floating  |
| Soldering defects                     | No soldering missing<br>No soldering bridge<br>No cold soldering<br>Notes: detail judgment referring to IPC-A-610 grade II |
| Resist flaw on Printed Circuit Boards | visible copper foil ( 0.5mm or more) on substrate pattern, none allowed  |
| Accretion of metallic Foreign matter  | No accretion of metallic foreign matters (Not exceed 0.2mm)  |
| Stain                                 | No stain to spoil cosmetic badly   |
| Plate discoloring                     | No plate fading, rusting and discoloring   |
| Newton ring                           | Referring to limited sample  |
| Mura                                  | Invisible with 5%ND, allowed   |
| Light leaks                           | Referring to limited sample  |

## 13. HANDLING PRECAUTIONS

### 13.1 Mounting method

The LCD module consists of two thin glass plates with polarizers which easily be damaged. And since the module is so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

### 13.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happens by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

### 13.3 Caution against static charge

The LCD module uses C-MOS LSI drivers, so we recommend that you:

Connect any unused input terminal to V<sub>dd</sub> or V<sub>ss</sub>, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

### 13.4 Packing

Module employs LCD elements and must be treated as such.

- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity

### 13.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

### 13.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.
- Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it . And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.  
It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

### 13.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

## 14. PRECAUTION FOR USE

**14.1** A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

**14.2** On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to OD , and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

## 15. PACKING SPECIFICATION

Please consult our technical department for detail information.