

# FPN530 FPN530A



## NPN Low Saturation Transistor

These devices are designed for high current gain and low saturation voltage with collector currents up to 3.0 A continuous. Sourced from Process NC.

### Absolute Maximum Ratings\* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
I <sub>C</sub>	Collector Current - Continuous	3.0	A
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

**NOTES:**

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		FPN530 / FPN530A	
P <sub>D</sub>	Total Device Dissipation	1.0	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	50	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	125	°C/W

## NPN Low Saturation Transistor

(continued)

FPN530 / FPN530A

### Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
--------	-----------	-----------------	-----	-----	-------

#### OFF CHARACTERISTICS

$BV_{CEO}$	Collector-Emitter Breakdown Voltage	$I_C = 10\text{ mA}, I_B = 0$	30		V
$BV_{CBO}$	Collector-Base Breakdown Voltage	$I_C = 100\ \mu\text{A}, I_E = 0$	60		V
$BV_{EBO}$	Emitter-Base Breakdown Voltage	$I_E = 100\ \mu\text{A}, I_C = 0$	5.0		V
$I_{CBO}$	Collector Cutoff Current	$V_{CB} = 30\text{ V}, I_E = 0$ $V_{CB} = 30\text{ V}, I_E = 0, T_A = 100^\circ\text{C}$		100 10	nA $\mu\text{A}$
$I_{EBO}$	Emitter Cutoff Current	$V_{EB} = 4.0\text{ V}, I_C = 0$		100	nA

#### ON CHARACTERISTICS\*

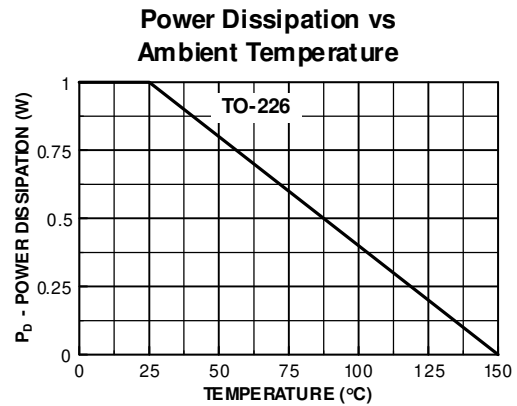
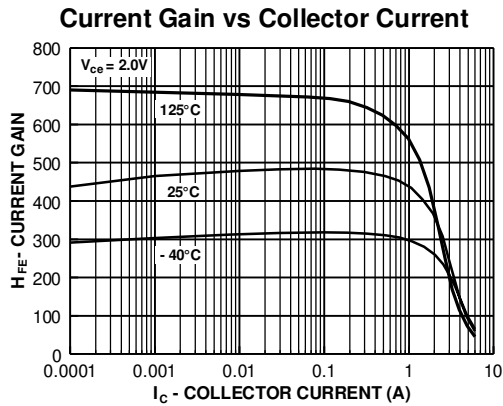
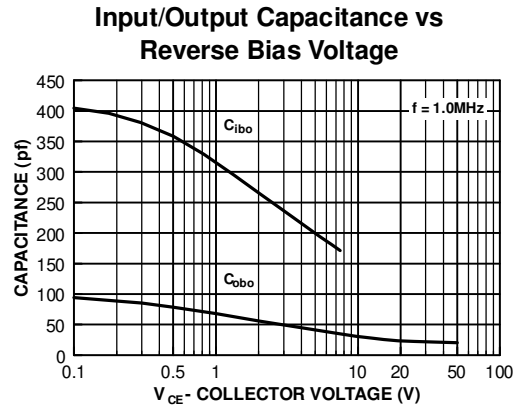
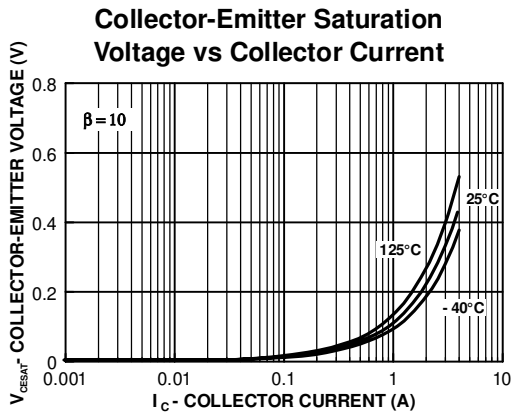
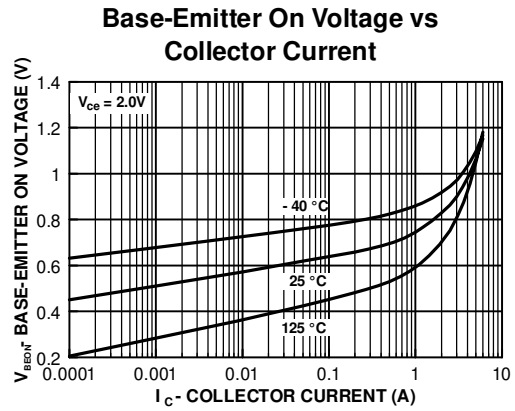
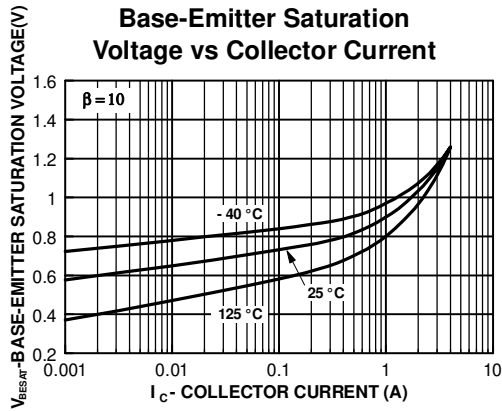
$h_{FE}$	DC Current Gain	$I_C = 100\text{ mA}, V_{CE} = 2.0\text{ V}$ $I_C = 1.0\text{ A}, V_{CE} = 2.0\text{ V}$ $I_C = 2.0\text{ A}, V_{CE} = 2.0\text{ V}$	<b>530</b> <b>530A</b>	100 250 120 80	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 1.0\text{ A}, I_B = 100\text{ mA}$ $I_C = 2.0\text{ A}, I_B = 200\text{ mA}$	<b>530</b> <b>530A</b>	300 250 450	mV mV mV
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 1.0\text{ A}, I_B = 100\text{ mA}$		1.25	V
$V_{BE(on)}$	Base-Emitter Saturation Voltage	$I_C = 1.0\text{ A}, V_{CE} = 2.0\text{ V}$		1.0	V

#### SMALL SIGNAL CHARACTERISTICS

$C_{obo}$	Output Capacitance	$V_{CB} = 10\text{ V}, I_E = 0, f = 1.0\text{ MHz}$		50	pF
$F_T$	Transition Frequency	$I_C = 100\text{ mA}, V_{CE} = 5.0\text{ V},$ $f = 100\text{ MHz}$	150		MHz

\*Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

Typical Characteristics



## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FASTr™	PowerTrench®	SyncFET™
Bottomless™	GlobalOptoisolator™	QFET™	TinyLogic™
CoolFET™	GTO™	QST™	UHC™
CROSSVOLT™	HiSeC™	QT Optoelectronics™	VCX™
DOME™	ISOPLANAR™	Quiet Series™	
E <sup>2</sup> CMOS™	MICROWIRE™	SILENT SWITCHER®	
EnSigna™	OPTOLOGIC™	SMART START™	
FACT™	OPTOPLANAR™	SuperSOT™-3	
FACT Quiet Series™	PACMAN™	SuperSOT™-6	
FAST®	POP™	SuperSOT™-8	

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.