



30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C		
201/	$6.8m\Omega$ @ $V_{GS} = -10V$	-50A		
-30V	$13m\Omega$ @ $V_{GS} = -4.5V$	-36A		

Description

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- **DC-DC Converters**

Features and Benefits

- Low R_{DS(ON)} Ensures On State Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher **Density End Products**
- Occupies Just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- HBM ESD Protection Level of 8kV Typical
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

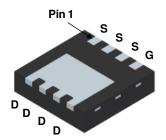
Mechanical Data

- Case: V-DFN3333-8 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Below Diagram Terminals: Finish -NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.030 grams (Approximate)

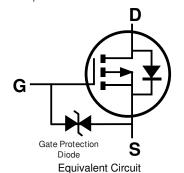








Bottom View



Ordering Information (Note 4)

Part Number	Case	Packaging		
DMP3007SCG-7	V-DFN3333-8 (Type B)	2,000/Tape & Reel		
DMP3007SCG-13	V-DFN3333-8 (Type B)	3,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



V07= Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 16 = 2016) WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±25	V
Continuous Drain Current (Note 7) V _{GS} = -10V Stead Stati		$T_C = +25$ °C $T_C = +70$ °C	I _D	-50 -40	Α
Maximum Continuous Body Diode Forward Current (Note 7)			Is	-40	Α
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	-100	Α
Avalanche Current (Note 8) L = 1mH			I _{AS}	-16	Α
Avalanche Energy (Note 8) L = 1mH			E _{AS}	130	mJ

Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	1.0	W
Thermal Resistance, Junction to Ambient (Note 5) Steady State		R _{0JA}	124	°C/W
Total Power Dissipation (Note 6) $T_A = +25^{\circ}C$		P _D	2.4	W
Thermal Resistance, Junction to Ambient (Note 6) Steady State		R _{0JA}	52	°C/W
Thermal Resistance, Junction to Case (Note 7)		Rejc	4.0	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	V _{DS} = -24V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	-	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	$V_{GS(TH)}$	-1.0		-3.0	٧	$V_{DS}=V_{GS},I_D=-250\mu A$	
Static Drain-Source On-Resistance	ם	-	5.7	6.8	mΩ	$V_{GS} = -10V, I_D = -11.5A$	
Static Dialii-Source Ori-nesistance	R _{DS(ON)}	_	8.0	13		$V_{GS} = -4.5V, I_D = -8.5A$	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}		2,826	_	pF	V _{DS} = -15V, V _{GS} = 0V, -f = 1.0MHz	
Output Capacitance	Coss	_	606	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	305	_	pF		
Gate Resistance	R_g	-	23	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	-	31.2	_	nC		
Total Gate Charge (V _{GS} = -10V)	Qg	-	64.2	_	nC		
Gate-Source Charge	Q _{gs}	-	10.6	_	nC	$V_{DS} = -15V, I_D = -11.5A$	
Gate-Drain Charge	Q_{gd}	-	11.6	_	nC	-	
Turn-On Delay Time	t _{D(ON)}	-	4.8	_	ns		
Turn-On Rise Time	t _R		4.3	_	ns	$V_{DD} = -15V$, $V_{GS} = -10V$, $R_g = 6\Omega$, $I_D = -11.5A$	
Turn-Off Delay Time	t _{D(OFF)}		306	_	ns		
Turn-Off Fall Time	t _F		125	_	ns		
Reverse Recovery Time	t _{RR}		19	_	ns	14.54.11/11.4004/	
Reverse Recovery Charge	Q _{RR}	_	9.8	_	nC	$I_S = -11.5A$, dl/dt = 100A/ μ s	

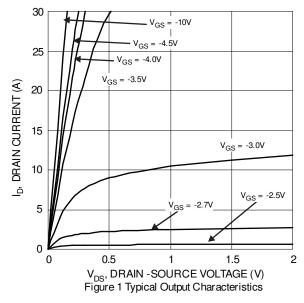
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

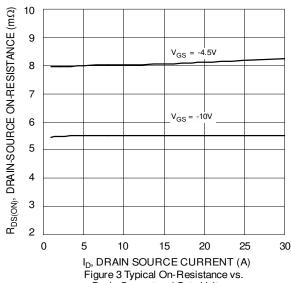
^{8.} I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25$ °C.

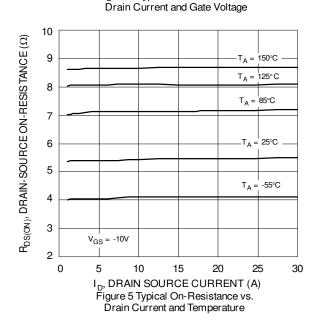
^{9.} Short duration pulse test used to minimize self-heating effect.

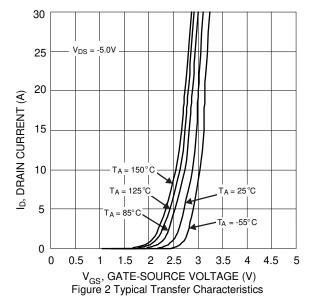
^{10.} Guaranteed by design. Not subject to product testing.











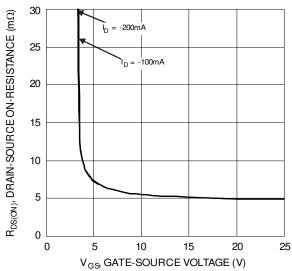


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

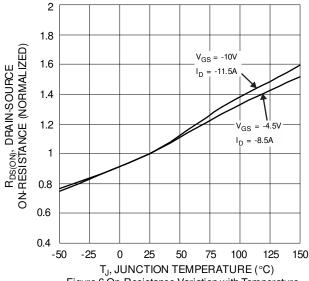
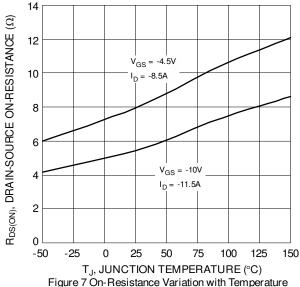
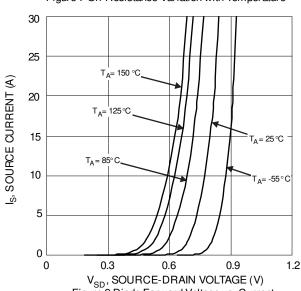
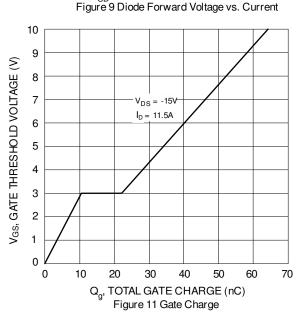


Figure 6 On-Resistance Variation with Temperature









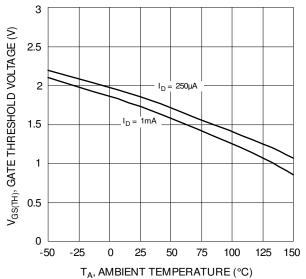
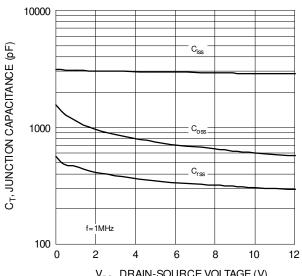
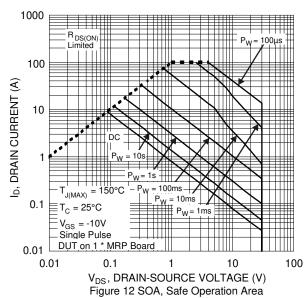


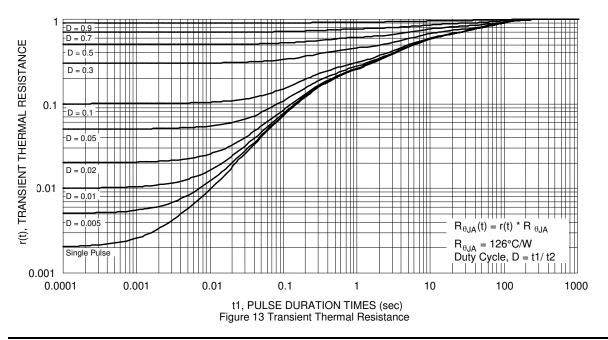
Figure 8 Gate Threshold Variation vs. Junction Temperature



V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10 Typical Junction Capacitance



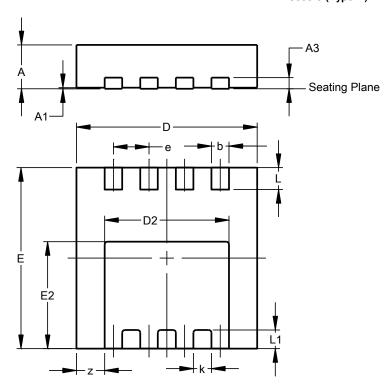




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN3333-8 (Type B)



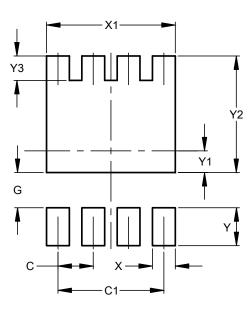
V-DFN3333-8						
(Type B)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A 1	0.00	0.05	0.02			
A 3	-	-	0.203			
b	0.27	0.37	0.32			
D	3.25	3.35	3.30			
D2	2.17	2.37	2.27			
Е	3.25	3.35	3.30			
E2	1.85	2.05	1.95			
е			0.65			
k		-	0.33			
L	0.35	0.45	0.40			
L1			0.34			
Z			0.515			
All Dimensions in mm						



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN3333-8 (Type B)



Dimensions	Value (in mm)		
С	0.650		
C1	1.950		
G	0.650		
Х	0.420		
X1	2.370		
Υ	0.700		
Y1	0.400		
Y2	2.150		
Y3	0.450		



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