



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
COV/	5Ω @ V _{GS} = 10V	0.21A
60V	6Ω @ V _{GS} = 5V	0.20A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- **Power Management Functions**

Features

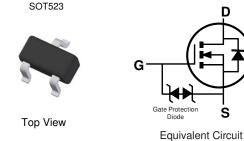
- Low On-Resistance: RDS(ON)
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected up to 1kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts gualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

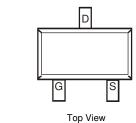
https://www.diodes.com/quality/productdefinitions/

Mechanical Data

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

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Pin Out Configuration

Ordering Information (Note 4)

ESD Protected up to 1kV

1	Part Number	Case	Packaging				
-							
	DMN65D8LT-7	SOT523	3000/Tape & Reel				
	DMN65D8LT-13	SOT523	10000/Tape & Reel				
Notes:	Notes: 1. EU Directive 2002/95/EC (BoHS), 2011/65/EU (BoHS 2) & 2015/863/EU (BoHS 3) compliant, All applicable BoHS exemptions applied.						

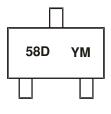
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant. All applicable RoHS exemptions applied. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 $\begin{array}{l} 58D = Product \ Type \ Marking \ Code \\ YM = Date \ Code \ Marking \\ Y \ or \ \overline{Y} = Year \ (ex: \ H = 2020) \\ M = Month \ (ex: \ 9 = September) \end{array}$

Date Code Key

Year	2014		2019	2020	2	021	2022	202	3	2024	2025	2026	2027	2028
Code	В		G	Н			J	K		L	М	Ν	0	Р
I	Month	Ja	n Feb	Mar	Apr	Мау	Jun	Jul	Aug	g Sep	Oct	N	ov	Dec
	Code	1	2	3	4	5	6	7	8	9	0		Ν	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit		
Drain-Source Voltage		V _{DSS}	60	V		
Gate-Source Voltage		V _{GSS}	±20	V		
Continuous Drain Current (Note 6) V_{GS} = 5.0V	Steady	T _A = +25°C	- I _D	210	mA	
	State	T _A = +70°C		170	ША	
Maximum Continuous Body Diode Forward Currer	nt (Note 6)		Is	210	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	%)	I _{DM}	0.7	A		
Pulsed Source Current (10µs Pulse, Duty Cycle =	1%)	I _{SM}	0.7	А		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	426	°C/W
Total Power Dissipation (Note 6)		PD	360	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	351	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

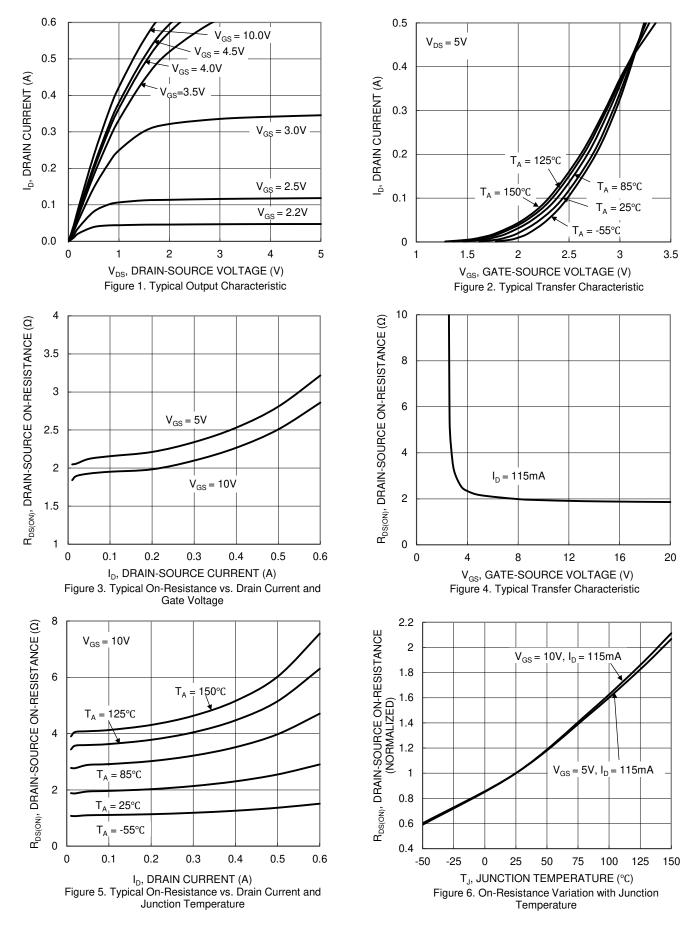
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	,		- 71-				
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1.2	_	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance			2.0	5.0	Ω	$V_{GS} = 10V, I_D = 0.115A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		2.2	6.0	12	V _{GS} = 5V, I _D = 0.115A	
DYNAMIC CHARACTERISTICS (Note 8)						·	
Input Capacitance	Ciss		24	_		V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	2.8	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	1.8	_			
Gate Resistance	Rg	_	50		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	0.4	_			
Gate-Source Charge	Q _{gs}	_	0.12	_	nC	$V_{DD} = 30V, V_{GS} = 4.5V, I_D = 150mA$	
Gate-Drain Charge	Q _{gd}	_	0.14	_			
Turn-On Delay Time	t _{D(ON)}		2.0				
Turn-On Rise Time	t _R		1.9	_	ns	$V_{GS}=10V,V_{DD}=30V,R_g=25\Omega,$	
Turn-Off Delay Time	t _{D(OFF)}		9.5		115	$I_D = 0.2A$	
Turn-Off Fall Time	t _F	_	4.3	_			

Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout
Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.





DMN65D8LT Document number: DS38190 Rev. 3 - 2



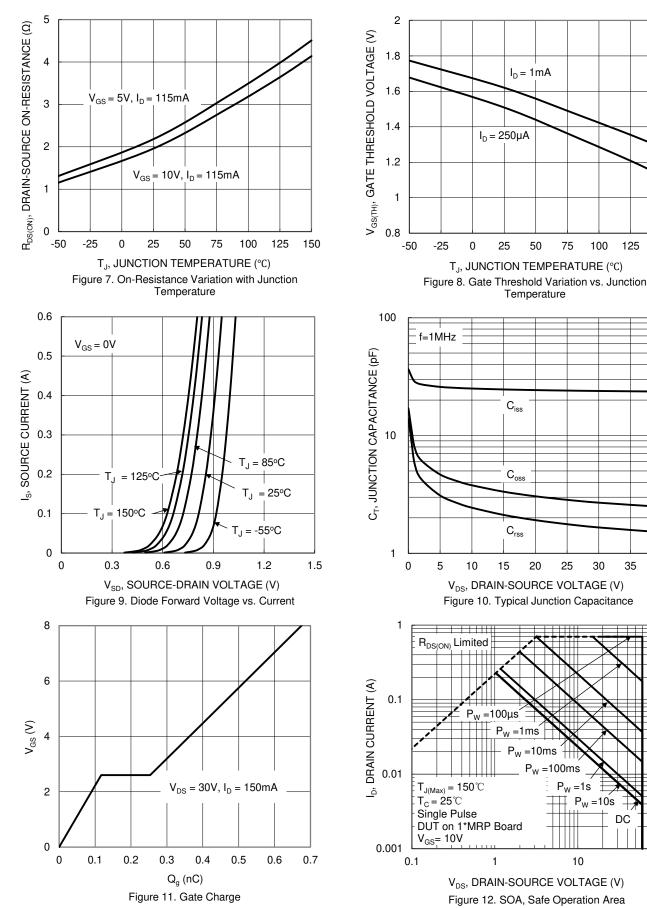
DMN65D8LT

125

35

40

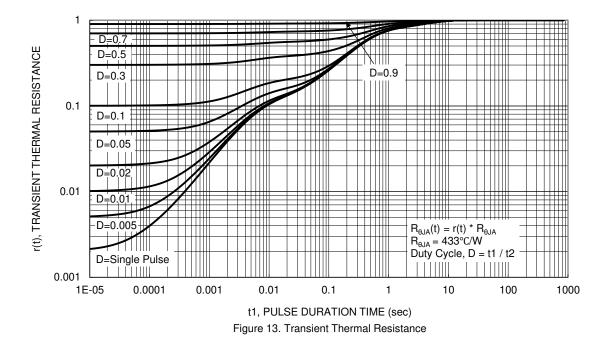
150



100

DC

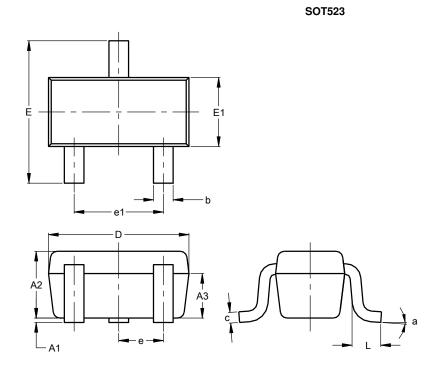






Package Outline Dimensions

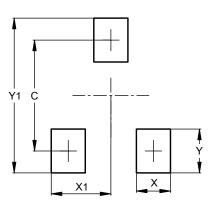
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523							
Dim	Min	Min Max Typ					
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
c	0.10	0.20	0.12				
D	1.50	1.70	1.60				
Е	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
е		0.50 BS	С				
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
Α	II Dimen	isions ir	ח mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80

SOT523



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