

# KA1L0380B/KA1L0380RB/ KA1M0380RB/KA1H0380RB

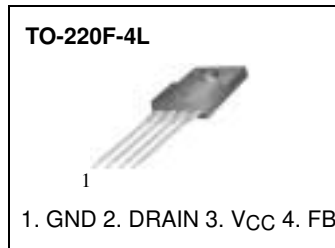
## Fairchild Power Switch(FPS)

### Features

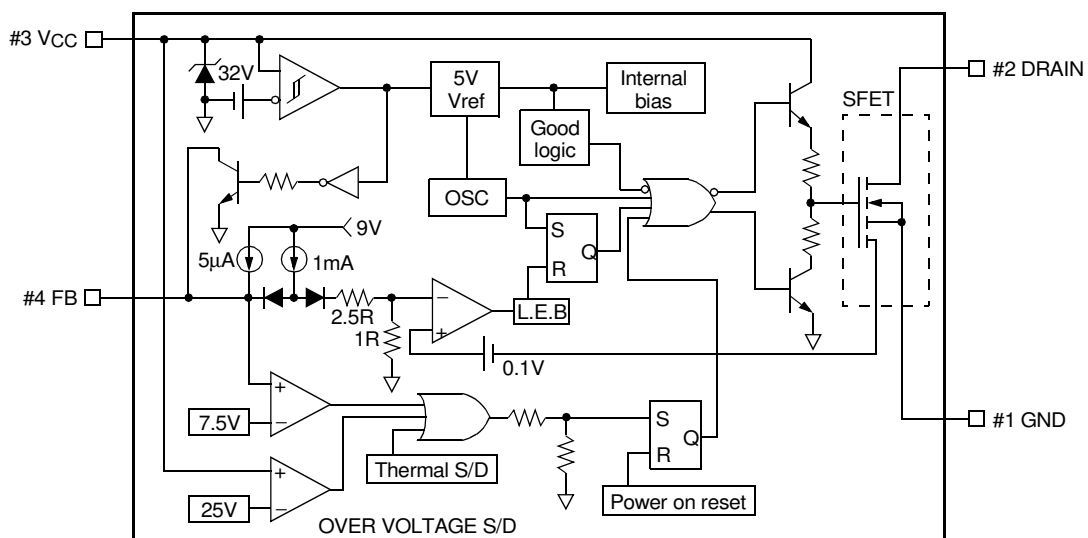
- Precision Fixed Operating Frequency
- KA1L0380B/KA1L0380RB (50kHz)
- KA1M0380RB (67kHz)
- KA1H0380RB (100kHz)
- Pulse by Pulse Over Current Limiting
- Over Load Protection
- Over Voltage Protection (Min. 23V)
- Internal Thermal Shutdown Function
- Under Voltage Lockout
- Internal High Voltage Sense FET
- Auto Restart Mode (KA1L0380RB/KA1M0380RB/KA1H0380RB)
- Latch Mode (KA1L0380B)

### Description

The Fairchild Power Switch(FPS) product family is specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM controller IC. PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, temperature compensated precision current sources for loop compensation and fault protection circuit compared to discrete MOSFET and controller or RCC switching converter solution, The Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase & efficiency, productivity, and system reliability. It has a basic platform well suited for cost effective design in either a flyback converter or a forward converter.



### Internal Block Diagram



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Maximum Drain Voltage <sup>(1)</sup>	V <sub>D,MAX</sub>	800	V
Drain-Gate Voltage (R <sub>GS</sub> =1MΩ)	V <sub>DGR</sub>	800	V
Gate-Source (GND) Voltage	V <sub>GS</sub>	±30	V
Drain Current Pulsed <sup>(2)</sup>	I <sub>DM</sub>	12	ADC
Single Pulsed Avalanche Energy <sup>(3)</sup>	E <sub>AS</sub>	95	mJ
Avalanche Current <sup>(4)</sup>	I <sub>AS</sub>	10	A
Continuous Drain Current (T <sub>C</sub> =25°C)	I <sub>D</sub>	3.0	ADC
Continuous Drain Current (T <sub>C</sub> =100°C)	I <sub>D</sub>	2.1	ADC
Maximum Supply Voltage	V <sub>CC,MAX</sub>	30	V
Input Voltage Range	V <sub>FB</sub>	-0.3 to V <sub>SD</sub>	V
Total Power Dissipation	P <sub>D</sub>	35	W
	Darting	0.28	W/°C
Operating Ambient Temperature	T <sub>A</sub>	-25 to +85	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

### Notes:

1. T<sub>j</sub> = 25°C to 150°C
2. Repetitive rating: Pulse width limited by maximum junction temperature
3. L = 51mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25Ω, starting T<sub>j</sub> = 25°C
4. L = 13μH, starting T<sub>j</sub> = 25°C

## Electrical Characteristics (SFET part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =50μA	800	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =Max., Rating, V <sub>GS</sub> =0V	-	-	50	μA
		V <sub>DS</sub> =0.8Max., Rating, V <sub>GS</sub> =0V, T <sub>C</sub> =125°C	-	-	200	μA
Static Drain-Source on Resistance <sup>(Note)</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A	-	4.0	5.0	Ω
Forward Transconductance <sup>(Note)</sup>	g <sub>fs</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =1.5A	1.5	2.5	-	S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz	-	779	-	pF
Output Capacitance	C <sub>oss</sub>		-	75.6	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	24.9	-	
Turn on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =0.5B V <sub>DSS</sub> , I <sub>D</sub> =3.0A (MOSFET switching time is essentially independent of operating temperature)	-	40	-	nS
Rise Time	t <sub>r</sub>		-	95	-	
Turn Off Delay Time	t <sub>d(off)</sub>		-	150	-	
Fall Time	t <sub>f</sub>		-	60	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Q <sub>g</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.0A, V <sub>DS</sub> =0.5B V <sub>DSS</sub> (MOSFET switching time is essentially independent of operating temperature)	-	-	34	nC
Gate-Source Charge	Q <sub>gs</sub>		-	7.2	-	
Gate-Drain (Miller) Charge	Q <sub>gd</sub>		-	12.1	-	

### Note:

1. Pulse test: Pulse width ≤ 300μS, duty cycle ≤ 2%

2.  $S = \frac{1}{R}$

**Electrical Characteristics (Control Part)** (Continued)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>UVLO SECTION</b>						
Start Threshold Voltage	VSTART	-	14	15	16	V
Stop Threshold Voltage	VSTOP	After turn on	9	10	11	V
<b>OSCILLATOR SECTION</b>						
Initial Accuracy	FOSC	<b>KA1L0380B</b>	45	50	55	kHz
		<b>KA1L0380RB</b>	45	50	55	
		<b>KA1M0380RB</b>	61	67	73	
		<b>KA1H0380RB</b>	90	100	110	
Frequency Change With Temperature <sup>(2)</sup>	$\Delta F/\Delta T$	-25°C ≤ Ta ≤ +85°C	-	±5	±10	%
Maximum Duty Cycle	Dmax	<b>KA1L0380B</b>	74	77	80	%
		<b>KA1L0380RB</b>	74	77	80	
		<b>KA1M0380RB</b>	74	77	80	
		<b>KA1H0380RB</b>	64	67	70	
<b>FEEDBACK SECTION</b>						
Feedback Source Current	IFB	Ta=25°C, 0V ≤ Vfb ≤ 3V	0.7	0.9	1.1	mA
Shutdown Feedback Voltage	VSD	-	6.9	7.5	8.1	V
Shutdown Delay Current	Idelay	Ta=25°C, 5V ≤ Vfb ≤ VSD	4.0	5.0	6.0	μA
<b>REFERENCE SECTION</b>						
Output Voltage <sup>(1)</sup>	Vref	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability <sup>(1)(2)</sup>	Vref/ΔT	-25°C ≤ Ta ≤ +85°C	-	0.3	0.6	mV/°C
<b>CURRENT LIMIT (SELF-PROTECTION) SECTION</b>						
Peak Current Limit	I <sub>OVER</sub>	Max. inductor current	1.89	2.15	2.41	A
<b>PROTECTION SECTION</b>						
Thermal Shutdown Temperature <sup>(1)</sup>	TSD	-	140	160	-	°C
Over Voltage Protection Voltage	VOVP	-	23	25	28	V
<b>TOTAL DEVICE SECTION</b>						
Start-Up Current	ISTART	VCC=14V	0.1	0.3	0.45	mA
Operating Supply Current (Control Part Only)	IOP	Ta=25°C	6	12	18	mA
VCC Zener Voltage	VZ	ICC=20mA	30	32.5	35	V

**Note:**

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS (wafer test) process

## Typical Performance Characteristics

(These characteristic graphs are normalized at  $T_a=25^\circ\text{C}$ )

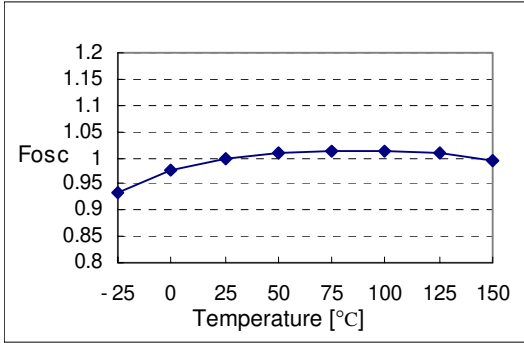


Figure 1. Operating Frequency

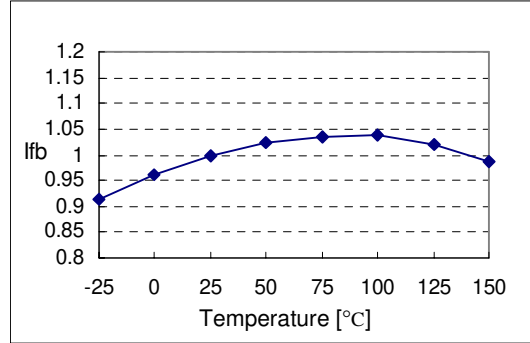


Figure 2. Feedback Source Current

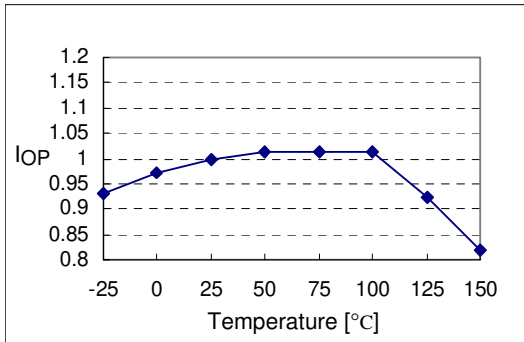


Figure 3. Operating Supply Current

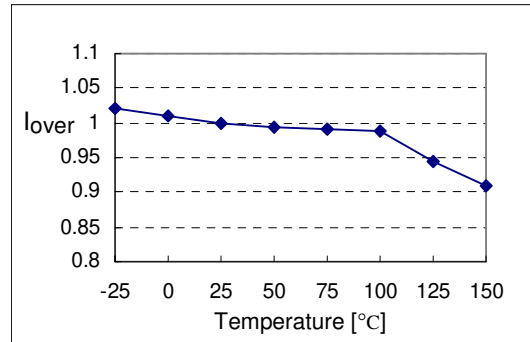


Figure 4. Peak Current Limit

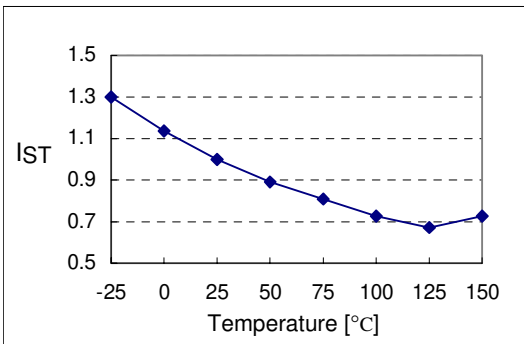


Figure 5. Start up Current

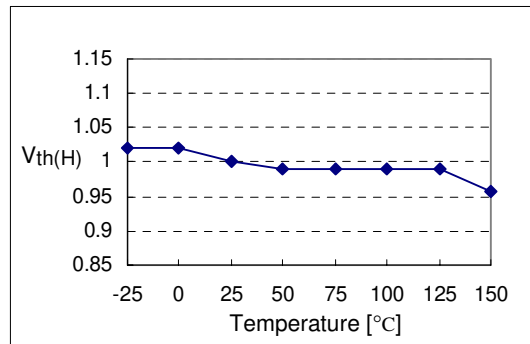


Figure 6. Start Threshold Voltage

## Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at  $T_a=25^\circ\text{C}$ )

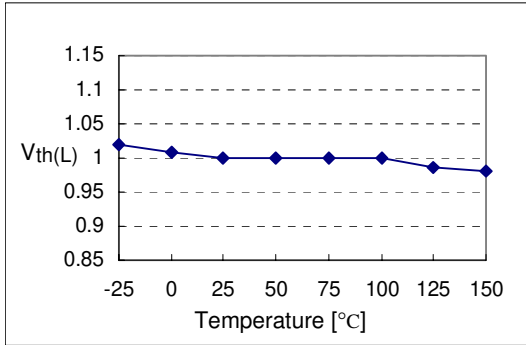


Figure 7. Stop Threshold Voltage

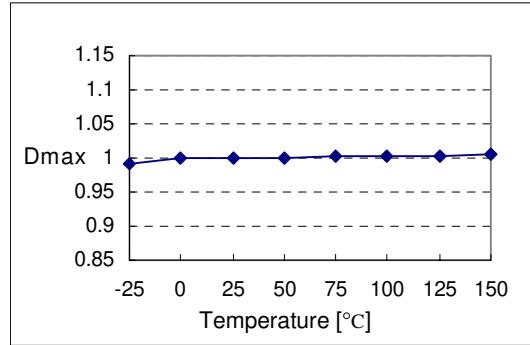


Figure 8. Maximum Duty Cycle

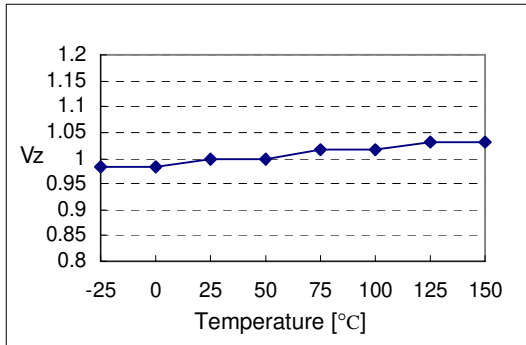


Figure 9. VCC Zener Voltage

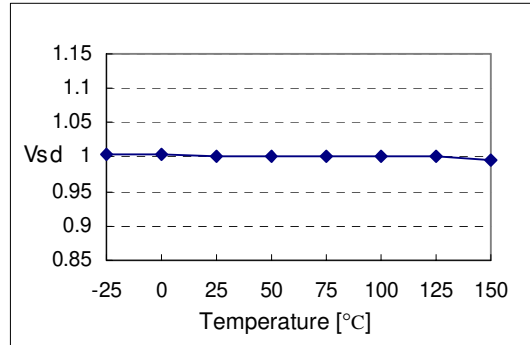


Figure 10. Shutdown Feedback Voltage

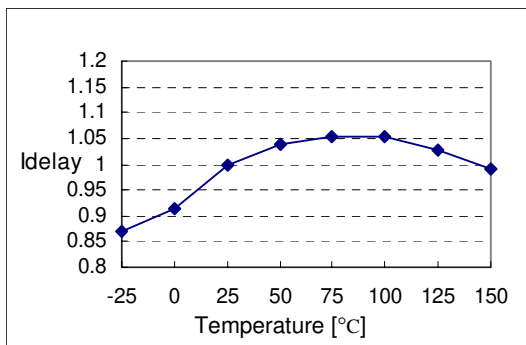


Figure 11. Shutdown Delay Current

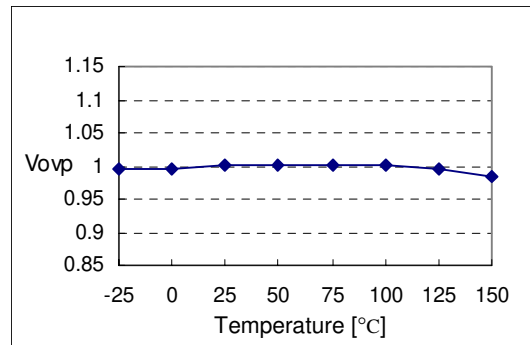
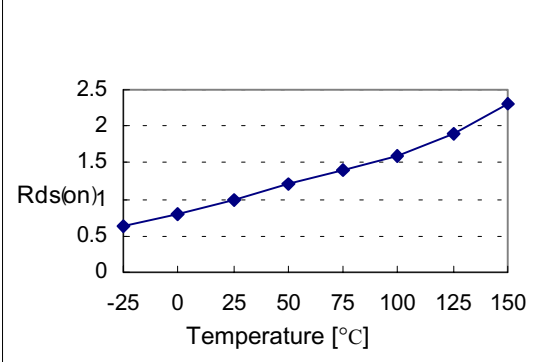


Figure 12. Over Voltage Protection

**Typical Performance Characteristics** (Continued)

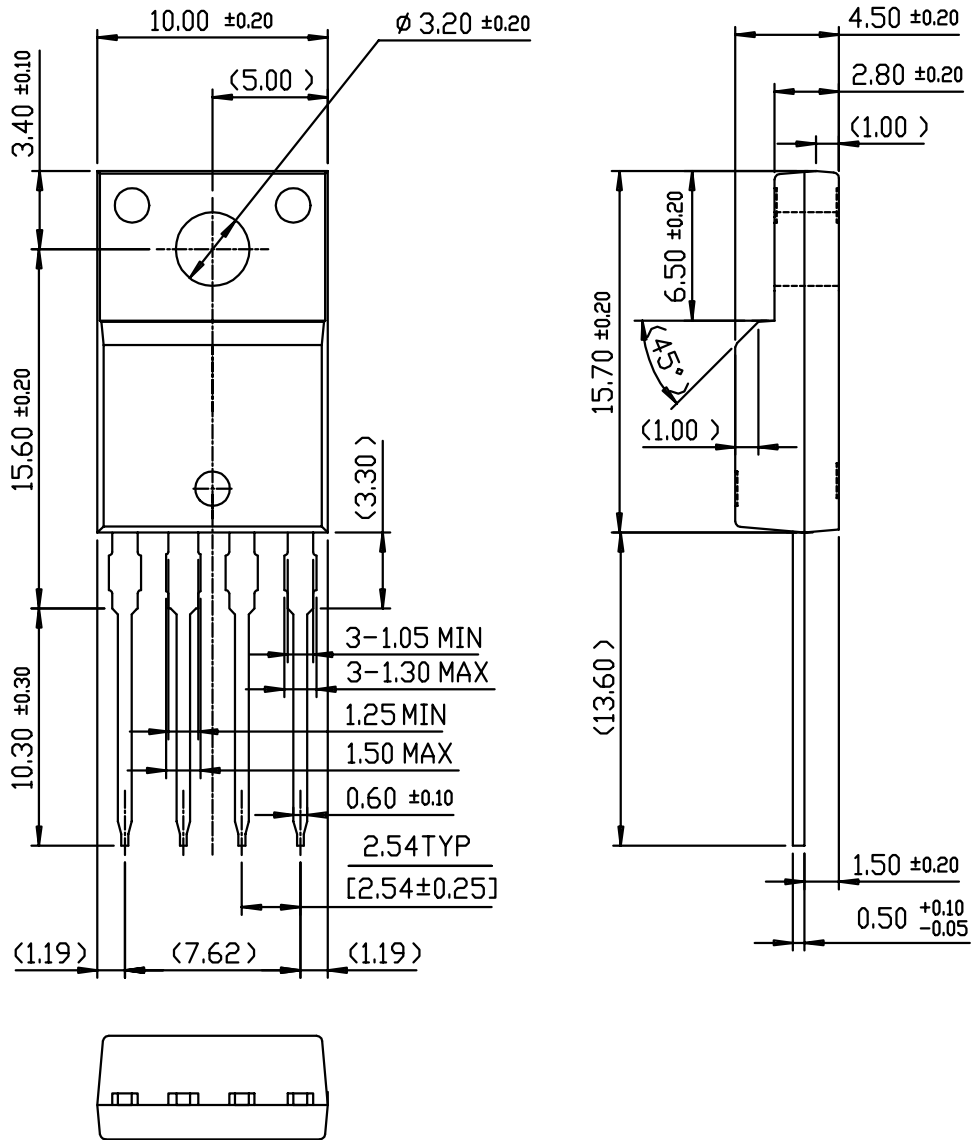
(These characteristic graphs are normalized at Ta=25°C)



**Figure 13. Static Drain-Source on Resistance**

# Package Dimensions

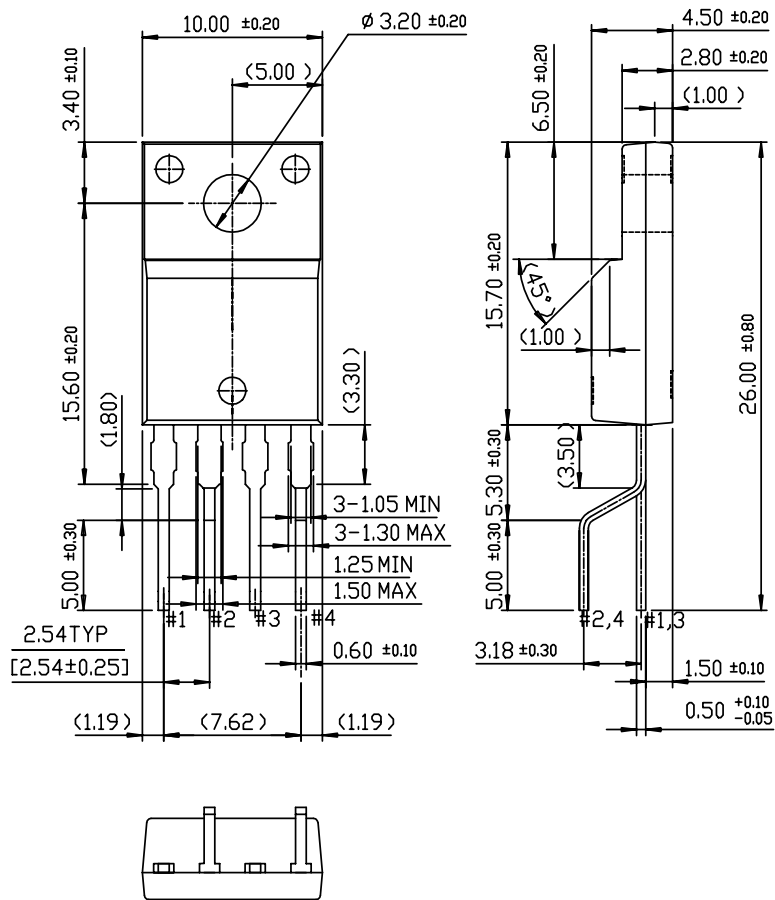
## TO-220F-4L





Package Dimensions (Continued)

TO-220F-4L(Forming)



## Ordering Information

Product Number	Package	Rating	Fosc
KA1L0380B-TU	TO-220F-4L	800V, 3A	50kHz
KA1L0380B-YDTU	TO-220F-4L(Forming)		
KA1L0380RB-TU	TO-220F-4L	800V, 3A	50kHz
KA1L0380RB-YDTU	TO-220F-4L(Forming)		
KA1M0380RB-TU	TO-220F-4L	800V, 3A	67kHz
KA1M0380RB-YDTU	TO-220F-4L(Forming)		
KA1H0380RB-TU	TO-220F-4L	800V, 3A	100kHz
KA1H0380RB-YDTU	TO-220F-4L(Forming)		

TU : Non Forming Type

YDTU : Forming Type

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