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New Japan Radio Co.,Ltd.

www.njr.com

LOW VOLTAGE AUDIO POWER AMPLIFIER

■ GENERAL DESCRIPTION

The NJM386B is wider operating voltage and higher output power version of NJM386. The maximum operating voltage is 18V, and the maximum output power is up to 1W.

■ FEATURES

Operating Voltage

(4V~18V)

Minimum External Components

Low Operating Current

(5mA)

Voltage Gain

 $(20 \sim 200)$

Single Supply Operation

Self-centering of Output Offset Voltage

Package Outline

DIP8, SIP8, DMP8

Bipolar Technology

APPLICATIONS

- AM-FM radio amplifiers
- · Portable tape player amplifiers
- Intercoms
- · TV sound systems
- Line drivers
- Ultra-sonic Drivers
- Small servo drivers
- Power converters

■ PACKAGE OUTLINE





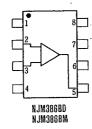
NJM386B0

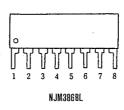
NJM386BM



NJM386BL

PIN CONFIGURATION





PIN FUNCTION

1. GAIN

2. -INPUT

3. +INPUT

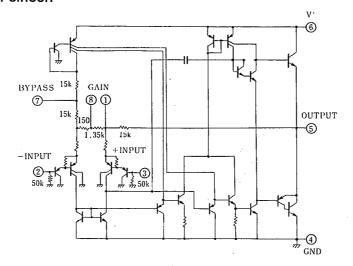
4. GND 5. OUTPUT

6. V+

7. BY PASS

8. GAIN

■ EQUIVALENT CIRCUIT



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	22	v
Power Dissipation	Po	(DIP-8) 700	mW
		(SIP-8) 800	mW
		(DMP-8) 300	mW
Input Voltage Range	V _{IN}	±0.4	V
Operating Temperature Range	Торг	-40~+85	r
Storage Temperature Range	T _{stg}	-40~+125	ర

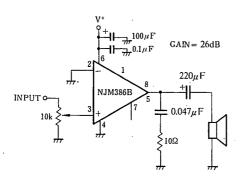
■ ELECTRICAL CHARACTERISTICS

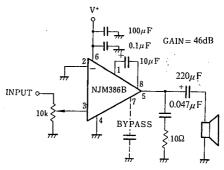
(Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V+		4		18	v
Operating Current	I _{CC}	$V^{+}=6V, V_{IN}=0$		5	8	mΑ
Output Power	Po	$V^{+}=6V, R_{L}=8\Omega, THD=10\%$	250	325	_	mW
		$V^{+}=9V$, $R_{L}=8\Omega$, THD=10% (note 2)	500	850	l —	mW
		$V^{+}=16V$, $R_{L}=32\Omega$, THD=10% (note 1)	700	1000	_	mW
Voltage Gain	Av	Vs=6V, f=1kHz	24	26	28	dB
		10μF from Pin 1 to 8	43	46	49	dB
Bandwidth	BW	V+=6V, Pins 1 and 8 Open	j	600	_	kHz
Total Harmonic Distortion	THD	$V^{+}=6V, R_{L}=8\Omega, P_{OUT}=125mV$	l —	0.1	—	%
		f=1kHz, Pins 1 and 8 Open				
Power supply Rejection Ratio	SVR	$V^{+}=6V$, $f=1$ kHz, $C_{BYPASS}=10\mu F$	-	50	_	dB
		Pins I and 8 Open			l	ŀ
Input Resistance	Rin		_	50	-	kΩ
Input Bias Current	l _B	V+=6V, Pins 2 and 3 Open	_	100	-	пA

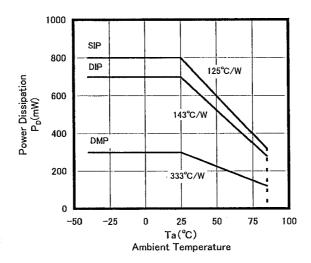
(note 1) NJM386BM: At on Board (note 2) NJM386BS: At on Board

■ TYPICAL APPLICATION





■ POWER DISSIPATION VS. AMBIENT TEMPERATURE



■ NOTICE WHEN APPLICATION

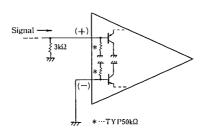
• Prevention of Oscillation

It is recommended to insert capacitors at around the supply source and the GND pins with the value of 0.1μ F and more than 100μ F which are featuring higher frequency efficiency.

When the speaker load condition, it is recommendable to insert the resisitor of 10Ω and the capacitor of $0.047\mu F$ between the output and the GND pins.

• How to use the Input Resistor $(\dot{T}YP. 50k\Omega)$

The input resistors have much deviation in value generally, so that it is recommended not to use them as the constant of the circuit. The countermeasure to be recommended is to apply the resistor of higher in value, which is so higher to be able to ignore the input deviation $(3k\Omega)$ approximately) in parallel application.



. Maintenance of Output Offset Voltage

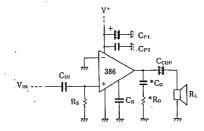
By making connection of both input pins with low value (below $10k\Omega$ approximately) to GND, the output offset voltage is automatically set in the medium range value of the supply source. However, the DC Gain of NJM386 is approximately at 20 times in value, so that when keeping one side input pin open, and the other side to GND on DC condition. The voltage drop caused by input resistor \times input bias current, that is, (input resistor \times input bias current) \times 20 times voltage is to be sheared, which in the result, no distortion output Oscillation range shall be decreeased.

In regard to dealing with the input pin, it is recommendable to put the input pin into the GND at first, and the other side of signal input pin, to be connected into GND with the resistor of less than about $10k\Omega$ on DC condition.

• The Application Purpose and Recommended Value of the External Parts.

EXTERNAL PARTS	APPLICATION PURPOSE	RECOMMENED VALUE	REMARKS
Rs	Current like noise reduction Voo stabilization	Below 10kΩ	The noise becomes high when the input pin opend.
C _{IN}	V _{OQ} stabilization	lμF	It is not required in case when there is no DC offset in the input signal.
CPI	V ⁺ stabilization	° ≧ Ccup.	It can be decreased in value when the output impedance source is low.
C_{P2}	Oscitallation prevention	0.1µF	Insert near around the supply source and GND pins.
Cv	Ripple rejection to Voby way of V+	47μF	It is not required when the V+ is stabilized.
*Co	Oscillation preventon	0.047μF	To be decided in value according to load condition.
*Ro	Oscillation prevention	10Ω	To be decided in value according to load condition.
CCUP	Output DC decoupling	470µF when	Low band cutoff frequency (fL) shall be decided by CCUP RL
		$R_L = 4\Omega$	When C _{CUP} is less in value, f _L is to be increassed.
		220μF when	
	!	$R_L = 8\Omega$	

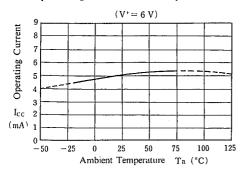
NJM386B Recommended Circuit



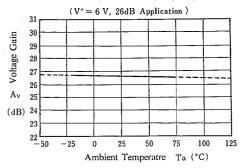
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■ TYPICAL CHARACTERISTICS

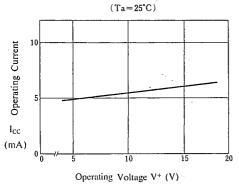
Operating Current vs. Temperature



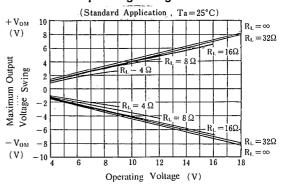
Voltage Gain vs. Temperature



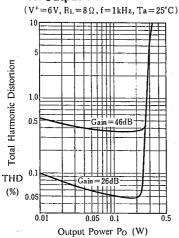
Operating Current vs. Operating Voltage



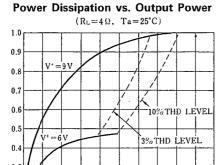
Maximum Output Voltage Swing vs. Operating Voltage

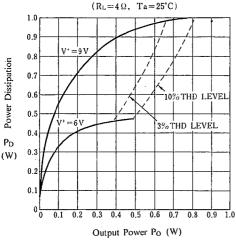


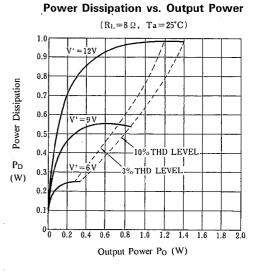
Total Harmonic Distortion vs. Output Power

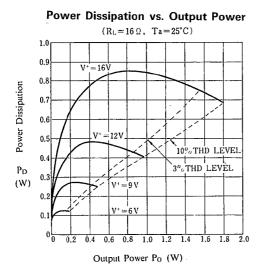


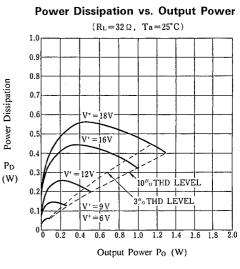
TYPICAL CHARACTERISTICS











NJM386B

MEMO

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