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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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January 2005

FAIRCHILD SEMICONDUCTOR®

BDW94/C PNP Epitaxial Silicon Transistor

Power Linear and Switching Application

- Power Darlington TR
- Complement to BDW93 and BDW93C Respectively



1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage		
	: BDW94	-45	V
	: BDW94C	-100	V
V _{CEO}	Collector-Emitter Voltage		
	: BDW94	-45	V
	: BDW94C	-100	V
I _C	Collector Current (DC)	-12	A
I _{CP}	Collector Current (Pulse) *	-15	A
I _B	Base Current	-0.2	A
P _C	Collector Dissipation ($T_C = 25^{\circ}C$)	80	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-65 ~ 150	٥°

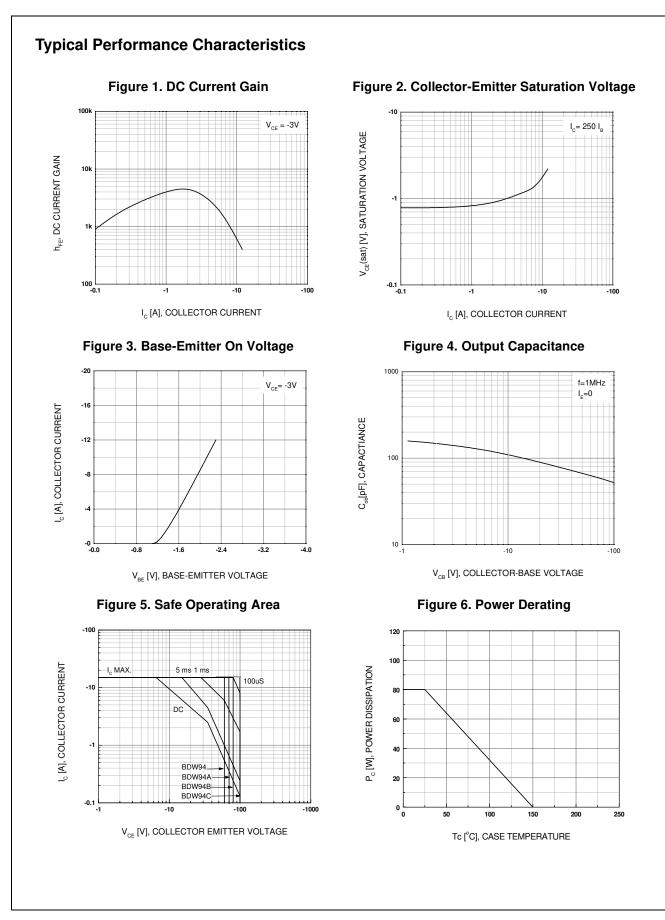
BDW94/C PNP Epitaxial Silicon Transistor

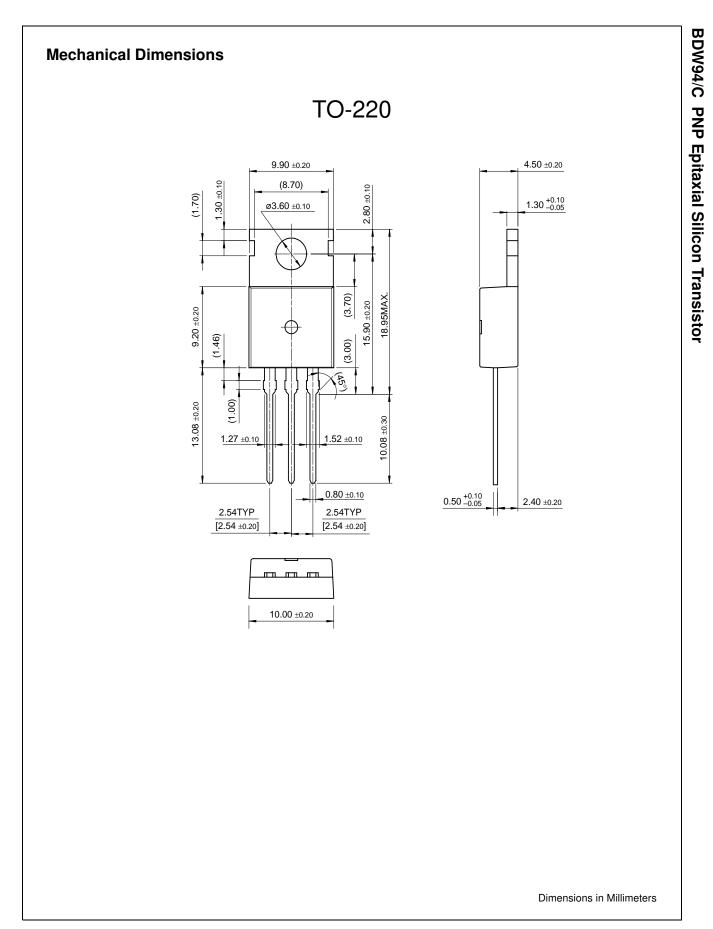
Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
V _{CEO(sus)}	Collector-Emitter Sustaining Voltage : BDW94 : BDW94C	I _C = -100mA, I _B = 0	-45 -100			V V
I _{CBO}	Collector Cut-off Current : BDW94 : BDW94C	$V_{CB} = -45V, I_E = 0$ $V_{CB} = -100V, I_E = 0$			-100 -100	μΑ μΑ
I _{CEO}	Collector Cut-off Current : BDW94 : BDW94C	$V_{EB} = -45V, I_B = 0$ $V_{CE} = -100V, I_B = 0$			-1 -1	mA mA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			-2	mA
h _{FE}	DC Current Gain *	$V_{CE} = -3V, I_C = -3A$ $V_{CE} = -3V, I_C = -5A$ $V_{CE} = -3V, I_C = -10A$	1000 750 100		20000	
V _{CE(sat)}	Collector-Emitter Saturation Voltage *	$I_{C} = -5A, I_{B} = -20mA$ $I_{C} = -10A, I_{B} = -100mA$			-2 -3	V V
V _{BE(sat)}	Base-Emitter Saturation Voltage *	$I_{C} = -5A, I_{B} = -20mA$ $I_{C} = -10A, I_{B} = -100mA$			-2.5 -4	V V
V _F	Parallel Diode Forward Voltage *	Diode Forward Voltage * $I_F = -5A$ -1.3 -2 V $I_F = -10A$ -1.8 -4 V		•		

Electrical Characteristics T_c = 25°C unless otherwise noted

* Pulse Test: PW = $300\mu s$, Duty Cycle = 1.5% Pulsed







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Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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Rev. 115

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